A TRANSLATOR WRITING SYSTEM FOR MICROCOMPUTER HIGH-LEVEL LANGUAGES AND ASSEMBLERS

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NASA LaRC uses many dedicated microprocessors in aerospace research. Few software tools are available for these machines, and in particular, very few have any form of high-level language facility. Since the Langley environment involves considerable experimentation, a great deal of software is experimental and may change frequently. It has to be prepared relatively quickly and at low cost.

In order to implement high-level languages whenever possible, a Translator Writing System of advanced design has been developed. It is intended for routine production use by many programmers working on different projects. As well as a fairly conventional parser generator, it includes a system for the rapid generation of table driven code generators. This code generation system is the result of research performed at the College of William and Mary under NASA sponsorship. The parser generator was developed from a prototype version written at the College of William and Mary.

The Translator Writing System includes various tools for the management of the source text of a compiler under construction. In addition, it supplies various "default" source code sections so that its output is always compilable and executable. The system thereby encourages iterative enhancement as a development methodology by ensuring an executable program from the earliest stages of a compiler development project.

This presentation will describe the Translator Writing System and some of its applications. These include the PASCAL/48 compiler, three assemblers, and two compilers for a subset of HAL/S. PASCAL/48 is a Pascal-like language for the Intel-8748 microcomputer. The assemblers which have been built are for assembly language subsets for the Intel-8080, the Motorola M68000, and the NSSC-II. The HAL/S subset was implemented for the Intel-8080 and the GE 703. Detailed measurements of the use of the system to build the code generators for the HAL/S compilers will be given.

*Work performed under NASA contract numbers NAS1-14900 and NAS1-16078. **Work performed under NASA grant number NSG-1435.

THE PROBLEM

NEED HIGH-LEVEL LANGUAGES, HENCE COMPILERS

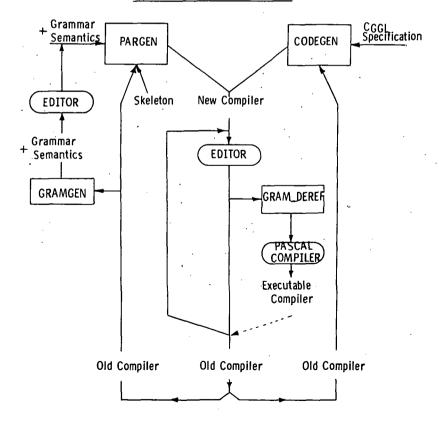
NEED ASSEMBLERS

ONE SOLUTION IS A TWS

TWS CRITERIA

- ENCOURAGE ITERATIVE ENHANCEMENT
 - EARLIEST POSSIBLE EXECUTION
 - TEXT MANAGEMENT TO RELIEVE TEDIUM
- FLEXIBILITY IN ITS USE
- TRANSPORTABLE IMPLEMENTATION

TRANSLATOR WRITING SYSTEM



USE OF TWS

1.	IF PARSER NEEDED, RUN PARGEN, EXECUTE RESULTING COMPILER TO TEST.						
2.	CHANGE GRAMMAR AS NECESSARY, RERUN PARGEN.						
3.	ADD SEMANTICS USING EDITOR.						
4.	RECOVER GRAMMAR AND SEMANTICS WITH GRAMGEN IF NECESSARY TO RERUN PARGEN.						
5.	IF CODE GENERATION NEEDED, PREPARE CGGL SPECIFICATION AND RUN CODGEN						
6.	MODIFY CGGL AS NEEDED.						
7.	ITERATE THROUGH ABOVE STEPS ADDING LANGUAGE FEATURES AS DESIRED.						
	PARGEN						
`							
	• INPUTS						

- GRAMMAR IN STANDARD BNF
- SEMANTICS IN PASCAL
- SKELETON OR OLD COMPILER
- OUTPUT IS AN EXECUTABLE COMPILER INCLUDING
 - SCANNER
 - LALR (1) PARSER
 - SEMANTICS ROUTINE
- TEXT MANAGER PRESERVES PROGRAMMER'S CONTRIBUTION TO COMPILER E. G., SYMBOL TABLE ROUTINES

CODGEN

- INPUTS
 - CGGL SPECIFICATION
 - SKELETON OR OLD COMPILER
- OUTPUT IS AN EXECUTABLE COMPILER INCLUDING A CODE GENERATOR.
- CGGL IS A NON-PROCEDURAL LANGUAGE FOR DESCRIBING THE CODE-GENERATION PROCESS.
- TEXT MANAGER PRESERVES PROGRAMMER'S CONTRIBUTION TO COMPILER E.G., MACHINE LANGUAGE FORMATTER.

PA'SCAL/48

- INTEL-8748
 - MICROCOMPUTER
 - 8-BIT CPU
 - 64 WORD RAM
 - 1024 WORD ROM
 - 27 I/O LINES
- PASCAL/48
 - PASCAL DERIVATIVE FOR 8748
 - EXTENSIONS TO ALLOW CONTROL OVER GENERATED CODE
 - RESTRICTIONS TO PROHIBIT INEFFICIENT FEATURES
 - COMPILER AVAILABLE ON CDC CYBERS

ASSEMBLERS

CUSTOMIZED SKELETON FOR ASSEMBLERS

- TWO PASSES
- STANDARD LISTING BY DEFAULT
- FLEXIBLE INPUT FORMAT CONVENTIONS
- HANDLES MACROS WITHOUT PARAMETERS

COMPARED TO META-ASSEMBLER, ASSEMBLER BUILT FOR NSSC-II

- WAS PRODUCED MORE QUICKLY
- EXECUTES 5 TIMES FASTER
- USES ONE FOURTH THE SPACE

EXAMPLE PASCAL/48 PROGRAM

		RESEARCH CENTER VERSION 1.0.0		08.51.12.	PAGE 1 CSC/NASA
1 2	PROGRAM FO	DR_YOU;			
3	VAR	I[2] : INTEGER; A[16_300, RDM] :	ARRAY [100] OF 3	INTEGER;	
6	VALUE	· A = (99.0F 0; 1)	;		
8		JRE GET_INPUT;			
10 11	BEGIN REPE	FAT			
12 13 14 15	UNTI	L PORT1 BIT 3 (* GET_INPUT *)			
	BEGIN (+	PROGRAM FOR_YOU +)		
17		- 100 DOWNTO 1 DO			
18	BEGI				
19 20 21	PORT	_INPUT; [1 = PORT1 AND 2_ [2 = A[I] + PORT1			
22	END	(* FOR I := 100 PROGRAM FOR_YOU +)		{ ; ;)	

GENERATED CODE FOR EXAMPLE PROGRAM

	JMP	L009	
	NOP	•	
L0031	JMP	L003	
	NDP	-	
	NOP		
L007:	JMP	L007	
L0091	CLR	A 1	
	NOV	P S W J A	•
	JMP	L012	
L00D:	IN	A,P1	; LINE 9
	CPL	A	; LINE 13
	JB3	LOOD	; LINE 13
	RET		JUINE 13
L012:	MOV	RZ;#99	ILINE 14
L014:	CALL	LOOD	; LINE 18
	ANL	P1,#227	; LINE 21
	IN	A, P1	; LINE 22
	MOV	R1,A	; LINE 22
	MOV	A, PZ	; LINE 22
	MOVP3	A , DA	J LINE 22
	ADD	A,R1	; LINE 22
	XRL	A,R2	J LINE 22
	OUTL	P2,A	; LINE 22
	DJNZ	R2,L014	J LINE 22

SEPARATE CODE GENERATION USING CGGL

LANGUAGE: HAL/S

INTERMEDIATE CODE LANGUAGE: HALMAT

- ---- 178 OPERATORS TOTAL
- ----- 30 OPERATORS IMPLEMENTED
- ----- 25 GENERATE CODE
- ----- BASICALLY AN INTEGER SUBSET WITH SIMPLE CONTROL STRUCTURES

CODE GENERATORS

- ---- ONE PASS
- ----- NO PRE-OPTIMIZATION PASS
- ----- NO PEEPHOLE OPTIMIZATION
 - ---- INTEL 8080, GE 703

Intel 8080

---- 8 BIT MACHINE

NO INDEX REGISTER

- 1, 2, 3 BYTE INSTRUCTIONS

----- HARDWARE STACK

- ----- ONLY INTEGER ADD, SUBTRACT
- ---- 16 BIT ADDRESSES

GE 703

----- 16 BIT MACHINE

INDEX REGISTER

- ONE WORD INSTRUCTIONS
- ----- NO HARDWARE STACK
- ----- INTEGER ADD, SUBTRACT, MULTIPLY, DIVIDE
- ---- ONLY ADDRESS CURRENT PAGE, PAGE ZERO
- PAGE: 256 WORDS

703 CODE GENERATOR

TASK	TIME (DAYS)		
READING MANUAL	.5		
CGGL PROGRAM	1.5		
WRITING PASCAL ROUTINES	1.5		
DEBUGGING	1.0		

4.5 DAYS

Notes:

1. ALL PROGRAMS WERE CODED AND KEYED BY NOONAN.

2. Some of debugging time was used in cleanup.

 ONE DEBUGGING RUN WAS USED TO FIX A BUG INTRODUCED BY CLEANUP.

4. A TOTAL OF 6 RUNS (EXECUTION) WERE USED.

5. ONE CGGL BUG,

703 IMPLEMENTATION

Source of Code	No. Procedures	% Lines	% Instr. <u>Storage</u>
8080 Imple.	46	58%	58%
MODIFIED 8080	4	87	6%
Noonan	9	10%	10%
CGGL	1	24%	26%

NOTES:

- 1. CGGL PROGRAM: 292 LINES
- 2. PASCAL PROGRAM: 890 LINES
- 3. For an earlier non-table-driven implementation, CGGL ACCOUNTED FOR 83% OF LINES AND 77% OF STORAGE.