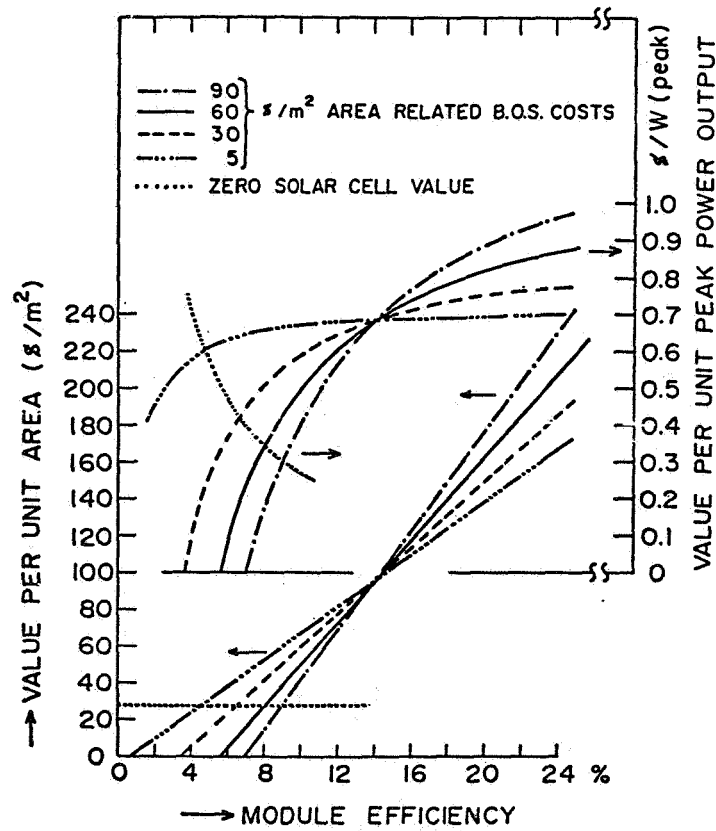


MATERIAL REQUIREMENTS FOR HIGH-EFFICIENCY SILICON SOLAR CELLS

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Primary Causes of Losses

1. LIGHT GENERATED CURRENT:
 - A. OPTICAL SURFACE PROPERTIES (REFLECTION)
 - B. CONTACT COVERAGE
 - C. INCOMPLETE ABSORPTION (THICKNESS)
 - D. RECOMBINATION OUTSIDE DEPLETION REGION (BULK AND SURFACE, INCLUDING CONTACTS)
 - E. ("DEAD LAYERS")

2. OPEN CIRCUIT VOLTAGE:
 - A. RECOMBINATION OUTSIDE DEPLETION REGION (BULK AND SURFACE, INCLUDING CONTACTS)
 - B. BANDGAP NARROWING
 - C. "CURRENT LEAKAGE"

3. FILL FACTOR:
 - A. } SAME AS OPEN CIRCUIT VOLTAGE
 - B. }
 - C. }
 - D. RECOMBINATION IN DEPLETION REGION
 - E. SERIES RESISTANCE

PRIMARY CAUSES OF LOSSES	SYMBOL	DESIGN PARAM	1970 COML	VIOLET	"BLACK	1978	1984 EXPER'L CELLS			GOALS	
			CELL	CELL*	CELL*	SPACE CELL	SPIRE	WESTINGH	M.A.GREEN	20%	22.6%
		BASE: WIDTH T_{np} T_{np}^+	? $3 \mu s$ -	$300 \mu m$? -	$300 \mu m$? -	$300 \mu m(A)$? ?	$380 \mu m$ $\sim 40 \mu m$?	$375 \mu m$ $\sim 23 \mu m$ ($0.1 \mu s$)	$280 \mu m$ $\sim 25 \mu m$ ($0.1 \mu s$)	$200 \mu m$ $95 \mu s$ $0.26 \mu s$	$200 \mu m$ $930 \mu s$ $2.6 \mu s$
		FRONT: WIDTH $T_{p,n}$ S TREATM.	$0.4 \mu m$? ? SIO	$\sim 0.15 \mu m$? ? Te ₂ O ₃ ⁺ GLASS	$\sim 0.2 \mu m$? ? TEXT'D + Te ₂ O ₃ + GLASS	$\sim 0.2 \mu m$? ? TEXT'D + Te ₂ O ₃	$\sim 0.2 \mu m$? ? TEXT'D + Te ₂ O ₃	$\sim 0.3 \mu m$ $\sim 10^4 \text{ cm}^{-2}$ $\sim 10^4 \text{ cm}^{-2}$ TiO ₂ /SiO ₂	$\sim 0.3 \mu m$ $1ns - 15 \mu s$ 10^3 cm^{-2} ZnS/ Mg F ₂	$2 \mu m$ $0.1 \mu s$ 10^3 cm^{-2} DUAL AR	$2 \mu m$ $10 \mu s$ 10^3 cm^{-2} DUAL AR
1. LIGHT GENERATED CURRENT:											
		FUNDAMENTAL LIMIT:	44 mA cm^{-2}								
A. OPTICAL SURFACE PROPERTIES (REFLECTION)	(1-R)		0.905	0.90	0.97	0.96	0.975	0.966	0.954	0.97	0.97
B. CONTACT COVERAGE	S		0.96	0.95	0.95	0.96	0.965	0.97(A)	0.948	0.966	0.966
C. INCOMPLETE ABSORPTION (THICKNESS)				0.95	0.96	0.96(A)	0.956	0.956	0.973		
D. RECOMBINATION OUTSIDE DEPLETION REGION (BULK AND SURFACE, INCLUDING CONTACTS)	η_{coll}		0.72	0.95	0.94	0.94	0.91	0.915	0.93	0.92	0.95
E. ("DEAD LAYERS")	(1-A)			1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0
OVERALL COLLECTION EFFICIENCY:	γ		0.63	0.77	0.84	0.84	0.82	0.82	0.82	0.86	0.89
LIGHT GENERATED CURRENT (AMI)	$j_{L,ld}$		28.1	34.0	37.1	37.0	36.2	36.0	36.0	37.9	39.2
2. OPEN CIRCUIT VOLTAGE:											
		FUNDAMENTAL LIMIT:	$0.836 \text{ V } (j_0 = 4.2 \cdot 10^{-16} \text{ A cm}^{-2})$								
A. RECOMBINATION OUTSIDE DEPLETION REGION (BULK AND SURFACE, INCLUDING CONTACTS)	$(VF) - (VF)_{techn}$		0.522	0.528	0.533	0.555	0.565	0.57	0.59	0.60	0.65
B. BANDGAP NARROWING	$(VF)_{fund}$		1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0
C. "CURRENT LEAKAGE"	(R_{sh})		1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0
OPEN CIRCUIT VOLTAGE:	$V_{oc} = (VF) - E_g$	(V)	0.574	0.581	0.586	0.610	0.622	0.627	0.653	0.661	0.715
3. FILL FACTOR:											
		FUNDAMENTAL LIMIT:	0.865								
A. } SAME AS OPEN CIRCUIT VOLTAGE			0.82	0.823	0.823	0.824	0.83	0.833	0.839	0.84	0.85
B. }			1.0(A)	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0
C. }			0.91	0.97	0.97	0.97	0.985	0.98	0.982	0.97	0.97
D. RECOMBINATION IN DEPLETION REGION	$(CF)_{add't}$		0.96	0.985	0.984	0.98(A)	0.98	0.98	0.984	0.98	0.98
E. SERIES RESISTANCE	(R_s)										
FILL FACTOR	(FF)		0.716	0.78	0.78	0.78	0.801	0.800	0.811	0.80	0.81
RESULTING CONVERSION EFFICIENCY	η		11.6	15.4	17.0	17.6	18.1	18.1	19.1	0.200	0.226

(A) - ASSUMED

The Recent Approach

- a. THOROUGH DEVICE ANALYSIS COUPLED WITH MODELING:
 - o TO DETERMINE ALL LOSS CONTRIBUTIONS
 - o TO IDENTIFY POSSIBILITIES FOR IMPROVED DEVICE DESIGN.

- b. GLOBAL DESIGN VIEW OF DEVICE:
 - o OPTIMIZED CONTACT DESIGN
 - o DUAL AR OR TEXTURED FRONT SURFACE
 - o FRONT SURFACE PASSIVATION (AT LEAST PARTIAL)
 - o BSF AND/OR BSR DESIGN (LIMITED EFFECT)
 - o SELECTION OF LOW RESISTIVITY FZ Si
 - o PROCESSING TO MAINTAIN HIGHER FRACTION OF ORIGINAL L_b
 - o OPTIMIZATION OF EMITTER IMPURITY CONCENTRATION FOR
PRESENT DESIGN

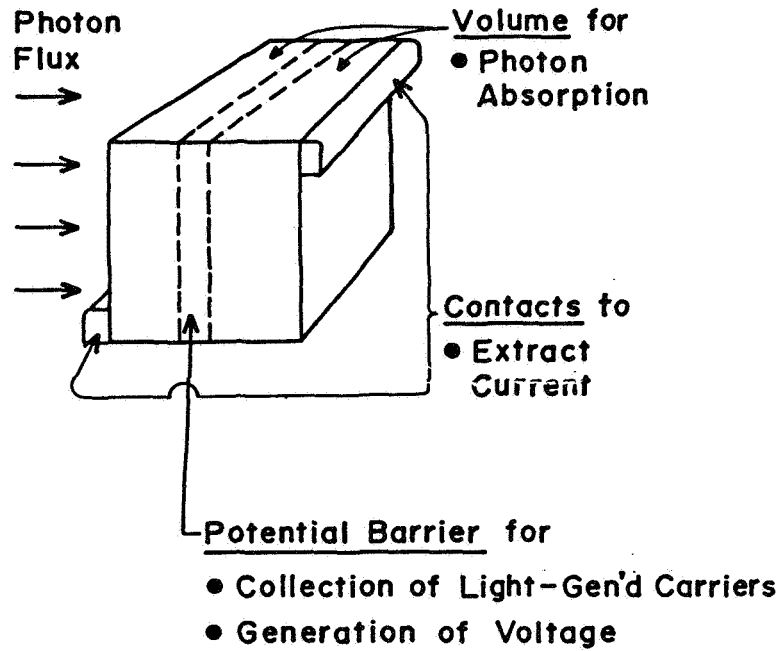
IN SUMMARY:

SQUEEZE A LITTLE MORE PERFORMANCE OUT,
WHEREVER CURRENT TECHNOLOGY PERMITS.

Status of Si Solar-Cell Technology

- TECHNOLOGY IS AVAILABLE TO REDUCE THE CONTRIBUTION OF EACH SECONDARY LOSS MECHANISM (REFLECTION, CONTACT SHADING, SERIES RESISTANCE, ETC.) TO THE MAXIMALLY 2-3% LEVEL.
- INTERNAL COLLECTION EFFICIENCY IS GENERALLY >90%; "SATURATES" WITH FURTHER REDUCED RECOMBINATION.
- OPEN CIRCUIT VOLTAGE CONTINUES TO SUBSTANTIALLY INCREASE WITH DECREASING MINORITY CARRIER RECOMBINATION, UP TO BASIC RECOMBINATION LIMIT (RADIATIVE AND AUGER).
- CURVE FACTOR (FUNDAMENTAL PART OF FILL FACTOR) CAN INCREASE (WITH V_{oc}) BY A FEW PERCENT.

The Basic Solar Cell

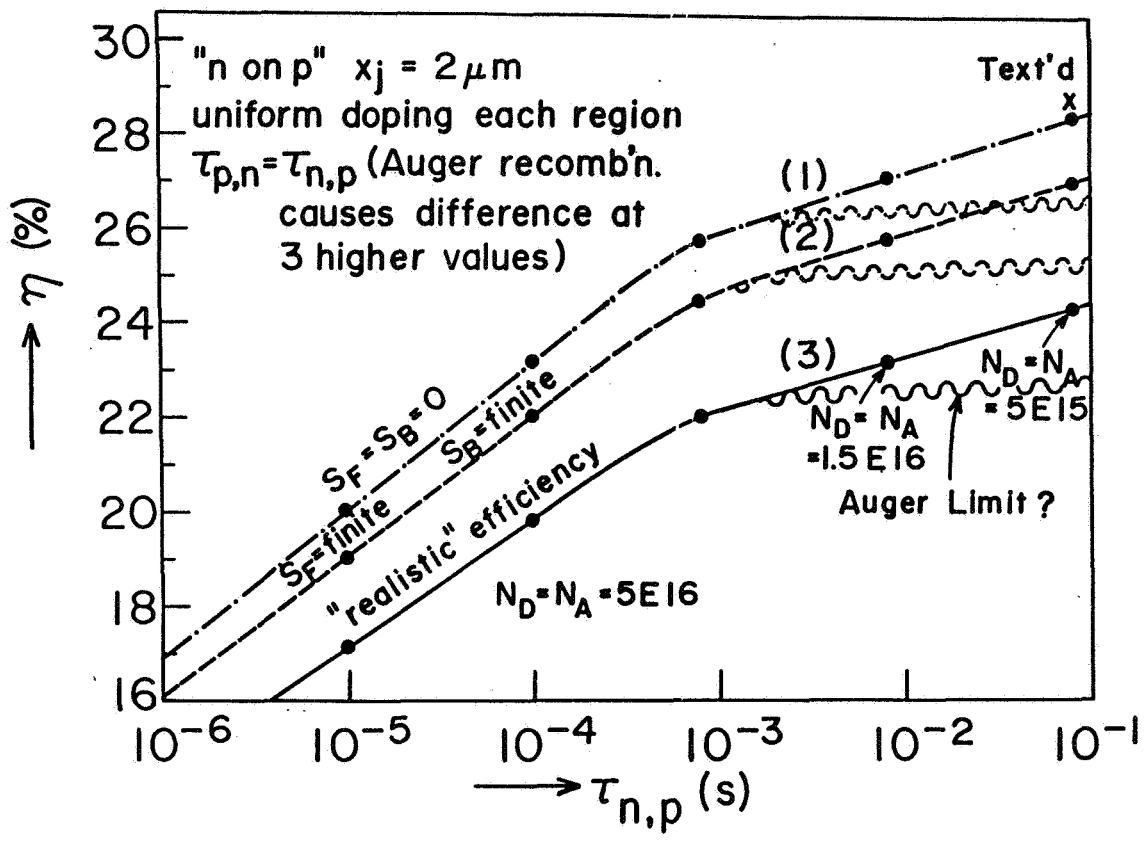


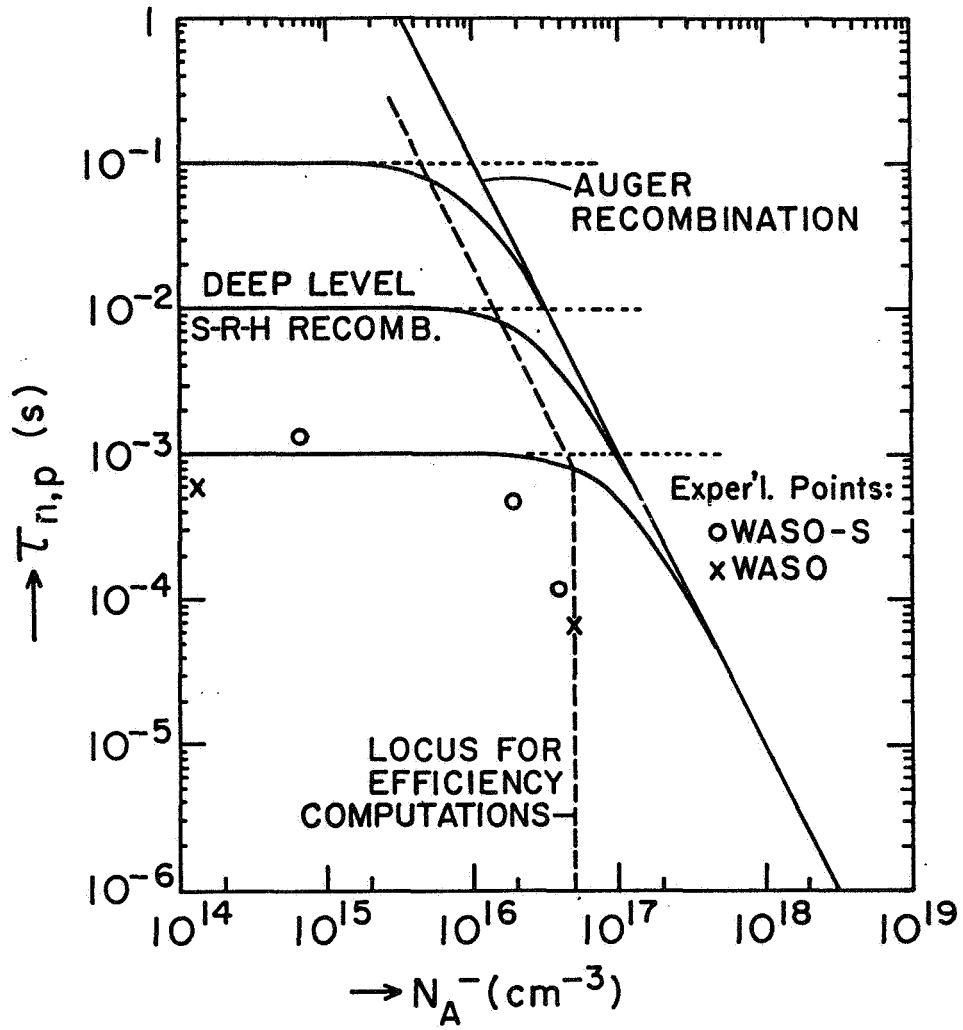
Everything Else

(AR coatings, Hi/Lo Junctions, extra Layers, etc)

to overcome

- Secondary Effects caused by material, process, or design limitations





The Three Principal Paths to Reduced Recombination

DECREASE

1. DENSITY OF RECOMBINATION CENTERS

- IN BULK N_t [cm^{-3}] \rightarrow HIGHER τ
- AT SURFACES $N_{s,t}$ [cm^{-2}] \rightarrow LOWER s

2. VOLUME OR AREA CONTAINING RECOMBINATION CENTERS:

- "THIN" LAYERS
- "DOT CONTACTS"

3. DENSITY OF EXCESS MINORITY CARRIERS

- FAST REMOVAL TO OUTSIDE
 - "SHIELDING" WITH POTENTIAL STEPS
 - "ISOLATING" FROM HIGHER RECOMBINATION RATE
 - HIGH DOPANT CONCENTRATION
- } FOR η_{coll} }
} FOR V_{oc}

High Dopant Concentration

PRINCIPLE:

$$j_d = q n_p \cdot \frac{L}{\tau} = q n_{p,0} e^{\frac{qV}{kT}} \cdot \frac{L}{\tau};$$

$$V = \text{HIGH IF } n_{p,0} = \text{SMALL: } n_{p,0} = \frac{n_i^2}{P_{p,0}}$$

LIMITS:

- HEAVY DOPING EFFECTS.

Is Heavy Doping Needed?

ITS PERFORMANCE-INCREASING APPLICATIONS:

- o REDUCE SHEET RESISTANCE
- o OBTAIN LARGER HIGH/LOW JUNCTION POTENTIAL STEP, OR HIGHER DRIFT FIELD.

ITS PERFORMANCE DECREASING ATTRIBUTES:

- o AUGER RECOMBINATION
- o BAND-GAP NARROWING.

Effective Bulk Recombination Mechanisms

INTRINSIC (INTERBAND) RECOMBINATION:

RADIATIVE

AUGER?

||| ULTIMATELY
LIMITS
EFFICIENCY

EXTRINSIC (BAND-TO-BOUND STATE) RECOMBINATION:

THERMAL (PHONON ASSISTED) SRH
(AUGER?)

EXTRINSIC RECOMBINATION CAN BE DECREASED BY REDUCING THE NUMBER OF BOUND STATES (RECOMBINATION CENTERS, "DEFECTS").

Knowledge of Defects

- a. ARE SOME DEFECTS "INTRINSIC"?
- (NEUTRAL DEFECT WITH ACTIVATION ENERGY E_a AT PROCESS TEMPERATURE "FROZEN IN." IONIZED FRACTION AT DEVICE OPERATION TEMPERATURE FORMS RECOMBINATION CENTER, PARTICULARLY IN N-TYPE).
- b. TRULY EXTRINSIC (PROCESS-INDUCED) DEFECTS
- o IMPURITIES (O, C, Au, Ti, Mo, Fe, ...)
 - BIG PROGRESS MADE IN DETECTING PRESENCE, DETERMINING CONCENTRATION. OPEN QUESTION OFTEN IS: INTERSTITIAL, SUBSTITUTIONAL, COMPLEXED, OR PRECIPITATED?
 - O: PRIMARY SOURCE: CRUCIBLE IN Cz PROCESS. TECHNIQUES KNOWN TO REDUCE O-CONTENT TO 0.1 OF STANDARD LARGE-CRUCIBLE Cz PROCESS. O-CONTENT INCREASES WITH C- OR B-CONTENT.
 - o CRYSTAL GROWTH DEFECTS.
 - BIG PROGRESS MADE IN DETECTION, IDENTIFYING CRYSTAL GROWTH DEFECTS.
 - STRONGLY CONNECTED WITH THE CRYSTAL GROWTH TECHNOLOGY APPLIED; TECHNOLOGY APPLIED SEEMS PRIMARILY DETERMINED BY THROUGHPUT, PRICE, AND WHAT THE MAJORITY OF USERS ARE WILLING TO ACCEPT.

Reduce Volume Recombination Center Density

- ORIGINAL MATERIAL PROCESSING:
 - FEWER IMPURITIES
 - ROLES OF OXYGEN, CARBON?
 - FEWER CRYSTAL DEFECTS (THERMAL ENVIRONMENT IN X-TAL GROWTH?)
 - ROLES OF DEFECT COMPLEXES
- DEVICE PROCESSING:
 - NO NEW IMPURITY INTRODUCTION
 - REMOVE EXISTING DEFECTS (GETTERING)
 - AVOID TRANSFORMATION OF DEFECTS TO RECOMBINATION CENTERS (EFFECTS OF THERMAL PROCESSES?)
 - FOSTER TRANSFORMATION OF RECOMBINATION CENTERS TO HARMLESS DEFECTS (PASSIVATION; CHANGES OF COMPLEXES?; ROLE OF HYDROGEN?)

Steps Toward Reduced Number of Recombination Centers

1. IDENTIFY "DEFECT(S)" WHICH FORM RECOMBINATION CENTER(S) - BROAD RANGE OF DEFECTS AND OF ENERGY LEVELS IDENTIFIED
- INTERCONNECTION AND RELATIONSHIP TO RECOMBINATION CENTERS MADE IN ONLY A FEW CASES.
2. IDENTIFY SOURCE(S) OF DEFECT(S) - USUALLY NOT KNOWN.
3. FIND WAYS FOR ELIMINATING SOURCE(S) OF DEFECT(S) - STILL MOSTLY "BLACK ART."
4. PASSIVATE EXISTING DEFECTS - LITTLE KNOWN. IS H⁺ THE BROAD SPECTRUM ANTIBIOTIC"?

Passivation with Hydrogen

- o IT CAN NEUTRALIZE RECOMBINATION CENTERS, APPARENTLY EVEN DEEP IN THE BULK, PARTICULARLY AT GRAIN BOUNDARIES.
- o HYDROGEN IMPLANTS PASSIVATE DANGLING BONDS, WHEREVER HYDROGEN IONS REACH THEM.
- o HYDROGEN IMPLANTS MAY POSSIBLY ALSO PASSIVATE DEEP LEVELS (IMPURITIES) in Si.
- o THE "IMPLANTATION" OF HYDROGEN IONS, EVEN AT LOW ENERGIES CAUSES SPATTER ETCHING, LATTICE DAMAGE (200 Å DEEP AT 400eV).
- o HYDROGEN CAUSES MORE LATTICE DAMAGE THAN ARGON, EVEN AMORPHIZES SURFACE LAYER, BUT FEWER DANGLING BONDS ("PASSIVATES ITS OWN DAMAGE")
- o WHETHER PASSIVATION DOMINATES OVER INTRODUCED DAMAGE DEPENDS ON IMPLANTATION ENERGY, PRIOR PROCESS HISTORY.
- o HYDROGEN IS ALSO KNOWN TO NEUTRALIZE B AS AN ACCEPTOR.

Low-Cost Crystalline Si Is Primarily:

- o "CAST" INGOT MATERIAL (SEMIX, SILSO, HEM, etc.)
- o NOT-SINGLE CRYSTALLINE RIBBON (EFG, LASS, etc.)

EFFORTS TO INCREASE GRAIN SIZE, REDUCE DEFECTS,
PASSIVATE REMAINING ONES, ALL SHOW PROGRESS.

BUT: IT SEEMS IMPOSSIBLE TO COMPLETELY PASSIVATE
ALL THE DEFECTS ASSOCIATED WITH GRAIN BOUNDARIES

ALSO: FASTER, LESS CONTROLLED GROWTH MAY ALWAYS RESULT
IN INCREASED NUMBERS OF IMPURITIES, CRYSTAL DEFECTS.

CONSEQUENTLY: THE ULTIMATELY ACHIEVABLE PERFORMANCE MARGIN
RELATIVE TO THAT OF SINGLE CRYSTAL DEVICES IS
NOT KNOWN.

- o WEB-DENDRITE RIBBON IS IN A CLASS BY ITSELF.
MAY HAVE POSSIBILITY, WITH INTERNAL GETTERING AT TWIN
PLANES, TO SURPASS THE QUALITY OF SINGLE CRYSTAL WAFERS.

IN ALL METHODS, THE CONTROL OF THE THERMAL ENVIRONMENT
DURING AND SHORTLY AFTER GROWTH APPEARS IMPORTANT.

The Device Engineer's Wish List to the Materials Engineer

1. SILICON OF LONG MINORITY CARRIER LIFETIME
(e.g., 0.2 Ω cm p-type with $\tau > 500 \mu\text{s}$)
2. SILICON OF REPEATEDLY UNIFORM LIFETIME
(not 50-1000 μs)
3. SILICON WHOSE LIFETIME DOES NOT DECREASE DURING NORMAL
DEVICE PROCESSING
(a repeatable, uniform increase is o.k.)
4. SILICON SHEET (WAFER) WHICH IS FLAT, AND STAYS FLAT
THROUGHOUT NORMAL DEVICE PROCESSING
5. SILICON WHICH UNIFORMLY HAS REASONABLE MECHANICAL STRENGTH
6. SILICON SHEET OF LOW COST
($< \$50/\text{m}^2$)

Final Discussion

- o FOR HIGHER EFFICIENCIES (AT LEAST $> 20\%$), BETTER SINGLE CRYSTAL Si IS NEEDED.
- o IT SHOULD BE POSSIBLE TO BRING Cz Si TO THE SAME LOW-RECOMBINATION LEVEL AS FZ Si.
- o HOW CAN DEVICES BE FABRICATED FROM THIS Si WITHOUT GREATLY INCREASING THE RECOMBINATION CENTER DENSITY?
- o ARE SPECIAL Si QUALITIES NEEDED TO PERMIT SUCH PROCESSING?
- o HOW CAN THE PROGRESS MADE IN MATERIAL SCIENCE BE TRANSLATED INTO BETTER PROCESSING METHODS?
- o IF SOLAR CELL FABRICATORS WOULD SPECIFY THE QUALITY OF Si THEY NEED, WOULD Si MANUFACTURERS DELIVER TO THESE SPECIFICATIONS?
- o DO SOLAR CELL FABRICATORS KNOW WHAT SPECIFICATIONS TO WRITE?

DISCUSSION

STORTI: How much of a sacrifice in efficiency and lifetime do you see by going down to very low values of resistivity?

WOLF: Calculations of intrinsic defect levels of recombination center densities indicate that the lifetime goes down with one over the impurity concentration. This means that you still would gain by going to a lower resistivity until you reach Auger recombination.

RAO: Regarding hydrogen passivation: are there data that verify if we are passivating the dangling bonds and/or the impurities?

CORBETT: No, it has not been verified. It does passivate sometimes, including the substitutional impurities, and sometimes it doesn't. We have speculations as to why it doesn't but we don't know for sure.

LANE: You've been talking about the defects and their effect on the cell. There is a lot of effort going on in industry today to improve the uniformity of the crystal, the uniformity of the distribution of defects, the uniformity of the impurities, striations, etc. If we get a perfectly uniform crystal and it's still full of impurities or defects, are we no farther ahead or is this effort on uniformity truly important?

WOLF: The device fabricator would like to see ingots all of nearly uniform lifetime from which to make a device.

SCHWUTTKE: If you want high-efficiency solar cells, you must have a uniform minority carrier lifetime over the whole device area. This means the minority carrier lifetime is not allowed to go below a certain limit. It is welcome to go over that limit if you average over the total area.

LESK: Regarding uniformity: I don't think that the material we get today is uniform either, but if making the material uniform drives the cost up too high, then we may still have to accept something that is not uniform but is still pretty good.

JASTRZEBSKI: How far away from the junction do you need a good lifetime?

WOLF: Silicon is an indirect bandgap semiconductor, so some of the photons of the solar spectrum penetrate up to a millimeter. To make devices with high voltage, we make the layers thinner than the diffusion lengths. The lifetime determines how thick we make the layers, therefore, a long lifetime would use a thicker wafer. For a lifetime of 100 μ s, a 10- μ m-thick wafer would be optimal, while a millisecond lifetime would require 300 to 500- μ m-thick wafers.