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OPTICAL DETECTORS FOR GaAs MMIC INTEGRATION: TECHNOLOGY ASSESSMENT

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ABSTRACT

Fiber optic links are being considered to transmit digital and analog signals in phased array antenna feed networks in space communications systems. The radiating elements in these arrays will be GaAs monolithic microwave integrated circuits (MMIC's) in numbers ranging from a few hundred to several thousand. If such optical interconnects are to be practical it appears essential that the associated components, including detectors, be monolithically integrated on the same chip as the microwave circuitry. In this paper the general issue of monolithic integration of microwave and optoelectronic components is addressed from the point of view of fabrication technology and compatibility. Particular attention is given to the fabrication technology of various types of GaAs optical detectors that are designed to operate at a wavelength of 830 nm.

1. INTRODUCTION

For many years attention has been given to a wide variety of system applications of optical fiber-based communications. These applications include telecommunications, internal and network-level computer interconnects, and microwave system interconnects.^{1,2} In each case there are advantages, ranging from speed improvements to reduction in cost and in space and power consumption, to be gained by appropriate monolithic integration of optical, optoelectronic, and electronic components. In an operational sense, however, perhaps the dominant factor in determining practical limits to the level of integration is compatibility of materials and processing technologies of the various components. This paper addresses the monolithic integration issue for the case of internal optical interconnection in microwave systems, with particular emphasis on optical detectors.

An underlying assumption in our discussion is that the system being considered here is, and will continue to be, basically a microwave system and that therefore if a choice is necessary, the dominant technology will be that which optimizes the performance of monolithic microwave integrated circuits (MMIC's), not that which optimizes the performance of the detectors. We have also assumed for purposes of this discussion that because of the short distances involved, optical interconnects in microwave systems will operate at a wavelength of 830 nm. In the sections to follow we will consider microwave system requirements that may generate a need for optical interconnects, briefly review current MMIC fabrication technology, and examine the various types of detectors and their fabrication technologies. Finally, we will consider some recent examples of optoelectronic integration of a type that may be appropriate to microwave systems.

2. MICROWAVE SYSTEM REQUIREMENTS

As microwave systems for spacecraft and aircraft become more compact their limiting size can be increasingly dominated by the hardware, e.g., coaxial cables and waveguides, required for transmitting signal and control information between circuit boards and between individual subsystems. As an example of this problem consider the output section of a communications system that includes a phased array antenna, as shown schematically in Fig. 1. Although the details of layout and the location of specific functions are a subject of discussion among system designers, in such a system the microwave carrier

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must be modulated by the signal information that is to be transmitted, and the modulated carrier must be delivered to each radiating element of the array with the correct relative phase and amplitude. Digitally controlled microwave phase shifter and variable gain amplifier MMIC's have been reported.³ As the figure suggests, both the signal information and the phase shifter and amplifier control signals as well as, perhaps, the modulated, phase shifted, amplified carrier are candidates for transmission by optical means. The former two are potentially wide bandwidth baseband signals while the latter is a narrow fractional bandwidth microwave frequency signal, so both the bandwidth and the operating frequency capabilities of an optical subsystem will be important.

Some considerations that must be made and which will affect the scale of integration of the MMIC's and the choice to use optical interconnects are:

- In a system such as that exemplified in Fig. 1, the microwave portions of the circuitry will be analog while the control portions will be digital. Since FET's for digital control and for analog microwave circuits are different, compromises may be necessary if integration is to be practical, independent of the optical interconnect issue.
- The scale of integration will depend upon material and processing compatibility, upon yield projections, and upon the system designer's requirements.
- If optical interconnects are to be used, detectors would act as the interface between the optical and the electrical subsystems.
- The overall system is primarily microwave, so the optical detectors and their associated circuitry must be material- and process-compatible with MMIC fabrication technology.

3. CURRENT MMIC FABRICATION TECHNOLOGY

To enable us to address the material and process compatibility issue let us first examine the current and probable future MMIC technology, as shown schematically in Fig. 2. At present perhaps the most commonly used technology is ion-implanted MESFET, directly on semi-insulating GaAs substrates, as shown in Fig. 2 (a).⁴ This process is well-developed, is economical to use, and gives reasonable yields, but it is limited in speed and noise characteristics by the physical properties of the wafer on which it is based. A second and somewhat similar technology bases the devices on a high purity GaAs layer that is grown epitaxially on a semi-insulating substrate, as shown in Fig. 2 (b).⁵ The conducting layers may be produced by ion implantation into the grown layer or by use of a second, doped, epitaxial layer grown on the high purity layer. This material has higher device speed capabilities than the direct wafer implant, but its speed is still limited by scattering from the dopant impurities in the conducting layer. In both of these cases the device of choice for low noise, high power linear microwave circuits is the high-pinchoff-voltage D-MESFET, which is normally on, while for low power digital circuits the E-MESFET, which is normally off, or a low pinchoff voltage D-MESFET is typically chosen.⁶ Similar, but far from identical processes are used to produce the two devices, so the usual choice is an intermediate pinchoff D-MESFET.

The structures shown in Figs. 2 (c) and (d) have been designed to overcome the scattering (by doping impurities) that limits device speed, and their use in MMIC's and high speed digital IC's is a topic of current research.^{7,8} In each material the conducting electrons originate from the n-type impurities in the n-AlGaAs layer, but because of the band structure of the layered material conduction occurs in the undoped GaAs layer as a two-dimensional electron gas (2 DEG), resulting in higher electron mobilities and saturation velocities than can be obtained in doped material. It has been suggested that with a minor

change in the heterostructure the same material can be used for both digital (E-MODFET) and analog devices (D-MODFET), with only slight differences in processing.⁷

4. DETECTOR TYPES AND FABRICATION TECHNOLOGIES

Planar, two-terminal optical detectors that are appropriate for consideration in MMIC interconnects can be classified into three types, namely back-to-back diodes, single diodes, and photoconductors, as shown schematically in Fig. 3. Fig. 3 (a) shows a MSM (metal-semiconductor-metal) detector as a representative of the back-to-back diode. Both contacts of the MSM device are Schottky-type blocking contacts, with the barrier height, and therefore the dark current, being determined by the choice of contact metal. The MSM detector can be fabricated directly on a semi insulating wafer or on a high purity epitaxial layer, with no ion implantation requirement. Fig. 3 (b) represents a p-i-n (p-type, intrinsic, n-type) photodiode that must be produced by ion implantation. The contacts on this device are ohmic to the p-type and n-type materials. The MSM and p-i-n detectors have comparable response speeds and noise characteristics, and each produces one electron in an external circuit for each photon absorbed. The third detector type is the photoconductive detector, as shown schematically in Fig. 3 (c). This detector, like the MSM device, can be produced by liftoff and etching, although those produced on high purity material layers may be ion implanted. Both contacts are ohmic, non-blocking contacts and the device exhibits gain because of the combination of injecting contacts and different transit times for electrons and holes. In general photoconductive detectors have a short rise time, but the fall time in their response to a short pulse is long, with the result that their 3 dB frequency is lower than that of the other two types.

The operating characteristics of several representative and easily integrated detectors of each type and material, as gathered from the literature, are given in Table I.9-15. Since the operating wavelength has a significant effect on detector speed, a particular effort was made to select devices for which measurements have been made at the proposed operating wavelength of 830 nm even though faster GaAs-based detectors have been reported in the literature. The effect of wavelength on speed is shown schematically, for two different wavelengths, in Fig. 4. In each case the absorbing material is GaAs, which has a band gap of 1.42 eV. The e^{-1} penetration depth of 830 nm light is $\approx 1 \mu\text{m}$ while at 600 nm it is $\approx 0.2 \mu\text{m}$. Thus at the longer wavelength significant numbers of photoelectrons are generated throughout the bulk of the detector, and they experience a depth-dependent electric field. At the shorter wavelength most of the photoelectrons are generated at the surface, where they experience a higher and relatively uniform electric field. The result is that a detector that has a bandwidth of, say, 100 GHz at 600 nm may have only a 25 GHz bandwidth at 830 nm.

The table shows that response speeds up to about 7 GHz are reasonably obtainable and that at least 20 GHz can be achieved with MESFET technology. The only report, by the authors of this paper, describing the response at 830 nm of detectors fabricated on HEMT material like that used for microwave devices is for photoconductive detectors. Those detectors exhibited approximately a 2 GHz bandwidth, and there was no indication of conduction by a 2 DEG. It may, however, be possible to increase the response speed of HEMT detectors - at the expense of responsivity - by specially tailoring the vertical profile of the material.

4. CURRENT STATUS OF 830 NM OPTOELECTRONIC INTEGRATION

As indicated previously, there has been much discussion of the potential use of optical interconnects to MMIC's, where an optical receiver would be directly integrated on the same chip with, for example, microwave amplifiers or similar circuitry. The authors are unaware of any reports of such integrated circuits in the literature, but there have been reports describing monolithic MESFET optoelectronic integrated circuits (OEIC's) that have various capabilities for detecting and processing optically transmitted information and control signals. There have also been reports of the monolithic integration of (digital) control circuitry with microwave devices. Since such chips are the precursors of fully integrated OEIC-

MMIC's, a set of appropriate examples, with varying degrees of complexity, of ion-implanted and epitaxial OEIC's have been selected from the literature. The salient features of these selected OEIC's, all of which operate at a wavelength of 830 nm, are given in Table II.¹⁶⁻²⁰

It should be pointed out that if OEIC's and MESFET microwave amplifiers are to be integrated on the same chip it will be necessary to compromise to a single structure for all of the FET's - and therefore of the optical detector - in the interest of ease of fabrication and production yield. The choice that has been made in similar circumstances is to choose a structure with a pinchoff that is midway between the high value that is optimum for high power linear devices and the low value that would be chosen for low power digital circuits.²¹ On the other hand, this compromise may not be necessary if the circuit is fabricated on a HEMT or MODFET structure.

5. CONCLUSIONS

In this paper we have given some consideration to the general issue of monolithic integration of control circuitry with microwave amplifiers and we have assessed optical detectors whose structures are compatible with GaAs MMIC's. We have seen that detectors of compatible technology, and of adequate speed, for both MESFET and HEMT structures, exist for use in control signal interconnects. We have also found that there are broader issues than detector compatibility that must be addressed before a decision can be made to do large scale integration of microwave, control, and optical components.

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Table I. Representative 830 nm Detectors
with MMIC-Compatible Technology

Material	Detector type	3 dB Frequency	Reference
1. Ion-implanted MESFET			
SI GaAs, direct on wafer	MSM	4.5 GHz	Slayman, et. al. (1981) ⁹
SI GaAs, ion implanted	nnp Photodiode	5.1	Wojtczuk, et. al. (1987) ¹⁰
	npM Photodiode	3.1	
	Photoconductor	0.25	
SI GaAs, ion implanted	MSM	4.	Claspy, et. al. (1989) ¹¹
	nnp Photodiode	>7.	
	Photoconductor	≈ 6.	
2. Epitaxial MESFET			
n-GaAs epi layer	Schottky diode	>1.3	Blauvelt, et. al. (1984) ¹²
GaAs,n ⁻ -GaAs, n ⁺ -GaAs epi layers	MSM	>1.3	Ito, et. al. (1986) ¹³
n-GaAs epi layer	Photoconductor	≈ 20.	Lam, et. al. (1987) ¹⁴
3. AlGaAs/GaAs HEMT (MODFET)			
n ⁺ -AlGaAs/AlGaAs/GaAs single period	Photoconductor	≈ 2.	Claspy, et. al. (1988) ¹⁵

TABLE II. Current Status of 830 nm Optoelectronic Integration

1. Ion implant MESFET Technology

- Bell Communications - Cray OEIC: Chang, et. al. (1987)¹⁶
 - MSM Detector, amplifier
 - 3 dB bandwidth = 1.5 GHz, 15 dB amplifier gain
 - Fully monolithic chip
- IBM OEIC: Crow, et. al. (1989)¹⁷
 - MSM detector, 4-stage amplifier, clock recovery, serial data recovery
 - Operates at 1 Gbit/sec, - 22 dBm optical input for BER < 10⁻⁹
 - Fully monolithic chip
- NASA Lewis - Honeywell OEIC: Unpublished
 - npn detector, 3-stage amplifier, 1:16 demultiplexer
 - Hybrid precursor tested to 300 Mbit/sec, - 30 dBm optical input
 - Fully monolithic chip to operate at 1 Mbit/sec, being packaged for test

2. Epitaxial MESFET Technology

- Fujitsu Laboratories: Makiuchi, et. al. (1985)¹⁸
 - MSM detector, 2-stage amplifier
 - 2 Gbit/sec NRZ, estimated BER = 10⁻⁹ with - 20 dBm optical input
 - Fully monolithic
- IBM Zurich: Harder, et. al. (1988)¹⁹
 - MSM detector, 2-stage amplifier
 - 5.2 GHz bandwidth
 - Fully monolithic
- Naval Research Laboratory: Anderson, et. al. (1988)²⁰
 - Photoconductor detector, linear amplifier array (10 elements)
 - 650 ps rise time, 1.1 ns fall time for detector-amplifier combination
 - Fully monolithic

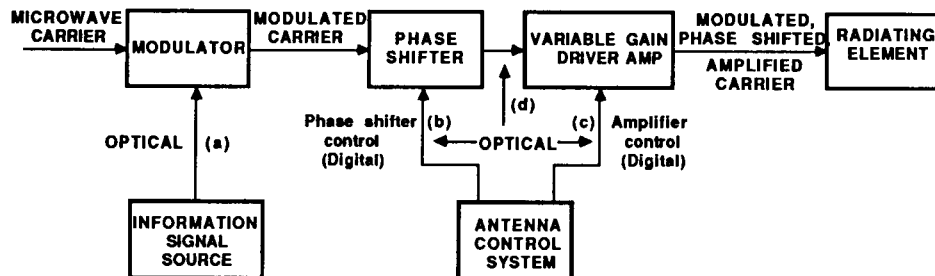


Fig. 1. Schematic of communications system output subsystem showing possible optical interconnects. a, b, and c are wide bandwidth baseband signals, d is a narrow fractional bandwidth signal based at the system carrier frequency.

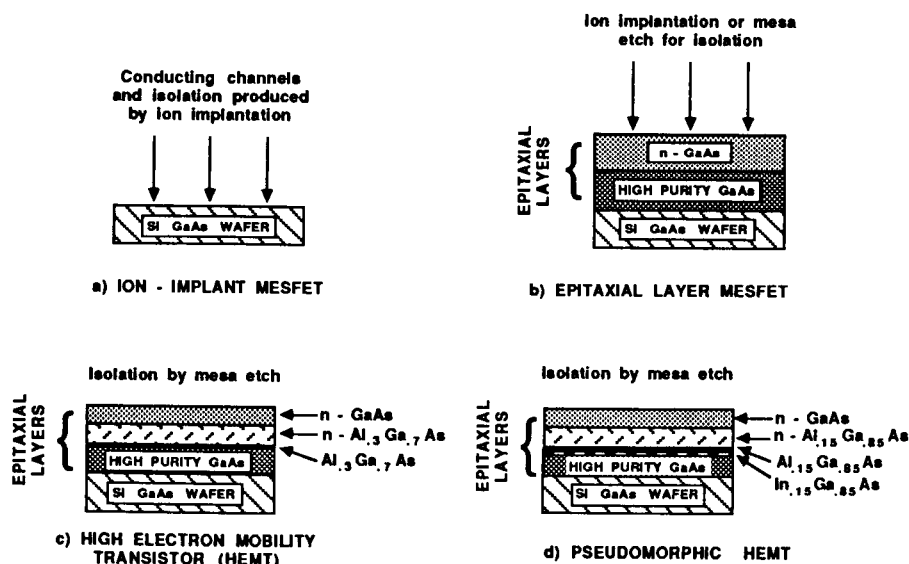


Fig. 2. Schematic of MMIC material structures and fabrication techniques.

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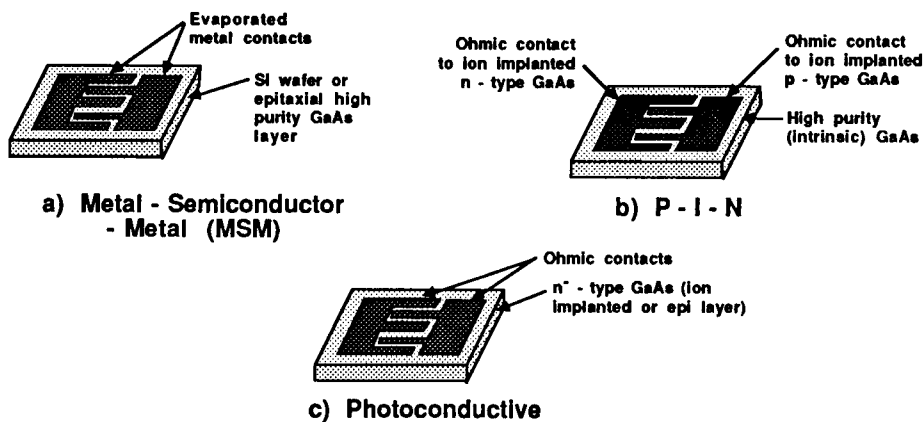


Fig. 3. Schematic of planar detector types.

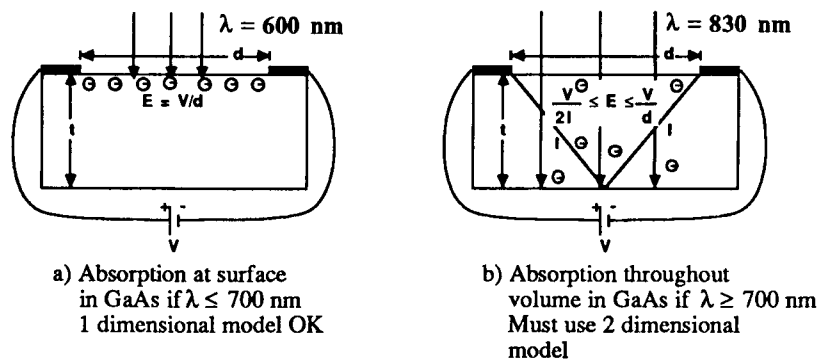


Fig. 4. Effect of absorption coefficient on detector speed.

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