

(NASA-Case-GSC-13564-1)  
CAPACIFLECTOR CAMERA Patent  
Application (NASA) 39 p

N94-15872

Unclas

G3/35 0190824

NASA CASE NO. GSC 13,564-1

PRINT FIG. 4b

NOTICE

The invention disclosed in this document resulted from research in aeronautical and space activities performed under programs of the National Aeronautics and Space Administration. The invention is owned by NASA and is, therefore, available for licensing in accordance with the NASA Patent Licensing Regulation (14 Code of Federal Regulations 1245.2).

To encourage commercial utilization of NASA-owned inventions, it is NASA policy to grant licenses to commercial concerns. Although NASA encourages nonexclusive licensing to promote competition and achieve the widest possible utilization, NASA will consider the granting of a limited exclusive license, pursuant to the NASA Patent Licensing Regulations, when such a license will provide the necessary incentive to the licensee to achieve early practical application of the invention.

Address inquiries and all applications for license for this invention to NASA Patent Counsel Goddard Space Flight Center Code 204  
Greenbelt, MD 20771

Approved NASA forms for application for nonexclusive or exclusive license are available from the above address.

Serial No.: 08/090,230  
Filing Date: 07/12/93

GSFC

## AWARDS DIGEST

## CAPACIFLECTOR CAMERA

The basic design for a capaciflector camera is shown in FIG. 4b in which multiple sensors 12 ( $12_1$  -  $12_7$  for seven rows and  $12_1$  to  $12_6$  for six columns) are used in conjunction with floating shield 1 and ground-coupled shield 2. Although seven rows and six columns are shown, any number of rows and columns can be used. As in the original "capaciflector", sensors 12 and shields 1 (if used) and 2 are strips of conductive material with an insulative coating. Each sensor strip 12 is attached to floating shield 1 (if used) with no gap between shield 12 and shield 1 (or shield 2 if shield 1 is not used); likewise, shield 2 is attached to surface 15 with no gap between shield 2 and surface 15. In order to have both rows and columns, it is necessary to have sensor strips 12 pass over and under each other in the weave geometry shown. Each sensor strip 12 "sees" the other sensor strips as shields, so the presence of these other sensor strips will be insignificant in terms of leakage.

Novelty is believed to reside in extending the original "capaciflector" technology to a device with multiple sensor strips.

NASA Case No. GSC 13,564-1

Inventor: John M. Vranish, Goddard Space Flight Center

Serial No.: 08/090,230

Filing Date: 07/12/93

**PATENT**

NASA Case No. GSC 13,564-1

Application of: John M. Vranish

TITLE OF THE INVENTION

"CAPACIFLECTOR" CAMERA

Origin of the Invention

The invention described herein was made by an employee of the United States Government, and may be manufactured and used by or for the Government for governmental purposes without the payment of  
5 any royalties thereon or therefor.

CROSS REFERENCE TO RELATED APPLICATIONS

This invention is related to an invention shown and described in (1) U.S. Serial No. 07/889,577 (Attorney Docket No. GSC 13,460-1), entitled "Phase Discriminating Capacitive Sensor", filed in the  
10 name of John M. Vranish, the present inventor, on 05/28/92; (2) U.S. Serial No. 07/979,987 (Attorney Docket No. GSC 13,489-1), entitled "Steering Capaciflector Sensor", filed in the name of Jenstrom et al. on 11/28/92; (3) U.S. Serial No. 08/008,426



GSC 13,564-1  
Vranish

individual sensors can be made large, such as 3 to 6 inches. Also, for a "capaciflector" sensor to perform efficiently, the shield must be much larger than the sensor (on the order of 7 times wider) and each sensor must have its own shield. The shield is slaved to the sensor via a voltage follower, so it changes frequency with the sensor. If another sensor is introduced, another CCO and voltage follower shield must be added. Hence, the sensors must be spaced no closer than the width of the shields. Even then, there is some cross talk because the two sensors are out of phase with each other. This application is satisfactory for collision avoidance arrays and rudimentary alignment, but an entirely new approach is necessary for a "capaciflector" camera.

Tactile capacitive arrays using pixels are also available in the research environment and appear to be practical for medical and industrial applications. However, these devices have such a short range that they would not have the depth of view necessary for good capacitive 3-D proximity images and they would be impractical for robotic collision avoidance purposes (frequently part of the "capaciflector" camera imaging function). Also, without the current-measuring voltage follower electronics architecture described in this disclosure (and an active current bridge version

GSC 13,564-1  
Vranish

of the same), there will be cross talk between the pixels and/or their leads.

"Capaciflector" 3-D imaging using a single sensor has also been achieved in the prior art. In this application, the single  
5 "capaciflector" sensor is moved through a volume with sensor readings being taken sequentially in the process. These readings are compared with the robot joint angles and a 3-D image results. There are three disadvantages to this type of system; first, large volumes often must be swept out in order to obtain a complete  
10 picture and this may not be possible or practical in a docking or berthing application; second, the process takes a considerable amount of time; and third, the measurements must be correlated with robot joint angle information which introduces further inaccuracies.

#### Statement of the Invention

It is therefore an object of the present invention to provide a sensor with multiple, independently-sensing "capaciflector" sensors in close proximity to each other sharing a common  
20 reflecting shield to facilitate robotic collision avoidance and berthing/docking.

GSC 13,564-1  
Vranish

Another object of the present invention is to provide a capacitive camera that will provide a 3-D image to give a robot a physical model of the region being observed by the camera.

5 A further object is to provide a sensor with a narrow bandwidth thereby providing increased range and sensitivity with less noise and EMI problems than current devices.

10 These and other objects are achieved by providing a device with several sensors placed in close proximity to each other above a driven shield with each sensor and shield interfaced to a common voltage source by current-measuring voltage follower circuits. All sensors and the shield are thus locked to the voltage source both in amplitude and in phase, and the shield "shields" all sensors from ground providing the desired "capaciflector" effect. An additional shield can be added and is a "floating" shield, which is  
15 not necessary for proper operation of this circuit, but can be added for use as an additional "sensor" if desired; it also has its own current-measuring voltage follower circuit. The floating shield is, itself, shielded from ground by the ground-coupled shield so it can "see" between the individual sensors to give an  
20 overall extended range reading. This device also has a reference circuit and an amplification circuit to further process the signals

GSC 13,564-1  
Vranish

from the sensors and provide an output which can be utilized as a control signal.

Functionally there is no difference between the shield and the sensors so each sensor "sees" the other sensors as a reflecting shield and vice versa, thus eliminating cross talk between the sensors. As an object approaches the sensors, different currents pass through each sensor, depending upon its location relative to the object, and each of these currents are "measured", independently, by the sensors' associated current-measuring voltage follower circuit. The result is independent sensing by the sensors in close proximity to each other and with outstanding range and sensitivity for each sensor.

Figure 4b shows a perspective of the "capaciflector" camera embodiment. Multiple sensors 12 ( $12_1$  -  $12_7$  for seven rows and  $12_1$  to  $12_6$  for six columns) are used in conjunction with floating shield 1 and ground-coupled shield 2. Although seven rows and six columns are shown, any number of rows and columns can be used. As in the original "capaciflector", sensors 12 and shields 1 (if used) and 2 are strips of conductive material with an insulative coating. Each sensor strip 12 is attached to floating shield 1 (if used) with no



GSC 13,564-1  
Vranish

gap between the insulation on shield 12 and the insulation on shield 1 (or shield 2 if shield 1 is not used); likewise, shield 2 is attached to surface 15 with no gap between the insulation on shield 2 and surface 15. In order to have both rows and columns, it is necessary to have sensor strips 12 pass over and under each other in the weave geometry shown. This sensor array of seven rows and six columns behaves exactly as described for the multiple sensors shown in figures 3a and 3b except that each sensor strip 12 must alternately pass over and under other sensor strips 12. As described above, each sensor strip 12 "sees" the other sensor strips as shields, so the presence of these other sensor strips will be insignificant in terms of leakage.

#### Brief Description of the Drawings

Figure 1a is a circuit diagram of a prior art "capaciflector" using capacitively-controlled oscillator technology.

Figure 1b shows the prior art "capaciflector" principle.

Figure 1c is a schematic diagram of a current-measuring voltage follower circuit.

Figure 2 is a circuit diagram of a source-locked "capaciflector" utilizing current-measuring voltage follower

circuits.

Figure 3a is a schematic diagram of a multiple sensor "capaciflector" circuit utilizing current-measuring voltage follower circuits.

5 Figure 3b is a diagram of the multiple sensor fields of figure 3a.

Figure 4a is a schematic diagram of the "capaciflector" camera parasitic capacitances.

10 Figure 4b is a perspective view of the "capaciflector" camera elements.

Figure 4c is a perspective view of an alternate embodiment of the "capaciflector" camera elements.

Figure 4d is a perspective view of another alternate embodiment of the "capaciflector" camera elements.

15 Figure 5a is a circuit diagram of a classic Wheatstone bridge utilized in the prior art.

Figure 5b is a circuit diagram of an active current bridge.

20 Figure 6 is a schematic diagram of an alternate embodiment "capaciflector" camera using active current bridge sensor array circuits.

Figure 7 is a circuit diagram of an active current bridge

GSC 13,564-1  
Vranish

sensor array.

#### Detailed Description of the Invention

The "capaciflector" camera sensor will be a fundamental  
5 building block of NASA robotics for use in berthing and docking for  
robotic construction, assembly and maintenance in outer space and  
for imaging and navigation for planetary rover robots. The ability  
to place individual "capaciflector" sensors in close proximity to  
each other as a "capaciflector" camera enables docking and berthing  
10 pins, and holes and latches to be precisely imaged in 3-D. Since  
the "capaciflector" camera gives monotonically accelerating  
stronger signals as it is closer to an object, the "capaciflector"  
camera will give very precise images in close to the object, the  
very region where other imaging techniques, such as standard  
15 cameras and lasers have problems. And, because several  
"capaciflector" camera sensors can be placed on or near pins,  
latches and holes, the information available at or near the points  
of contact between the payload and the fixture it is being mated to  
will be exceptionally good. For exploration robots, the  
20 "capaciflector" camera will provide close-in images which will  
prove invaluable in obstacle avoidance and navigation.

GSC 13,564-1  
Vranish

The fundamental advantages of capacitive-based sensors are retained in this new embodiment of the original "capaciflector" which is described in detail in U.S. Patent 5,166,679 and shown schematically in figure 1a where a capacitive proximity sensing  
5 element 12, backed by a reflector shield 2 driven by op-amp 4 at the same voltage as and in phase with sensor 12, is used to reflect the field lines away from a grounded surface 15 towards an intruding object 10. The output signal from sensor 12 is coupled to capacitively controlled oscillator circuit 3 whose output is  
10 inversely proportional to the capacitance of sensor 12. The advantages of this type sensor system include: a simple, rugged, flexible configuration that can fit on the surface of any structure; ability of the system to function in outer space; the ability to sense virtually any object it encounters; the ability of  
15 the sensor to "see" through paint and other protective coatings; and, low power. The previous problem of inadequate range has also been solved by "capaciflector" technology.

An electronic circuit architecture is disclosed in my "Current-Measuring Op-amp Devices" U.S. patent application,  
20 referenced above, that is particularly useful in my "capaciflector" camera. The innovative circuit element is called a current-

GSC 13,564-1  
Vranish

measuring voltage follower and is shown in figure 1c. Because op-amp 4 is, in effect, a micro-miniature precision high-gain servo system, source 28 voltage  $V_s$ , at the + terminal of op-amp 4, is servoed through op-amp 4 to its - terminal. Although both of the  
5 input terminals of op-amp 4 are of high impedance, op-amp 4 is also a current source with a low impedance output. Therefore, if the current-measuring voltage follower circuit of figure 1c is inserted between each of a series of sensors, and excites them by a common voltage source, the voltage drop (amplitude and phase) across the  
10 sensors will be nearly identical, and essentially the same as the voltage source. As with any feedback system, there are slight losses, but these are in microvolts and of no practical importance. The current through each sensor will also vary according to what it "senses", and this current changes can be "measured" by the  
15 sensors' current-measuring voltage follower circuit. A standard voltage follower circuit, on the other hand, would supply the required sensor current, but would not "measure" it.

Figure 2 shows schematically a source-locked "capaciflector" sensor system that uses my current-measuring voltage follower  
20 circuit (op-amp 6, resistor 18) to interface source 28 voltage to

GSC 13,564-1  
Vranish

sensor 12 and also a second one of my current-measuring voltage follower circuits (op-amp 4, resistor 16) to interface source 28 voltage to shield 2, located above grounded surface 15. This effectively locks both sensor 12 and shield 2 to source 28 voltage, and to each other, and therefore shield 2 electrical fields serve to block the path of sensor 12 electrical fields to ground, resulting in a "capaciflector" action and with it increased range and sensitivity. The remaining components of figure 2 represent standard electronic circuits; resistors 20, 22, 24, 26 and op-amp 8 comprise reference circuit 5 which extracts a reference voltage from source 28, which is converted to a DC current by AC to DC converter 30; the signal from sensor 12 "measured" by its current measuring voltage follower circuit is converted to DC by AC to DC converter 32; the difference between these two "signals" is then amplified by amplification circuit 7 containing resistors 34, 36, 38, 40 and op-amp 14; the amplified signal is then supplied as output signal  $V_o$  to a suitable measurement/display device (not shown).

With the approach of object 10, current through sensor 12 increases and this increased current is detected by a change in voltage across resistor 18 in sensor 12's current-measuring voltage

GSC 13,564-1  
Vranish

follower circuit. Shield 2 will also "see" object 10, and its increased current flow through resistor 16 can also be detected, if desired, although this is not shown on figure 2. The figure 2 "capaciflector" circuit works for a fixed, as well as variable  
5 frequency so it is possible to use a crystal-controlled oscillator as source 28, if desired, thereby reducing frequency drift over long periods of time in outer space and to increase range and sensitivity by using a very narrow band system.

The circuit shown in figure 3a extends the figure 2 source-  
10 locked "capaciflector" concept to one involving several (in this case three) sensors 12 in close proximity to each other. Figures 3a and 3b also introduce the use of two reflective shields. As shown, if several sensors 12 (i.e.,  $12_1$ ,  $12_2$ ,  $12_3$ ) are placed in close proximity to each other above driven shield 2 and if each  
15 sensor 12 (and shield 2) are interfaced to a common voltage source 28 by current-measuring voltage follower circuits (op amp  $6_1$  and resistor  $18_1$  for sensor  $12_1$ ; op amp  $6_2$  and resistor  $18_2$  for sensor  $12_2$ ; and op amp  $6_3$  and resistor  $18_3$  for sensor  $12_3$ ) then all sensors  
20 12 as well as shield 2 are locked to voltage source 28 both in amplitude and in phase, and shield 2 "shields" all sensors 12 from

GSC 13,564-1  
Vranish

ground providing the desired "capaciflector" effect. The additional shield 1 is a "floating" shield, which is not necessary for proper operation of this circuit, but can be added for use as an additional "sensor" if desired; it also has its own current-measuring voltage follower circuit of op amp 4, and resistor 16.  
5 Floating shield 1 is, itself, shielded from ground by the ground-coupled shield 2 so it can "see" between the individual sensors 12 to give an overall extended range reading. Although not explicitly shown, just as in figure 2 this circuit also has a reference  
10 circuit 5 and an amplification circuit 7 to complete the schematic.

Functionally there is no difference between shield 2 and sensors 12 so each sensor 12 "sees" the other sensors as a reflecting shield and vice versa, thus eliminating cross talk between sensors 12. As object 10 approaches sensors 12, different  
15 currents pass through each sensor 12, depending upon its location relative to object 10, and each of these currents are "measured", independently, by sensors' 12 associated current-measuring voltage follower circuit. The result is independent sensing by sensors 12 in close proximity to each other and with outstanding range and  
20 sensitivity for each sensor 12.



GSC 13,564-1  
Vranish

Figure 4b shows the source-locked "capaciflector" concept extended still further to an entire "capaciflector" camera embodiment. Multiple sensors 12 ( $12_1$  -  $12_7$  for seven rows and  $12_1$  to  $12_6$  for six columns) are used in conjunction with floating shield 1 and ground-coupled shield 2. Although seven rows and six columns are shown, any number of rows and columns can be used. As in the original "capaciflector", sensors 12 and shields 1 (if used) and 2 are strips of conductive material with an insulative coating. Each sensor strip 12 is attached to floating shield 1 (if used) with no gap between the insulation on sensor 12 and the insulation on shield 1 (or shield 2 if shield 1 is not used); likewise, shield 2 is attached to surface 15 with no gap between the insulation on shield 2 and surface 15. In order to have both rows and columns, with equal access to the object by each, it is necessary to have sensor strips 12 pass over and under each other in the weave geometry shown. This sensor array of seven rows and six columns behaves exactly as described for the multiple sensors shown in figures 3a and 3b except that each sensor strip 12 must alternately pass over and under other sensor strips 12. As described above, each sensor strip 12 "sees" the other sensor strips as shields, so

GSC 13,564-1  
Vranish

the presence of these other sensor strips will be insignificant in terms of leakage.

The parasitic capacitances for a single sensor strip 12 of the figure 4b embodiment are given in the equivalent circuit shown in figure 4a where the dashed lines represent leakages to other elements and:

$C_{so}$  = sensor to object capacitance  
 $C_{og}$  = object to ground capacitance (assumed to be large)  
 $C_{sg}$  = sensor to ground capacitance (typically approx. 6 pf)  
 $C_{HI}$  = sensor to High Impedance shield capacitance (typ. approx. 120 pf)  
 $C_{IG}$  = High Impedance Shield to ground capacitance (typ. approx. 800 pf)  
 $C_{OTO}$  = secondary sensor to object capacitance (typ. approx. 6 pf)  
 $C_{OTG}$  = secondary sensor to ground capacitance (typ. approx. 6 pf)  
 $C_{OT}$  = sensor to secondary sensor capacitance

The potential difference between any sensor 12 and ground through these parasitics is less than 50 micro volts.

Current leakage in the "capaciflector" camera is not noticeably reduced from that of a single sensor "capaciflector". Each current-measuring voltage follower circuit tracks the voltage across its resistive load to within  $100\mu V$  of source 28 voltage. Using an insulation separation between sensor strips passing over and under each other of 0.005 in., a leakage field of 20 mV/in. from one sensor 12 to adjacent sensor 12 and from any sensor 12 to

GSC 13,564-1  
Vranish

shield 1 exists. Assuming a minimum or worst case distance of 3 in. from the edge of shield 2 and assuming a maximum load voltage (typical) of 8V, a ground return leakage voltage of 2.6 V/in. is typical. The ground return leakage is therefore 130 times that of the leakage caused by the current-measuring voltage follower circuits. For an object 10 approaching the "capaciflector" camera, a 20 mV/in. field is induced at a range of approx. 33 ft. when a 1 ft. range is required. These figures represent worst case assumptions. Leakage between sensor 12 strips and or shield 2 requires the charge to leak across at least two shields in series so the real leakage field is orders of magnitude below 20 Mv/in.

#### Alternate Embodiments of the Invention

Figure 4c shows an alternate embodiment "capaciflector" camera to that depicted in figure 4b. In this embodiment, multiple sensors 12 ( $12_1 - 12_3$  for three rows and  $12_4$  to  $12_6$  for four columns) are used in conjunction with ground-coupled shield 2. Although three rows and four columns are shown, any number of rows and columns can be used. As in the figure 4b "capaciflector" camera, sensors 12 and shield 2 are strips of conductive material with an

GSC 13,564-1  
Vranish

insulative coating. Although not shown, a floating shield could also be used in this embodiment. Each sensor strip 12 is attached to shield 2 with no gap between the insulation on sensor 12 and the insulation on shield 2; likewise, shield 2 is attached to surface  
5 15 with no gap between the insulation on shield 2 and surface 15. In order to compensate for the unequal access to the object by each sensor strip 12, it is necessary to have sensor row strips 12 larger than the sensor column strips 12 as shown. This sensor array of three rows and four columns behaves nearly the same as described  
10 for the multiple sensors shown in figure 4b. A minimum amount of simple calibration will be needed with this design.

Figure 4d shows another alternate embodiment "capaciflector" camera to that depicted in figure 4b. In this embodiment, multiple sensors 12 (12<sub>1</sub> - 12<sub>5</sub> for five rows and 12<sub>1</sub> to 12<sub>3</sub> for three columns)  
15 are used in conjunction with ground-coupled shield 2. Although five rows and three columns are shown, any number of rows and columns can be used. As in the figure 4b "capaciflector" camera, sensors 12 and shield 2 are strips of conductive material with an insulative coating. Although not shown, a floating shield could  
20 also be used in this embodiment. Each sensor strip 12 is attached

GSC 13,564-1  
Vranish

to shield 2 with no gap between the insulation on sensor 12 and the insulation on shield 2; likewise, shield 2 is attached to surface 15 with no gap between the insulation on shield 2 and surface 15. In this embodiment, the unequal access to the object by each sensor strip 12 is compensated for by sensor row columns 12 spaced larger  
5 apart than the sensor row strips 12 as shown. This sensor array of five rows and three columns behaves nearly the same as described for the multiple sensors shown in figure 4b. A miniman amount of simple calibration will be needed with this design.

10 A classic Wheatstone bridge circuit, as shown in figure 5a, normally performs the role in signal detection now performed by an active current bridge in the alternate embodiment of the "capaciflector" camera shown in figure 6. A classic Wheatstone  
15 bridge operates as a voltage divider. The voltage down the left leg is identical to that down the right. The voltage down the left leg divides according to the impedances  $Z_1$  and  $Z_4$  at point A and that down the right leg divides according to the impedances  $Z_2$  and  $Z_3$  at point B. Thus, as  $Z_4$  varies (as in the case of a sensor) the voltage difference between points A and B changes and the voltage  
20 meter joining these two points reflects that change. For two sensors (or a sensor and a reference), this difference in potential

GSC 13,564-1  
Vranish

results in leakage (or crosstalk). This can be a very significant problem which increases as the imbalance grows (as in very high sensor readings).

5 The active current bridge shown in figure 5b arranges current-measuring voltage follower circuits and impedances in a bridge configuration similar to the Wheatstone bridge. In the figure 5b circuit the voltage across points A and B is always equal no matter how much  $Z_4$  varies. Thus, leakage (or crosstalk) is significantly reduced. This condition holds true even at very high sensor readings.  
10

Figure 6 shows an alternate embodiment of my "capaciflector" camera using current-measuring voltage follower circuits in an active current bridge arrangement (in contrast to a Wheatstone bridge arrangement). Capacitors 20 and 21<sub>1</sub>, 21<sub>2</sub>, 21<sub>3</sub> are used in  
15 the current-measuring voltage follower circuits (rather than resistors) for the current "measuring" function to eliminate the phase shift between the voltage measured directly at the output of current-measuring op-amps 4<sub>1</sub> and 6<sub>1</sub>, 6<sub>2</sub>, 6<sub>3</sub> and source 28 voltage, no matter how object 10 to sensor 12 proximity changes. The  
20 parallel resistors  $R_b$ , which accompany each of these capacitors is

GSC 13,564-1  
Vranish

very large and serves only to permit op-amp bias currents to bleed off. It is also possible to adjust the  $R_b$  and capacitors values to provide a nominal phase shift value if desired. This would be useful in phase measuring schemes.

5        Figure 5b illustrates the active current bridge used in the figure 6 alternate embodiment. In the active current bridge, common voltage source 28 is fed into a left and right current-measuring voltage follower which form the first element of the left leg and the first element of the right leg respectively. The left  
10    leg current,  $I_{L1}$ , is generated by the left current-measuring voltage follower and the right leg current,  $I_{L2}$ , by the right current-measuring voltage follower. The left leg current proceeds through reference  $Z_1$  to point A and from there across reference  $Z_3$  to ground. The right leg current proceeds through reference  $Z_2$  to  
15    point B and from there across unknown impedance  $Z_4$  to ground. The voltage drop from points A and B to ground are identical and equal to the source 28 voltage  $V_s$ . The right leg current,  $I_{L2}$ , is adjusted by the right leg op-amp as  $Z_4$  changes to keep the voltage drop across  $Z_4$  locked to  $V_s$ . This, however, changes the voltage  
20    across reference  $Z_2$ . Thus, there is a voltage differential between

GSC 13,564-1  
Vranish

the voltage drop across  $Z_2$  and that across  $Z_1$  and it is here where the bridge measurement is made. The differential voltage measurement is in front of impedances  $Z_1$  and  $Z_2$  in the active current bridge and is behind those impedances in the classical  
5 Wheatstone bridge.  $I_s \approx 0$  for the active current bridge, whereas  $I_s = I_{L1} + I_{L2}$  for the classical Wheatstone bridge.

Figure 7 shows how the active current bridge is utilized in the alternate embodiment "capaciflector" camera of figure 6. To reduce the effects of phase shifts in the bridge, with its  
10 capacitive loads, current-measuring capacitors 20 are installed in the current-measuring voltage follower circuits. A capacitor in a current-measuring voltage follower tends to integrate the offset voltages of op-amp 6, thus a large charge-bleeding resistor  $R_b$  is inserted in parallel with capacitor 20. The common reference leg  
15 of the bridge consists of a current-measuring voltage follower circuit which is identical with the other bridge legs and a reference capacitive load  $C_{Ref}$  which is the average of the sensor 12 element loads (except that it is not effected by the approach of an object) and a reference current-measuring capacitor 50 which is  
20 the average of the current-measuring capacitors 50. Each sensor 12



GSC 13,564-1  
Vranish

element acts as a bridge leg and is compared with the reference leg as is the floating shield leg. There is one bridge (complete with reference leg) for the rows and one for the columns. The floating shield 1 will be the leg of either the column or the row bridge.

5 The ground-coupled shield 2 will not be compared and so will not belong to either bridge. Each bridge will also have a second thermal drift reference current-measuring voltage follower circuit and reference capacitive load, but with a slight bridge voltage between the two reference legs leading to a reference voltage all  
10 the way through the amplification system. This voltage can change only as a result of thermal changes in the reference components and, so, gives an indication of the thermal drift in the rest of the bridge.

15 To those skilled in the art, many modifications and variations of the present invention are possible in light of the above teachings. It is therefore to be understood that the present invention can be practiced otherwise than as specifically described herein and still will be within the spirit and scope of the appended claims.

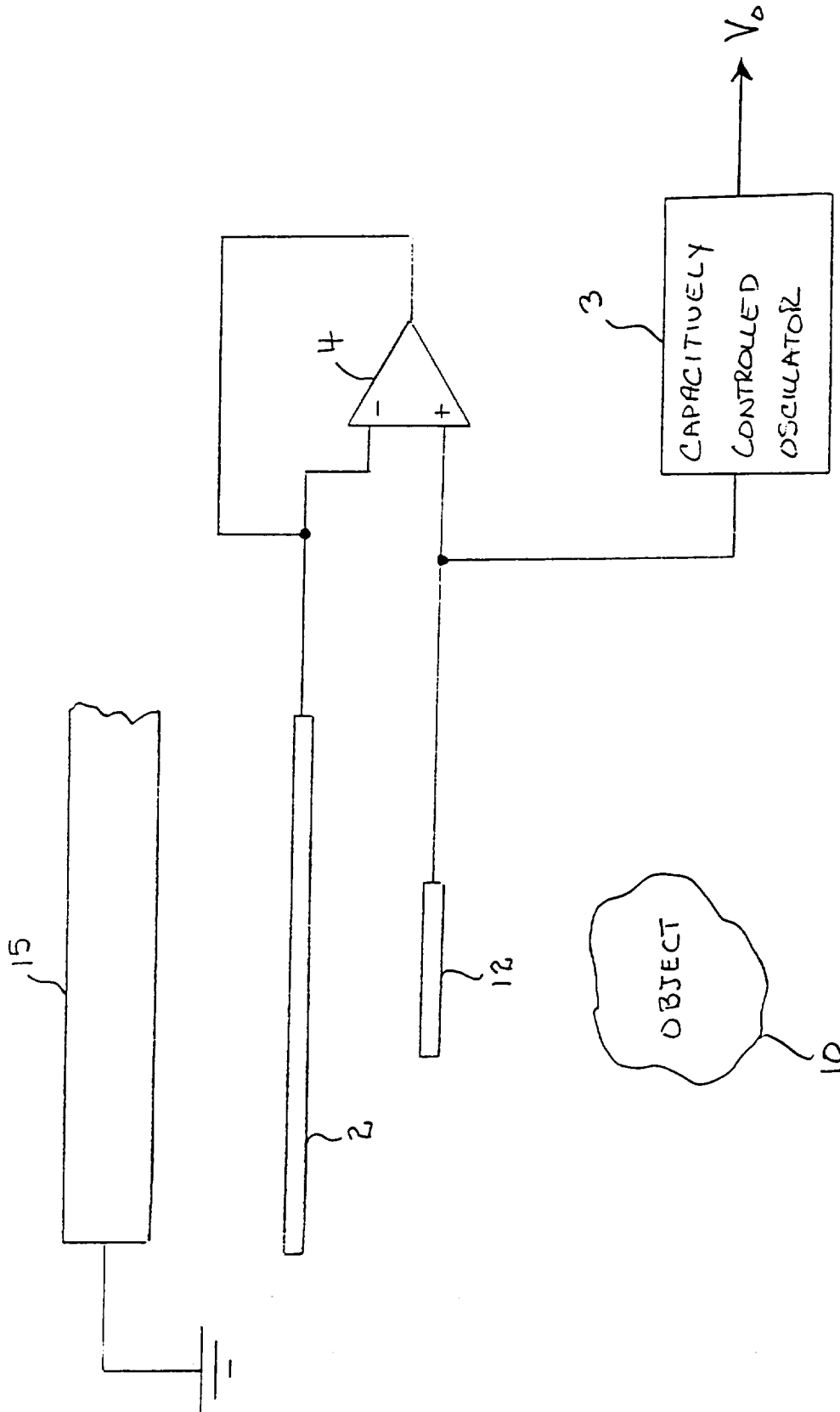
GSC 13,564-1  
Vranish

#### ABSTRACT

A capacitive type proximity sensor having improved range and sensitivity between a surface and an intruding object in the vicinity of the surface having a voltage source, a number of outer electrical conductors on the surface with each forming one electrode of a number of sensor capacitors, the other electrode for each sensor capacitor being the object. The outer conductors are made from thin sheets of conductive material covered with insulation. An intermediate electrical conductor is located between the outer conductors and the surface and is of a size larger than the outer conductors to act as a shield for reducing the parasitic capacitance between the outer conductors and the surface. The intermediate conductor is also made from a thin sheet of conductive material covered with insulation. The outer conductors and the intermediate conductor are attached to the surface with no gap between the insulation on the conductors and no gap between the surface and the insulation on intermediate conductor, the outer conductors and the intermediate conductor conjoining with each other and with the surface, with the surface acting as a ground plane. A current-measuring voltage follower circuit is connected to the voltage source for coupling in phase

**GSC 13,564-1**  
**Vranish**

and amplitude the instantaneous voltage at the voltage source to the outer electrical conductors and the intermediate electrical conductor. This circuit is responsive to the change in capacitance of the sensor capacitors and generates a number of output signals.



PRIOR ART

FIG 1a

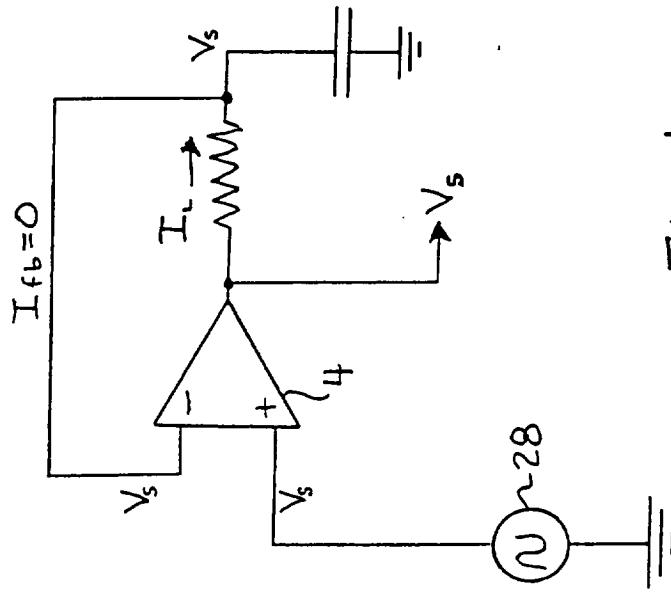
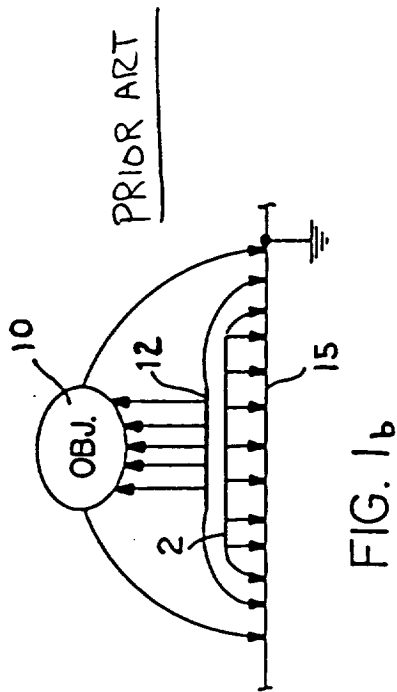


FIG 1c

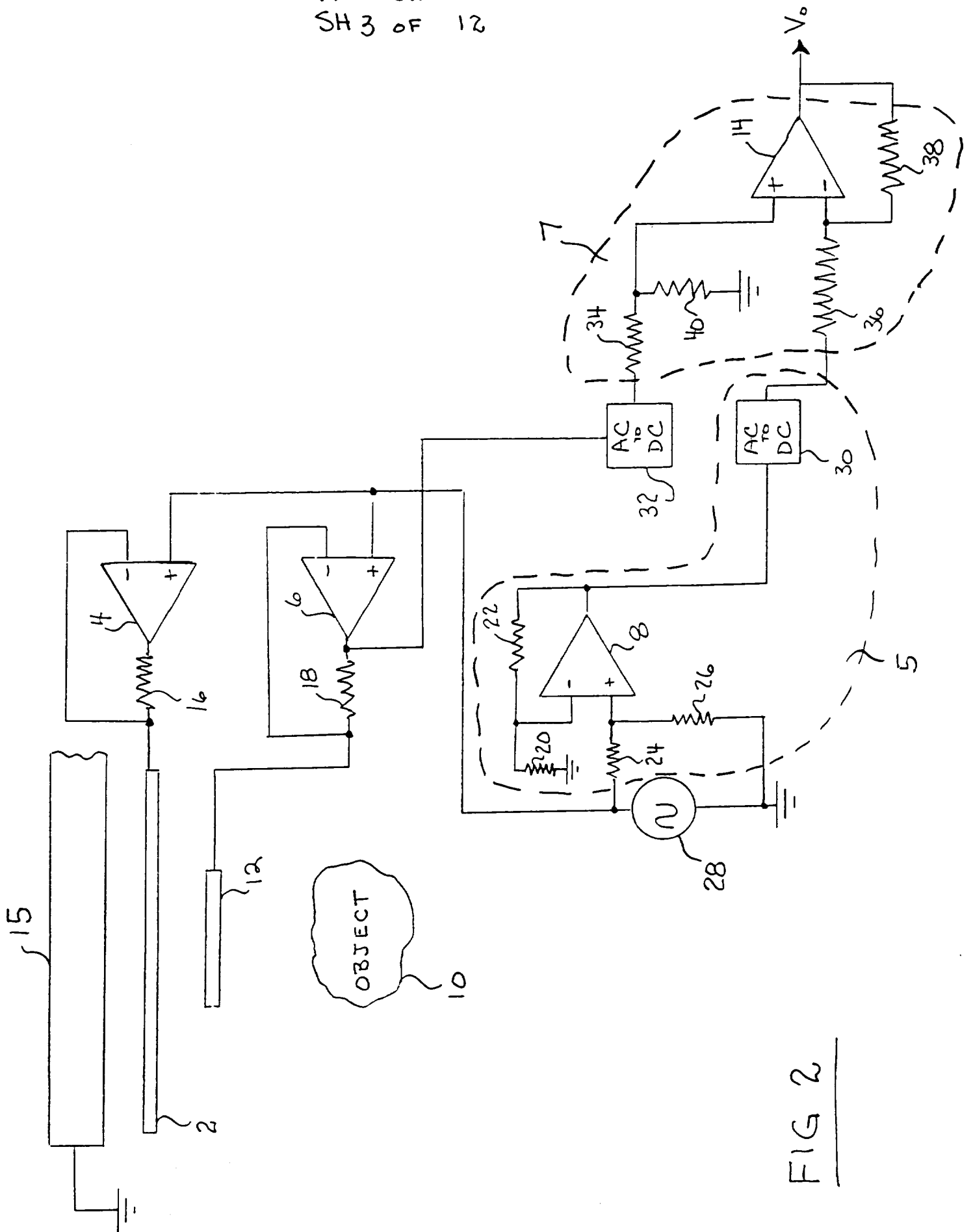


FIG 2

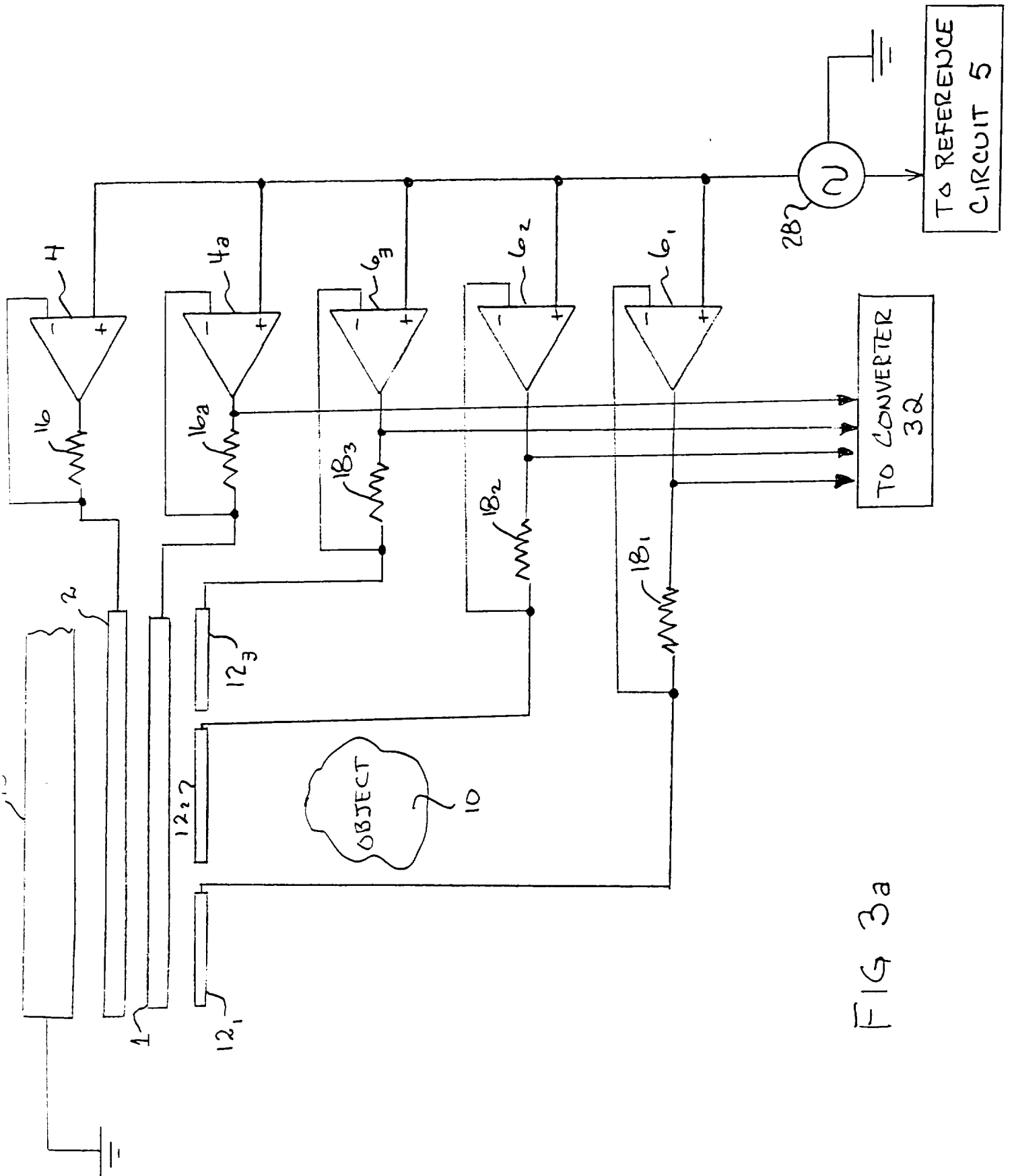


FIG 3a

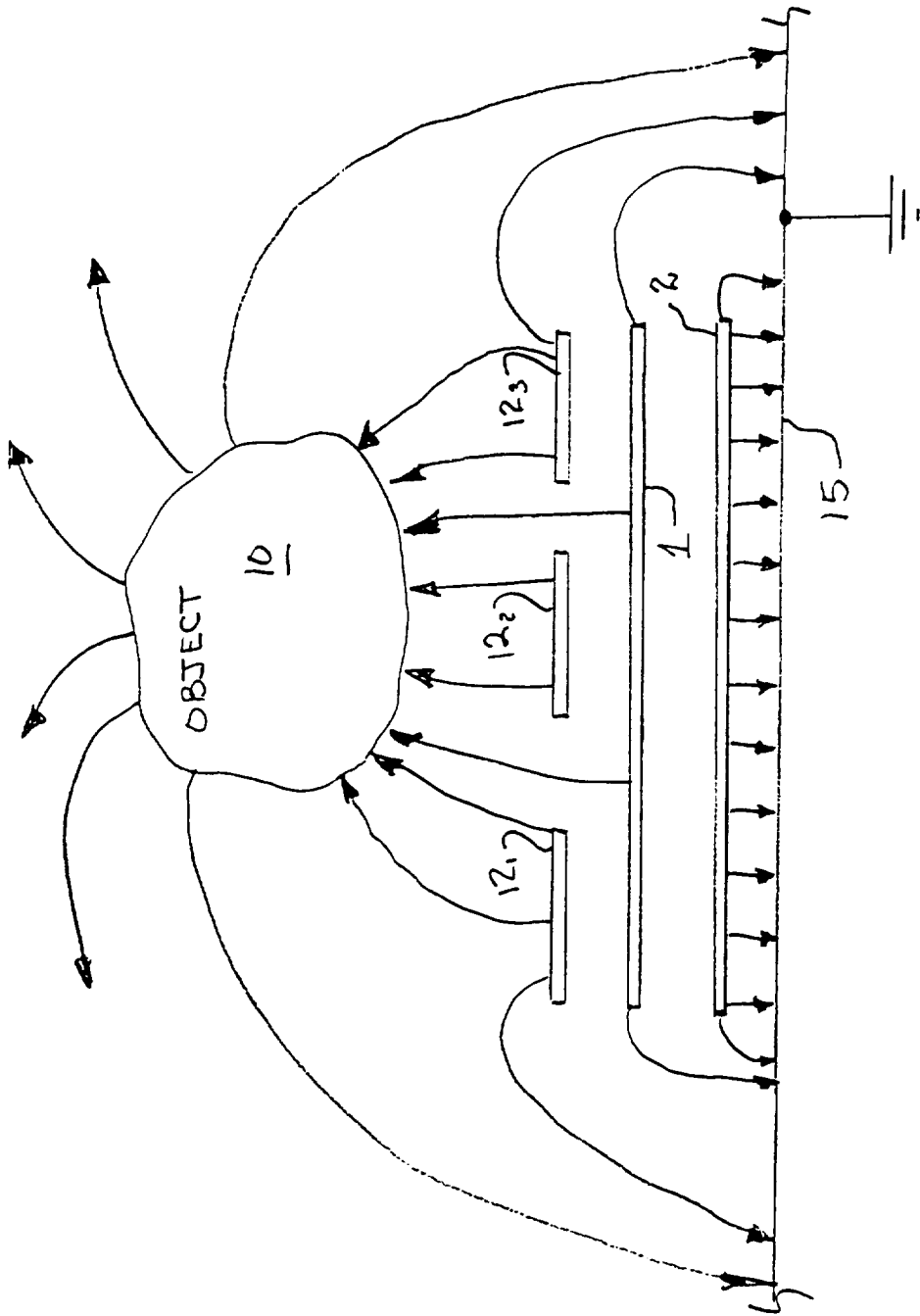


FIG 3<sub>b</sub>





PH 11

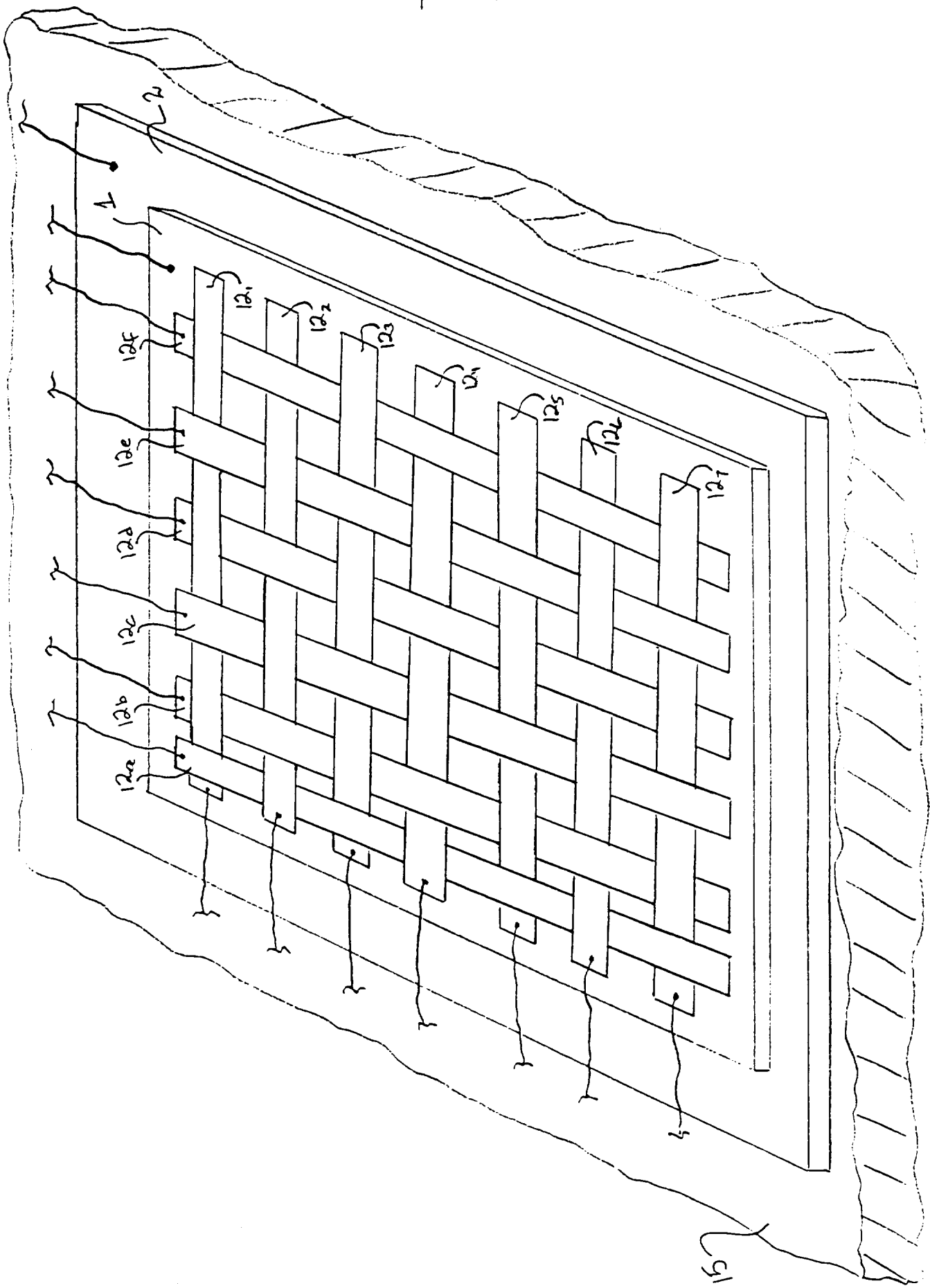
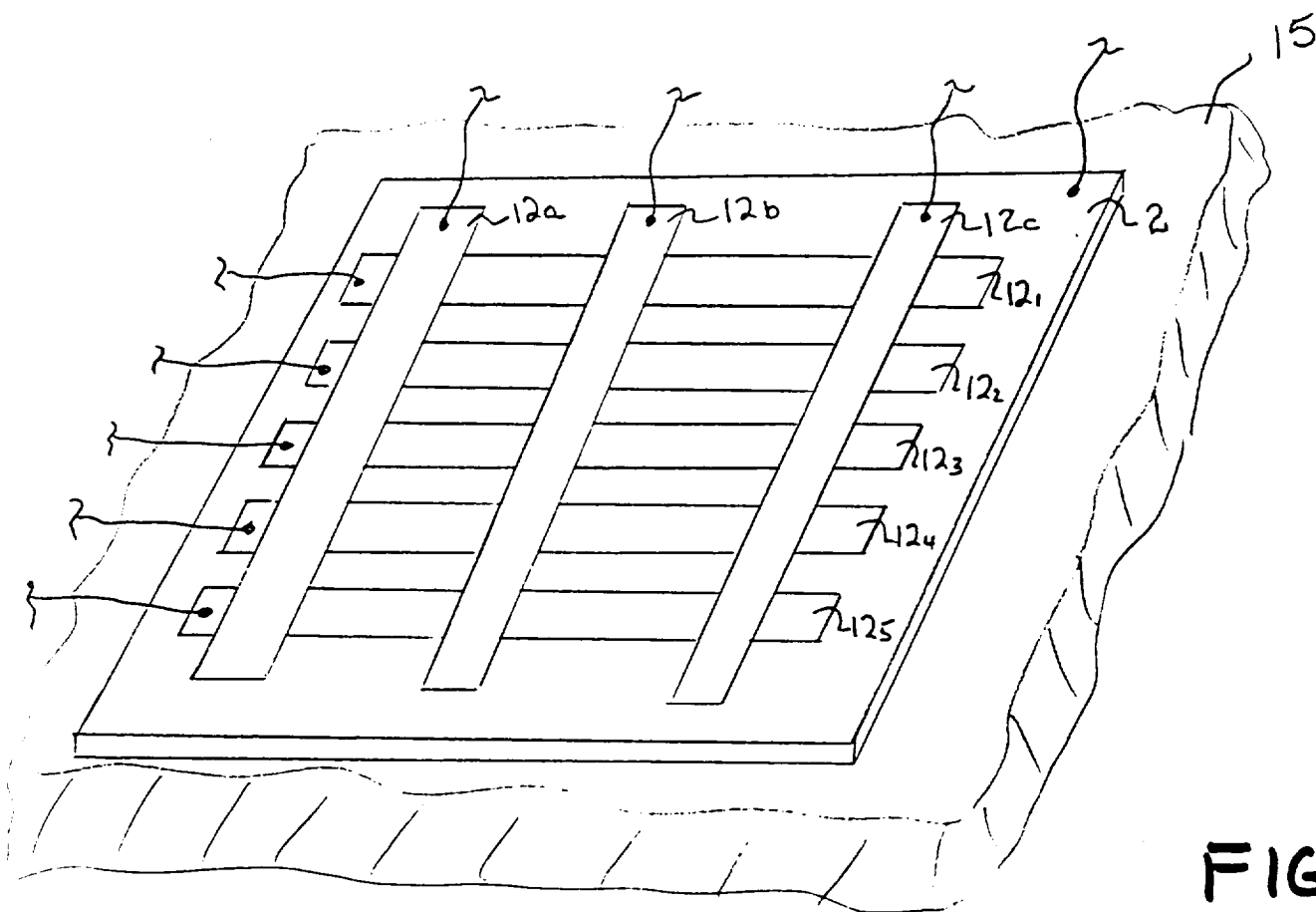
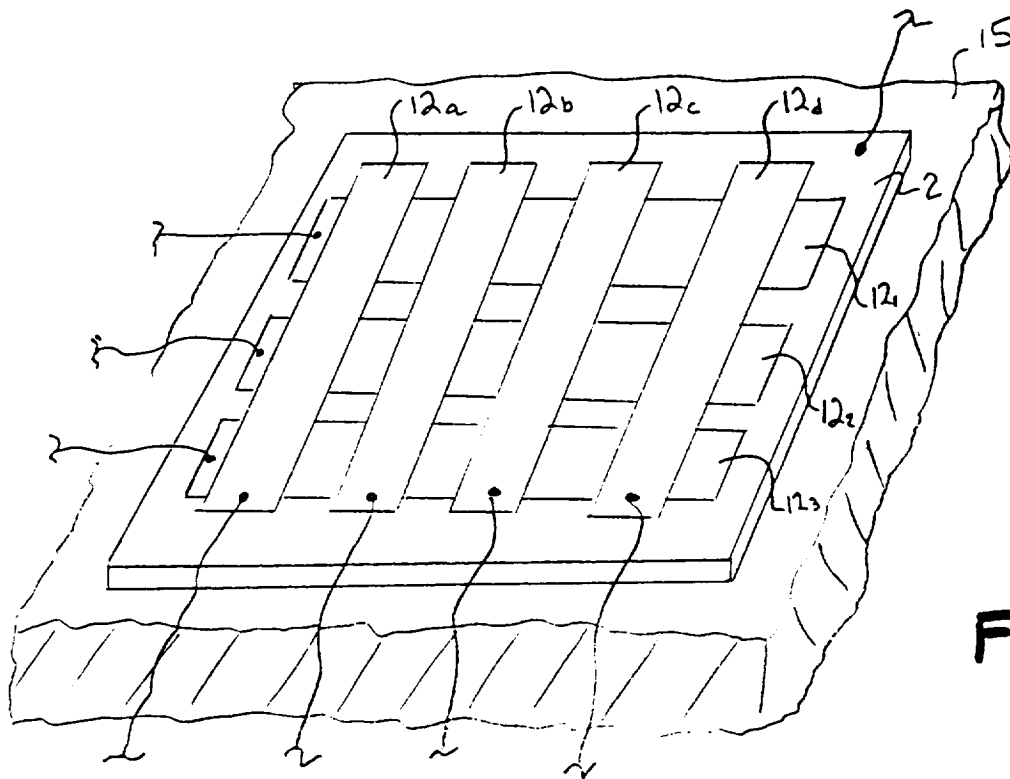
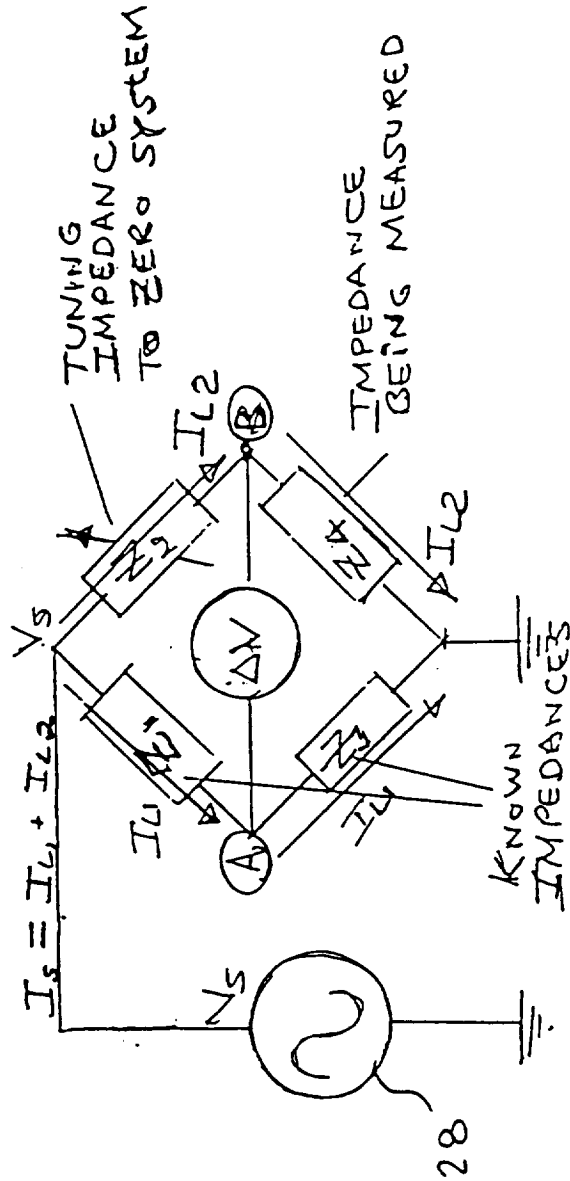


FIG. 4b





PRIOR ART

FIG. 5a

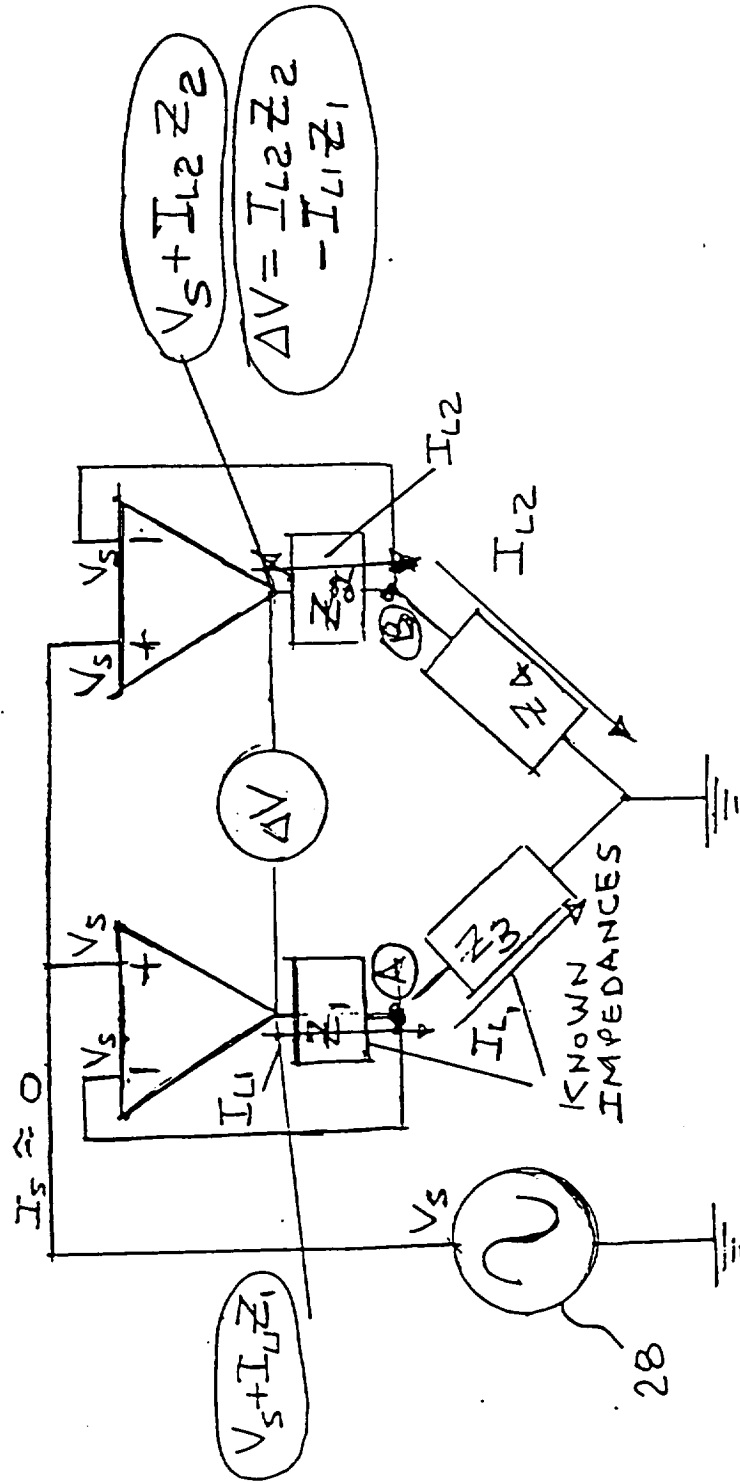


Fig. 5b

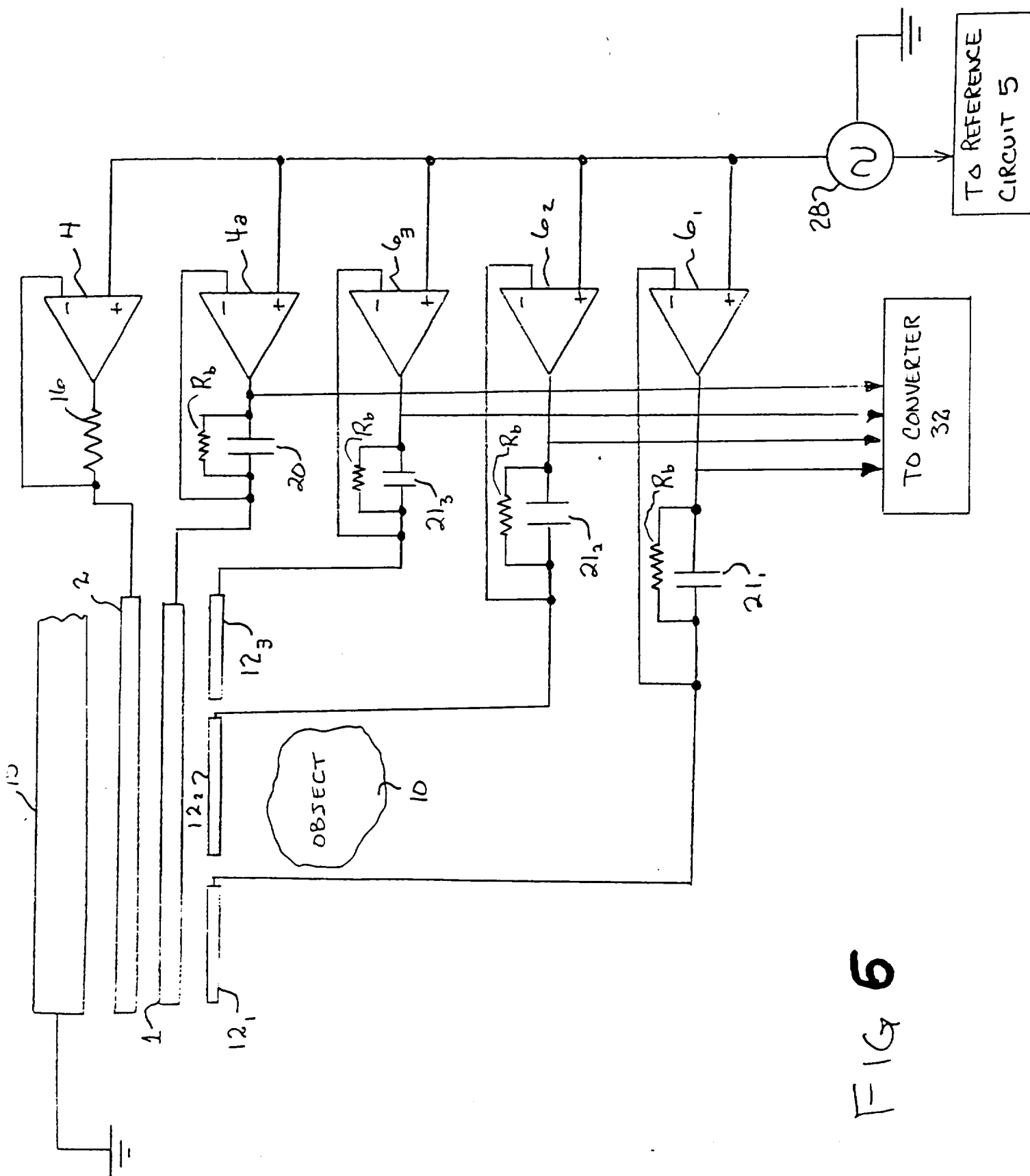


FIG 6

