Using ADA tasks to simulate operating equipment

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A method of simulating equipment using ADA tasks is discussed. Individual units of equipment are coded as concurrently running tasks that monitor and respond to input signals. This technique has been used in a simulation of the space-to-ground Communications and Tracking subsystem of Space Station Freedom.

INTRODUCTION

Many computer simulations¹⁻³ written in procedural languages (e.g., C, FORTRAN, or PASCAL) simulate systems of equipment by tracking signals through the components. Although this may represent a logical solution to the problem, it usually requires that a piece of equipment know not only what it is connected to for input, but also the destination of its output. This is contrary to the way equipment generally operates. For example, an amplifier may have inputs of line voltage, signal level (volts), and load impedance, and control settings of gain and ON/OFF switch position. Its output would include the output signal level, and perhaps some parameter indicating the quality of the output. When the input values change, the values of the output parameters change accordingly. The destination of the output signal is of no concern to the amplifier, and it therefore does not know (or care) what is connected to its output. (Note that we are treating loading as the input parameter "load impedance.")

The ADA language is especially suited to simulating a piece of equipment because of the "task" construct.⁴ Each piece of equipment can be modeled as a concurrent freerunning task that constantly monitors its input values and adjusts the outputs accordingly. As each unit reacts to changes in its input values, one can monitor the signal flow through a collection of components by placing "sensors" at strategic locations. Issues of signal transition delays, and other equipment characteristics can be addressed as need-ed.

I. METHOD

Each unit of equipment is modeled as an ADA task. The individual equipment characteristics are supplied in separate ADA packages (one for each unit of equipment), which include the appropriate transfer function(s) for the input signal(s). All of the values that are external to a piece of equipment are stored in a global database (or "blackboard" data structure⁵), and the individual components link their internal values to these blackboard values. Each component task then monitors those blackboard values that serve as input to the equipment it is simulating, and while the equipment is ON and running, appropriate output values for that equipment are generated and written out to the "blackboard" where they can then be monitored for input by those tasks using them. For example, if all of the equipment is plugged into the same power source, then each monitors the blackboard value of the line voltage. If the line voltage vanishes ("blackout") or is low ("brownout"), then this can be incorporated into the determination of the appropriate output signal(s).

In order to prevent a task from "running" when the equipment is in the OFF state, an algorithm is used that requires that the equipment be turned ON in order for it to process input information. Table I contains the structure of this ON/OFF algorithm, which is coded in a generic ADA package so that it can be implemented ("instantiated" in ADA terminology) for each piece of equipment. One of the features of the algorithm is that it will accept and ignore any ON/OFF requests that are redundant, rather than queue them as might normally be the case with ADA task rendezvous (i.e., if an ON command is sent to a task that is already ON, then the command is discarded). Another feature is that the task of a piece of equipment in the OFF state does no processing, but merely waits for a rendezvous to turn it ON. This minimizes the use of CPU resources.

II. ILLUSTRATIVE EXAMPLE

Consider the equipment depicted in Fig. 1, which consists of a saw-tooth function generator attached to a pulse generator/amplifier. The circled numbers refer to sensor or test points whose values are to be monitored. The waveform produced by the function generator is used by the pulse generator to determine the pulse width as follows: While the value of the input to the pulse generator is at or beyond a certain threshold value (taken as 0.5 V), the value of the pulse generator output is +10.0 V; when the value of the input is below threshold, then the pulse generator output is 0.0 V. We thus have a waveform transformer that converts a saw-tooth signal into a rectangular pulse. The frequency of both active signals is the same, and the pulse width can be varied by changing the amplitude of the saw-tooth signal. In addition, effects of a "brownout" (line voltage less than a nominal value of 120 V) have been incorporated in TABLE I. ADA package with task code for algorithm used to simulate an equipment unit.



that the output levels of each device will decrease until a "minimum operating voltage" for the equipment is reached: Output from the function generator vanishes when the line voltage drops below 90 V, and for line vol-



Main Power Line

FIG. 1. Illustrative Example equipment setup consisting of a (saw-tooth) function generator whose output drives a pulse generator. Circled values indicate sensors or test points.



FIG. 2. Flow diagram of data and command information flowing between the blackboard global database, the ADA equipment simulator tasks, and the main procedure of the Illustrative Example. Straight line (3/M) = data; dashed line (---) = commands.

function

tages below 80 V, the output from the pulse generator also ceases. Figure 2 is a dataflow diagram for this equipment setup.

Table II contains ADA code that implements the above for each piece of equipment and includes the structure of the global database. Table III is a main ADA proce-

TABLE II. ADA code for Illustrative Example equipment, including the Global Database definition and the packages for the function and pulse generators.

ACXAGE GLOBAL_VARIABLES 15	
Power Source Quantities:	
STD_LINE_VOLTAGE	<pre>: constant float := 120.0;</pre>
MAX_LINE_VOLTAGE	: constant float := 130.0;
LINE_VOLTAGE	: float := STD_LINE_VOLTAGE;
Function Generator Signals	and Levels
FUNCTION_GENERATOR AMPLITU	DE : float := 1.0;
FUNCTION_GENERATOR_OUTPUT	: float := 0.0;
Pulse Generator Signals an	d Levels
PULSE GENERATOR AMPLITUDE	: float := 10.0:
PULSE TRIGGER LEVEL	t float in 8.5
PULSE GENERATOR OUTPUT	: float := 0.0:
Connect Pulse Generator	trigger input line to Funct, Gen. Output
PULSE_TRIGGER_LINE	: float renames FUNCTION_GENERATOR_OUTPU
A GLOBAL VARIABLES:	

- Function Generator Simulato	r Last update: 11-#3-89 LAD

-* Package with Procedures fo	r the simulation of a Function Generator *
-* which will generate a Sa	w-tooth waveform of variable amplitude *
-* (nominal value is 1.# V).	Output is degraded if LINE VOLTAGE IN *
-* is less than 120 V ("brow	mout"), and ceases when LINE VOLTAGE IN *
-* is < 80 V ("blackout").	Louis A. DeAcetis *

ith GENERIC_EQUIPMENT;	
with GLOBAL VARIABLES; Glo	bal Database where signal values are stored
•	and signal connections are made
ackage FUNCTION_GENERATOR is	
procedure OFF Values:	
procedure INITIAL ON Values:	
procedure OUTPUT_Value;	
DECKAGE FOULPMENT is new GEN	ERIC EQUIPMENT (
01	Pr_Values, INITIAL_ON_Values, OUTPUT_Value);
LYPE FROBE NAMES IN (UN UFF	The (UDADE NIMES) of float.
PROBE : PROBE_ARRAY_TYPE :	<pre>:= (others => 0.0);Initialize sensors to</pre>
COUNTER : Float := 8 8:	Cycle count
	clare count
and SUNCTION CENERATOR.	
de reaction_semention;	

--Explicitly associate local variables with values in Global Database: LINE VOLTAGE IM : float renames GLOBAL VARIABLES.LINE VOLTAGE; STD_LINE VOLTAGE: float renames GLOBAL_VARIABLES.STD_LINE_VOLTAGE;

AMPLITUDE : float renames GLOBAL_VARIABLES.FUNCTION_GENERATOR_AMPLITUDE; INCREMENT : calender.day_duration; SAW TOOTH_VALUE : float; BASE_TIME : calendar.day_duration := calendar.seconds(calendar.clock); procedure OFF_Values is perin --Set outputs to values for equipment in OFF state: probe(output_SM) := 0.8; OUTPUT - := 0.8; PROBE(OUTPUT_SIGNAL) := OUTPUT; end; : float renames GLOBAL_VARIABLES.FUNCTION_GENERATOR_OUTPUT; OUTPUT procedure INITIAL_ON_Values is begin --Set values for when equipment just turned ON: PROBE(OM_OPF_SM) := 1.0; OUTPUT := 0.0; PROBE(OUTPUT SIGNAL) := 0UTPUT; SAW TOOTH_VALOL: := 0UTPUT; SAW TOOTH_VALOL: := 0 endd:=IntE := calendar.meconds(calendar.clock); endd:=IntE := calendar.meconds(calendar.clock); end; procedure OUTPUT_VALUE is --Set output values for equipment ON and running function WAVE VALUE (AMPLITUDE : float) return float is function SAM_TOOTH return float is begin INCRUME := calendar.seconds(calendar.clock) = BASE_TIME; SAWATTOTH VALUE := float(INCREMENT)/5.0; if SAWATTOTH VALUE := 4.0; BASE_TIME := 4.0; BASE_TIME := calendar.seconds(calender.clock); end if; return SAW TOOTH_VALUE; end SAW_TOOTH; begin return AMPLITUDE*SAW_TOOTH; end WAVE_VALUE; begin COUNTER := COUNTER + 1.0; --Count cycles for monitoring purposes if LINE VOLTAGE IN < 80.0 then --"Blackout" condition OUTPUT := 0.07 OUTOUT := LINE VOLTAGE IN/STD LINE_VOLTAGE* --*Brownout factor* OUTOUT := LINE VOLTAGE IN/STD LINE_VOLTAGE* --*Brownout factor* WAVE_VALUE(ARPLITUDE); end if; PROBE(OUTPUT_SIGNAL) := OUTPUT; end OUTPUT_VALUE; end FUNCTION GENERATOR; -- Pulee Generator Simulator -- Last Update: 11-07-89 LAD Package with Procedures for the simulation of a Pulse Generator which
 generates a pulse of height PULSE GENERATOR AMPLITUDE when the value
 of the TRIGGER INDUT is greater than the value of the TRIGGER LEVEL
 (output is zerö otherwise). Output is degraded if LIME VOLTAGE IN is
 less than 128 v ("brownout"), and ceases when CIME VOLTAGE IN is
 < 98 v ("blackout"). with GENERIC_EQUIPMENT; with GLOBAL_VARIABLES; -- Global Database where signal values are stored -- and signal connections are made package PULSE_GENERATOR is procedure OFF Values; procedure INITIAL_ON_Values; procedure OUTPUT_Value; package EQUIPMENT is new GENERIC_EQUIPMENT(OFF_values,INITIAL_ON_values,OUTPUT_value); type PROBE NAMES is (ON OFF SK, OUTPUT SIGNAL); type PROBE ARRAY TYPE is array(PROBE WAMES) of float; PROBE : FROBE ARRAY TYPE := (others => 0.0); --Initialize sensors to 0 COUNTER : float := 0.0: --Cycle count end PULSE GENERATOR; package body PULSE_GENERATOR is : float renames GLOBAL_VARIABLES.PULSE_GENERATOR_AMPLITUDE; AMPLITUDE TRIGGER_INPUT : float renames GLOBAL_VARIABLES.PULSE TRIGGER_LINE; TRIGGER_LEVEL : float renames GLOBAL_VARIABLES.PULSE_TRIGGER_LEVEL; procedure OUTPUT_VALUE is --Set output values for equipment ON and running begin PROBE(ON_OFF_SW) := 1.0; -- Switch sensor On COUNTER := COUNTER + 1.0; if LINE_VOLTAGE_IN < 99.9 or -- "Blackout" condition check TRIGGER_INPUT < TRIGGER_LEVEL then OUTPUT := 0.0; elae elae OUTPUT := LINE VOLTAGE_IN/STD_LINE_VOLTAGE * --*Brownout factor* AMPLITUDE; AMPLITUDE; end if; PROBE(OUTPUT_SIGNAL) := OUTPUT; end OUTPUT_VALUE; end PULSE_GENERATOR;

TABLE III. ADA code for the procedure SIMULATE which displays the sensor and signal values for the Illustrative Example equipment.

```
Function generator
Pulse generator
        Nain program to exercise equipment:
         and display sensor readings.
  -- Lest Update: 11-02-89 Louis A. DeAcetis
  with GLOBAL VARIABLES;
  with FUNCTION GENERATOR:
  with PULSE GENERATOR;
  with text_io;
use text_io;
  --DOS interface packages for Alsys or Meridian compilers:
--with tty; with video; --Meridian compiler
--with DOS; --Alsys compiler
  procedure SIMULATE is
      package INT_IO is new integer_io(integer);
    use INT_IO;
      package FLOT_IO is new float_io(float);
    use FLOT_IO;
      VALUE : float := 0.0;
PARAM : character := ascii.nul;
       subtype ABSISSA is integer range 5..36;
subtype ORDINATE is integer range 9..23;
      X1, X2 : ABSISSA;
LAST LINE : ORDINATE := ORDINATE'last;
TMARKER : ABSISSA;
      TMARËRR : ABSISSA;
BLANK_LINE : string(ABSISSA'first.,(ABSISSA'lsst+4)) := (others =>' '};
WINDOW_ARRAY : array(ORDINATE) of string(ABSISSA'first.,(ABSISSA'last+4))
: cichter => BLANK_LINE;
      type PROBE_ARRAY_TYPE is array(PROBE_NAME) of float;
      PROBE : PROBE_ARRAY TYPE : (others => 0.0); --Initialize sensors to 9 PREVIOUS PROBE: PROBE_ARRAY_TYPE : (others => 0.0); COUNTER: PROBE_ARRAY_TYPE :-(0.0 others => 0.0);
LOURIER: PROBE_ARRAY_TYPE := (1.0, others > 0.0);

--Create link between local values and Global Values:

LINE VOLTAGE : float renames GLOBAL_VARIABLES.LINE_VOLTAGE;

MAX_EINE VOLTAGE : float renames GLOBAL_VARIABLES.LINE_VOLTAGE;

FUNCTION_GENERATOR_AMPLITUDE : float renames

GLOBAL_VARIABLES.FUNCTION_GENERATOR_AMPLITUDE;

PULSE_GENERATOR_AMPLITUDE : float renames

GLOBAL_VARIABLES.FULSE_GENERATOR_AMPLITUDE;

PULSE_TRIGGER_LEVEL : float renames

GLOBAL_VARIABLES.FULSE_GENERATOR_AMPLITUDE;

PULSE_TRIGGER_LEVEL : float renames

GLOBAL_VARIABLES.FULSE_GENERATOR_AMPLITUDE;

Procedure SET_CURSOR ( X: in integer; --NOTE: Uses ANSI escape

Y: in integer; is -- DRIVER * ANSI.STS

XX,YT integer; BG; -- IN MS-DOS CONFIG.STS file

begin
     if x < 1 then XX := 1;
elsif x > SCREEN_WIDTH then XX := SCREEN_WIDTH;
else XX := X;
end if;
      if Y < 1 then YY := 1;
elaif Y > 24 then YY := 24;
else YY := Y;
end if;
  procedure CLEAR_SCREEN is begin
  text_io.put(ascii.esc & "[2]");
end CLEAR_SCREEN;
      procedure DISPLAY_SENSORS is
begin
          egin
--Display screen labels:
if COUNTER(MAIN POWER_LINE_VOLTAGE) = 1.0 then
SET_CURSOR(2,1);
testio.put("Probe
for 1 in PROBE_NAME'FIRST..PROBE_NAME'LAST loop
SET_CURSOR(2, PROBE_NAME'POS(1)+2);
test_io.put(PROBE_NAME'IMAGE(1));
end loop;
end if;
                                                                                                                                    Cycle Count");
           for I in PROBE_NAME'FIRST..PROBE_NAME'LAST loop
if COUNTER(I) /* 0.0 then
SET CURSOR(40,PROBE_NAME'pos(I)+2); put(integer(COUNTER(I)),5);
end if;
             if not (PROBE(1) = PREVIOUS_PROBE(1)) then
    stT_CURSOR(27, PROBE_NAME'pos(i)+2);
    put(PROBE(1), 3, 2_0);
    PREVIOUS_PROBE(1) := PROBE(1);
    ord if=
            end if;
end loop;
       new line; -- Force output to screen with new_line
end DISPLAY_SENSORS;
    --
procedure PLOT_SIGNALS is
begin
--Sccoll "window" contents:
--Include following if Meridian Compiler:
--uncleo.scroll_up(1, ORDINATE'first-1, ABSISSA'first-1,
--- ORDINATE'lest, ABSISSA'last+2);
---
       -- / Include following if not Meridian Compiler
for IV in ORDIMATE flret+1.LAST_LINE loop
Ser_CUSSOR(ABISSA'firet,IV-1);
er_Lio_put(WINDOW_ARRAY(IV));
WINDOW_ARRAY(IV-1);= WINDOW_ARRAY(IV);
end loop;
     --Blank out last line:
WINDOW_ARRAY(LAST_LINE) := BLANK_LINE;
         WINDOW ARRAY(LAST LINE)(
(ABSISSA'last-ABSISSA'first)/2+ABSISSA'first+1) :• '|';
         X1 := integer(PROBE(F GEN OUTPUT)*float(ABSISSA'last-ABSISSA'first)/
2.9/FUNCTION GENERATOR AMPLITUDE) + ABSISSA'first;
```

TABLE III. (Continued.)

```
X2 := integer(PROBE(P_GEN_OUTPUT)*float(ABSISSA'last-ABSISSA'flrst)/
2.$/PULSE_GENERATOR_AMPLITUDE)+(ABSISSA'last+ABSISSA'flrst)/2-1;
        WINDOW ARRAY (LAST_LINE) (X1) := '+';
WINDOW_ARRAY (LAST_LINE) (X2+3) := '*';
        SET_CURSOR(ABSISSA'first, LAST_LIME);
test_io.put(WINDOW_ARRAY(LAST_LINE));
        new line; -- Force output with new_line
  end PLOT SIGNALS;
--Set up Screen for displays:
CLEAR_SCREEN;
--Draw "box" around data plotting "window"
SET CURSOR(ABSISSA'first-1,ORDINATE'first-2);
     text_lo.put('/');
for I in ABSISSA'first+1..ABSISSA'last+5 loop
text_lo.put('-');
end loop;
text_lo.put('\');
    -Hark trigger level over Function Generator output plot:
TMARKER := integer(PULSE TRIGGER_LEVEL*
float(HSSISSA'last-ABSISSA'first)/2.0/
float(HSSISSA'last-ABSISSA'first)/2.0/
SET_CURSOR(TWARKER,ORD)NATE'irst-2; test_io.put('V');
      for 1 in ORDINATE'first-1..ORDINATE'last loop
     SET_CURSOR(ABSISSA'first-1,1); text_lo.put('|');
SET_CURSOR(ABSISSA'first-1,1); text_lo.put('|');
end loop;
      SET_CURSOR(ABSISSA'first + 1, ORDINATE'first-1);
text_io.put("Punction Gen:");
     -Turn on equipment
      FUNCTION GENERATOR. EQUIPMENT. SWITCH CONTROL.CLOSE SWITCH;
PULSE GENERATOR. EQUIPMENT. SWITCH CONTROL.CLOSE SWITCH;
      delay 0.001;
     DISPLAT_SEMSORS;
OUTER_LOOP: loop
begin --Exception block
100p
     loop -- Is there input from the keyboard?
--exit when DOS.RBD DATA AVAILABLE; --Alsys compiler
--exit when tty.CHAR_READY; --Meridian compiler
           COUNTER(F GEN_ON_OFF_SW) := FUNCTION_GENERATOR.COUNTER;
COUNTER(P_GEN_ON_OFF_SW) := PULSE_GENERATOR.COUNTER;
          Continue (current current current); (current current current current); (current current current current); (current current current); (current current current); (current current 
         if (LINE_VOLTAGE > MAX_LINE_VOLTAGE) and
(PROBE(P_GEN_ON_DFF_SW) + PROBE(P_GEN_ON_DFP_SW) /= 0.0)
           COUNTER (MAIN_POWER_LINE_VOLTAGE) :=
COUNTER (MAIN_POWER_LINE_VOLTAGE)+1.0;
            DISPLAY SENSORS;
PLOT_SIGNALS;
delay 0.25;
        end loop;
           SET_CURSOR(72,17); text_io.put(*
SET_CURSOR(72,17);
get(PARAM); get(VALUE);
                                                                                                                              •);
      --Increment local cycle count:
COUNTER(MAIN_POWER_LINE_VOLTAGE) :=
COUNTER(MAIN_POWER_LINE_VOLTAGE)+1.0;
      --Clear out error message if present from previous input:

SET_CURSOR(45,21); text io.put("");

SET_CURSOR(47,22); text_io.put("");

SET_CURSOR(59,23); text_io.put("");
          -Check input from keyboard for valid command;
case PARAM is
when 'f'|F' =>
if VALUE = 0.0 then
FUNCTION_GENERATOR.EQUIPMENT.SWITCH_CONTROL.OPEN_SWITCH;
                             FUNCTION_GENERATOR.EQUIPMENT.SWITCH_CONTROL.CLOSE_SWITCH;
                  when 'p'|'P' =>
if VALUE = 9.9 then
```

dure, called SIMULATE, which interfaces with the simulator and produces output similar to that in Fig. 3. Although this is a simple application, it does illustrate the method and suggests how greater sophistication is possible.

III. IMPLEMENTATION

Figure 4 is a block diagram of the starboard portion of the proposed Space-to-Ground subsystem of the Communications and Tracking System on Space Station Freedom. As above, the circled numbers represent sensors whose values can be monitored. The oval enclosed numbers represent sensors whose values indicate the ON/OFF state of the equipment. This system has been simulated using the above paradigm: Each rectangular box is represented by a task that, when "ON," monitors the values of its input signals and sets the values of the output values and sensors accordingly. The simulator may be controlled by other programs or using a keyboard interface program that permits the asynchronous entry of commands to turn equipment ON/OFF and set cross-strapping switches. There are upward of



FIG. 3. Sample output of procedure SIMULATE which displays probe readings and signal levels for the Illustrative Example. The outputs of the function and pulse generators are displayed graphically and scroll upward to show the changes with time. The "cycle count" indicates the number of cycles completed by each running task and procedure SIMULATE.



FIG. 4. Block diagram of the starboard space-to-ground subsystem of the Communications and Tracking System of Space Station Freedom. The numbers are sensor identifiers. BSP: baseband signal processor; HDR: high data rate recorder; TDRSS: Tracking and Data Relay Satellite System.

34 tasks for the total system (which includes the port system and contingency communications equipment not shown here). At present, the simulator transforms signal levels and sets sensor readings to typical values. Once the detailed electronic characteristics for this equipment (under development) are established, they can be incorporated into the corresponding tasks.

The simulator has been compiled under a variety of ADA compilers (including Alsys, DEC, Meridian, and Verdix), and runs under MS-DOS on PC's (80286 and 80386 CPU's), and DEC VMS and Ultrix operating systems. Although it is unlikely that all of the communications equipment on the Space Station would be ON simultaneously, the current simulator has been run in that state with no major problems. (As more tasks are switched "ON," they do slow execution somewhat, especially on an IBM PC-AT.)

Additional refinements of the model presented here are possible, most especially in the area of information hiding. One of the major problems with a blackboard approach is that signal information is not only visible to all of the equipment tasks, but accessible (i.e., modifiable) as well. It is thus possible for the Function Generator to get access to the output value of the Pulse Generator even though it has no electrical connection to that output. However, from the code in Table II, it can be seen that one would deliberately have to associate a local variable with an improper global variable in order to accomplish such a connection. There are ADA constructs that can be used to prevent any unwarranted access to normally inaccessible signal levels,⁶ but the level of abstraction and programming complexity required would obscure what is basically a simple concept and implementation, and these were deemed beyond the scope and intent of the present article.

IV. CONCLUSIONS

One of the unique aspects of the ADA programming language is the ability to do logically "parallel" processing using the task construct. This is especially useful in simulating concurrently running equipment. Satisfactory results are readily obtainable for situations where transient states can be ignored (e.g., where we are not concerned with the output of the pulse generator during the transitions between its minimum and maximum voltage states). When the latter are important, timing considerations can greatly increase the complexity of the problem. Real-time simulations, which require timing and interrupt considerations, constitute a further challenge in ADA,⁷⁻¹⁰ which is not considered here.

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Modeling superconducting networks containing Josephson junctions by means of PC-based circuit simulation software

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Software packages are now available with which complex analog electronic circuits can be simulated on desktop computers. Using Micro Cap III it is demonstrated that the modeling capabilities of such software can be extended to include *superconducting* networks by means of an appropriate equivalent circuit for a Josephson junction.

INTRODUCTION

Superconducting circuits, containing Josephson devices, inductances, capacitors, and resistors, have many important practical applications.^{1,2} These include SQUID magnetometers, high-speed superconducting computer elements, and voltage standards. The usual procedure for predicting the behavior of such circuits has been to solve the corresponding sets of nonlinear differential equations numerically. However, as we shall demonstrate, *superconducting electronics* can be included within the modeling capabilities of presently available circuit simulation software, and this provides a powerful and flexible alternative method of analysis.

With the advent of computer-aided engineering (CAE) software, analog circuits can be simulated on a computer before a hardware prototype is constructed. A well-known mainframe oriented software package is SPICE,³ which was developed at UC Berkeley in the 1970s. The appearance of high-performance personal computers based on the 80386, and most recently 80486, chips has made CAE simulation of relatively large circuits feasible on desktop machines. Micro Cap III⁴ (which was selected for the present work) is a leading simulation package for use on PC's. It has an extensive library of standard devices. Each specific component, such as a 2N2222 transistor or an LM741 op-amp, is modeled so as to replicate accurately that device's static and dynamic characteristics. As will be shown, this library can be extended by creating an equivalent circuit for a Josephson junction.

I. JOSEPHSON JUNCTION SIMULATION

The circuit for simulating a current-biased noncapacitive Josephson device is shown in Fig. 1. The principal elements are an operational amplifier (op-amp) and a voltage-controlled oscillator (VCO). MicroCap III does not provide a VCO in its component library, and so a separate macro, described below, was designed for this purpose. The VCO shown schematically in Fig. 2 contains three separate submacros:

(1) SPDT—a voltage-controlled switch set to act as a zero crossing detector; this is formed by combining two voltage-controlled single-pole/single-throw switches provided within MicroCap III.

(2) X—a simple voltage multiplier created from a voltage-controlled voltage source in MicroCap III.

(3) SINECONV—a four-diode triangle-to-sine wave converter⁵ as shown in Fig. 3. This type of sine converter possesses the important attribute of not introducing any phase shift in the waveform.

The input voltage to the VCO is applied to PIN 1, and then is passed to the control terminal of the first SPDT (which enables the VCO to handle both positive and negative input voltages) and to the multiplier. The action of the circuit may be followed by assuming for the moment that $V_{in} > 0$. Suppose the present state of the circuit is as indicated in the schematic. One input to the multiplier is 8 V, the other is V_{in} . The multiplier output is thus $+ 8 \times V_{in}$, and so



FIG. 1. Circuit for simulating a current-biased resistively shunted Josephson junction. The polarity shown for V_b is required for positive equivalent bias current. Note that in this and subsequent figures, Micro Cap III represents V_b as VB, R_b as RB, etc.