INTEGRATED CIRCUIT IMMUNITY

DOD E^3 Program Review
11-14 April 2000

NAS8-98217

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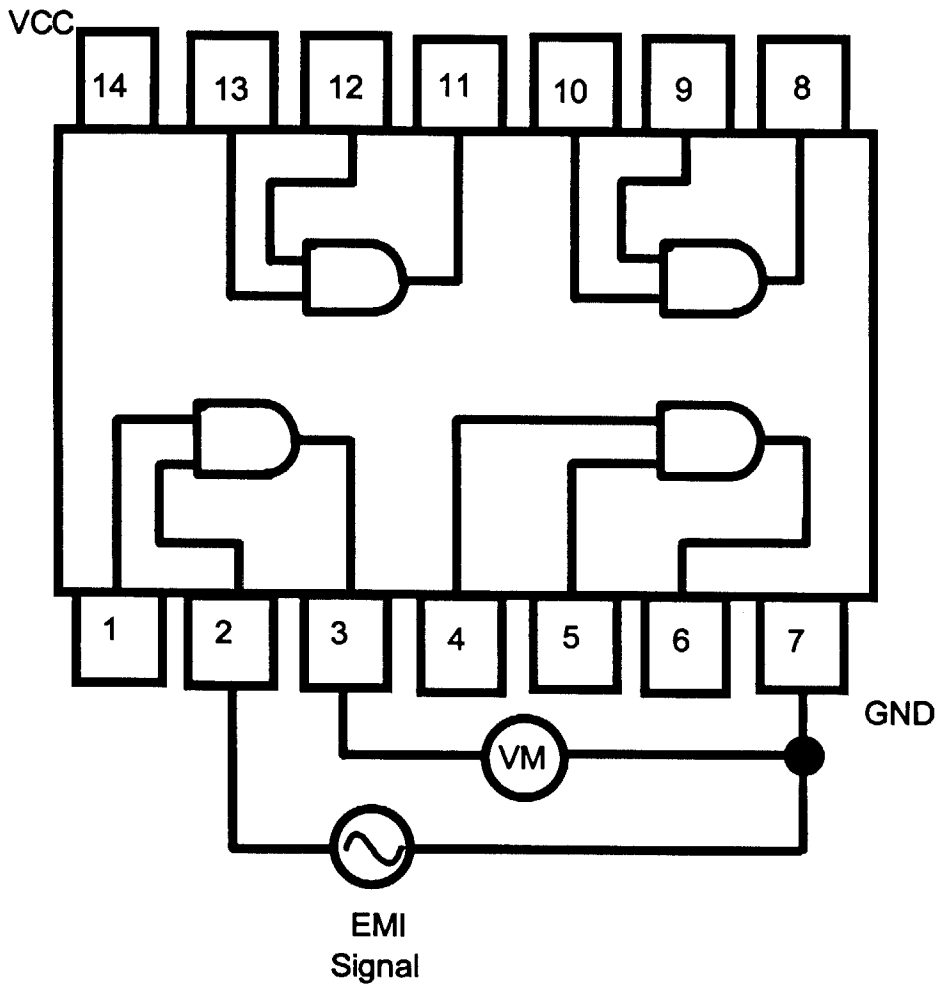
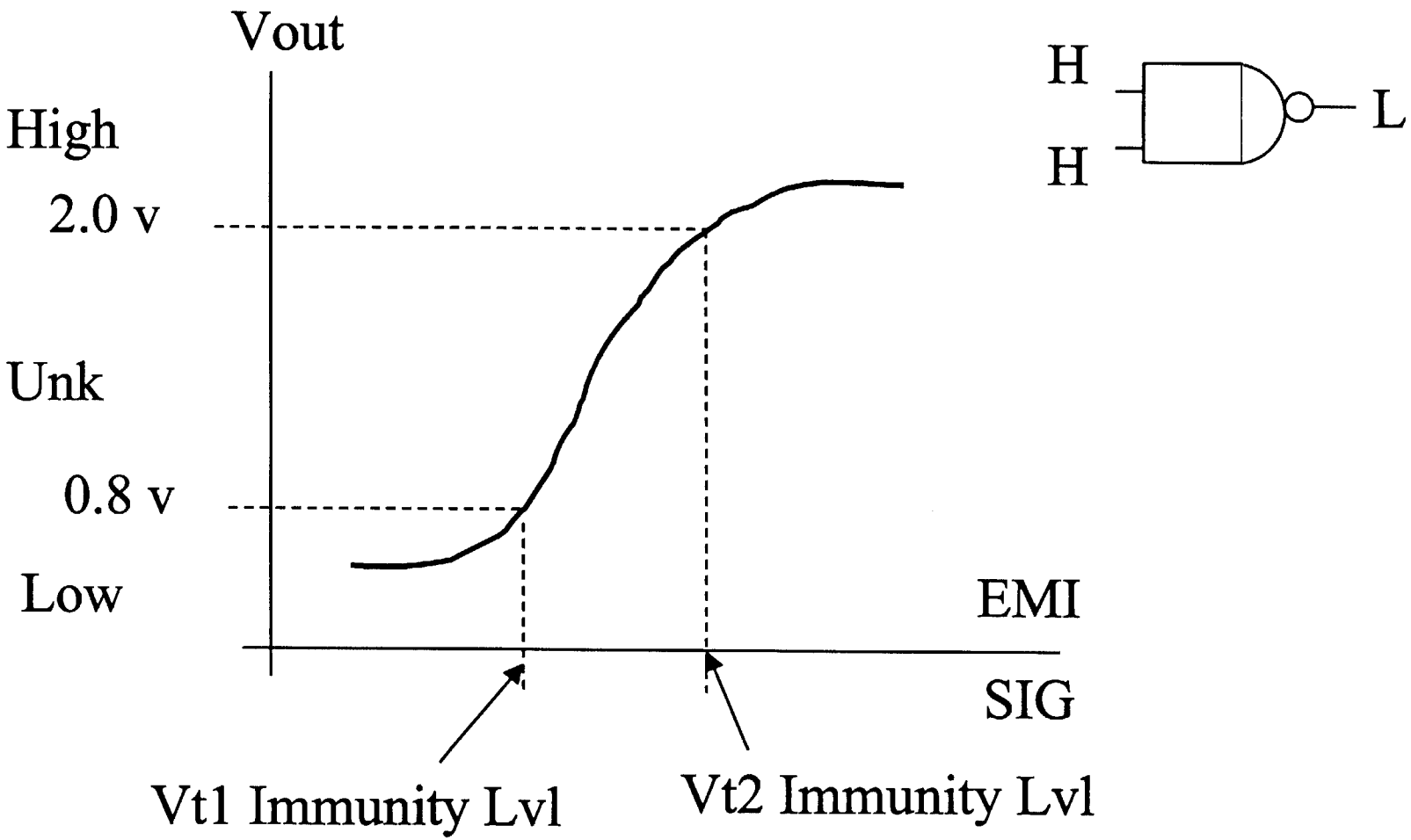
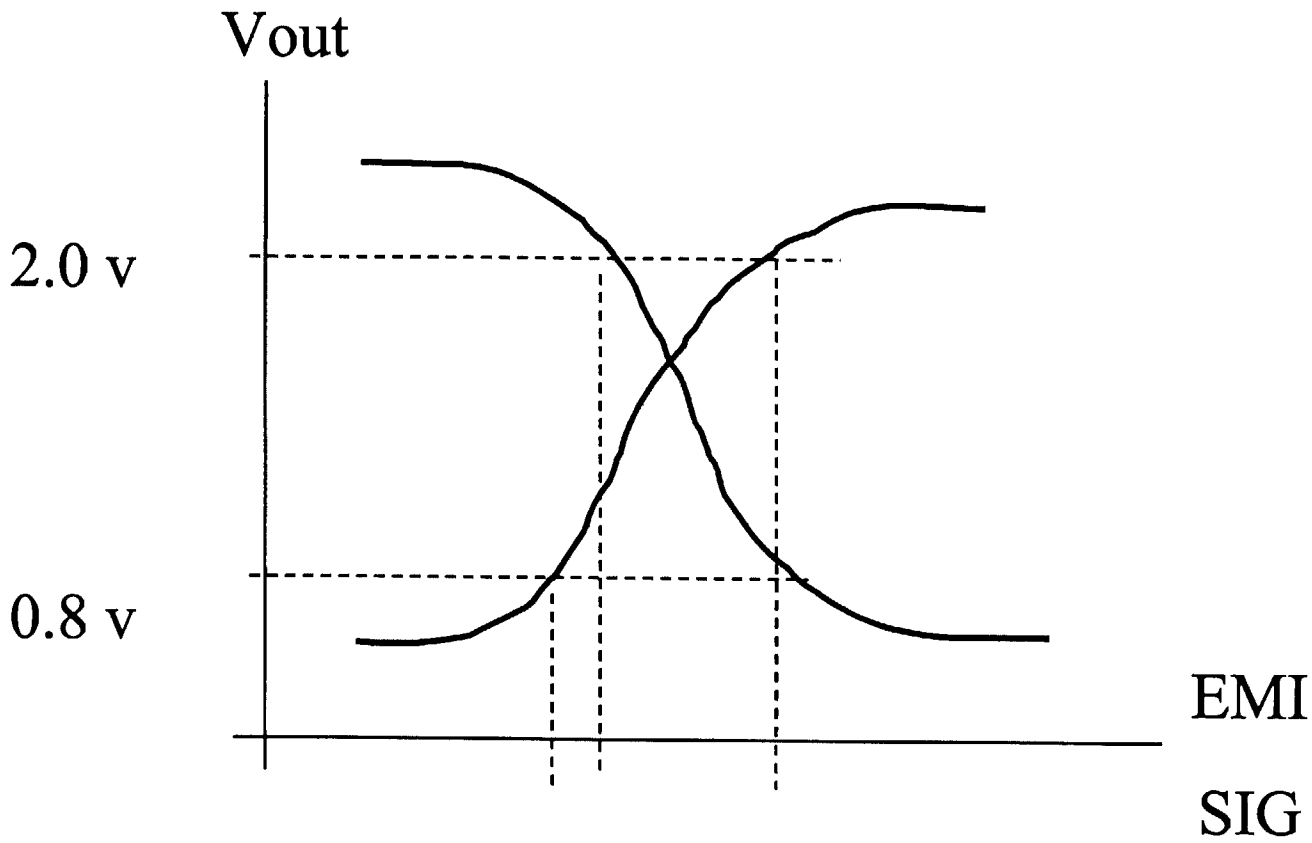
EMI IMMUNITY TESTING



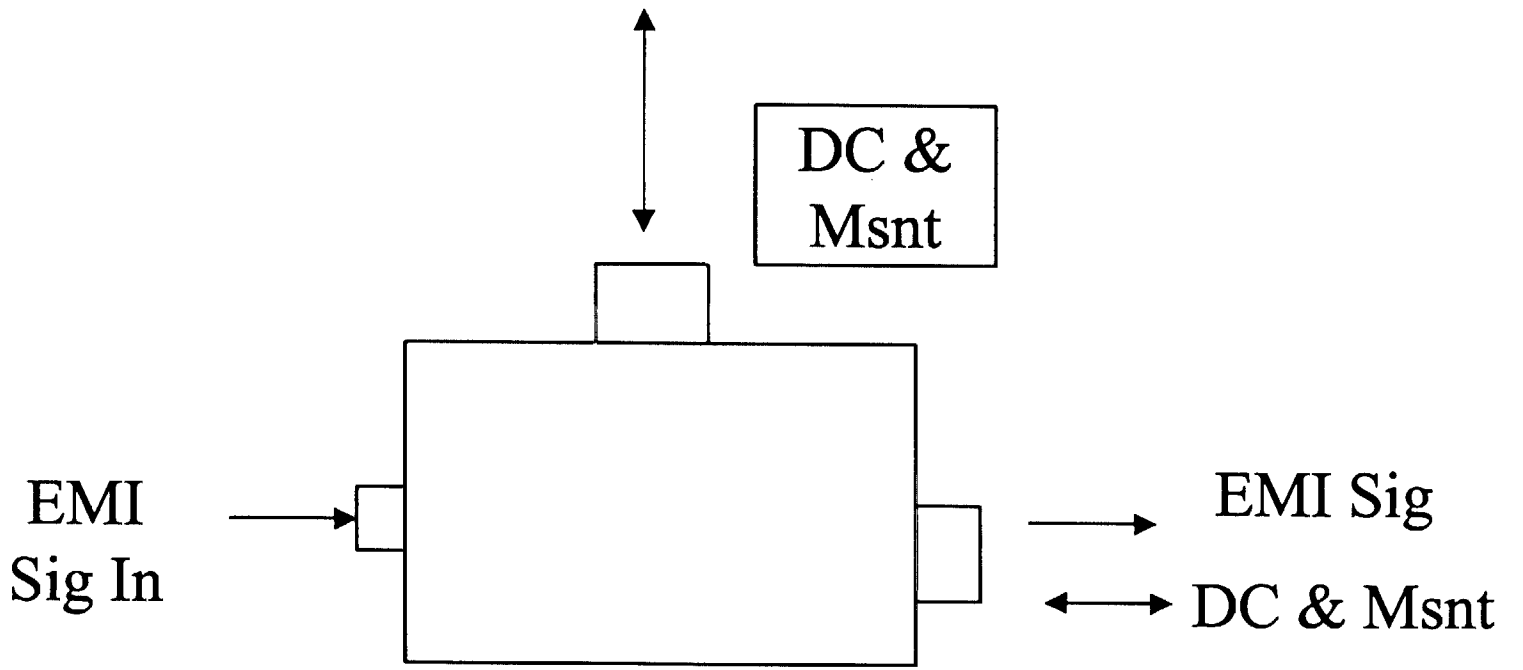
THRESHOLD DEFINITION



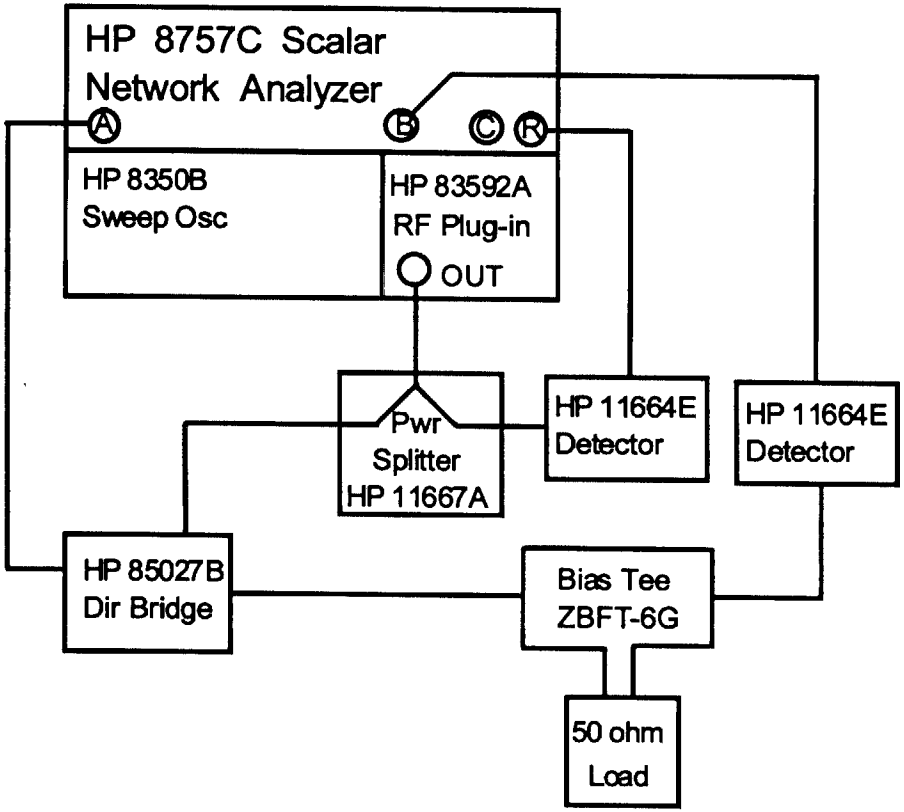
THRESHOLD DEFINITION



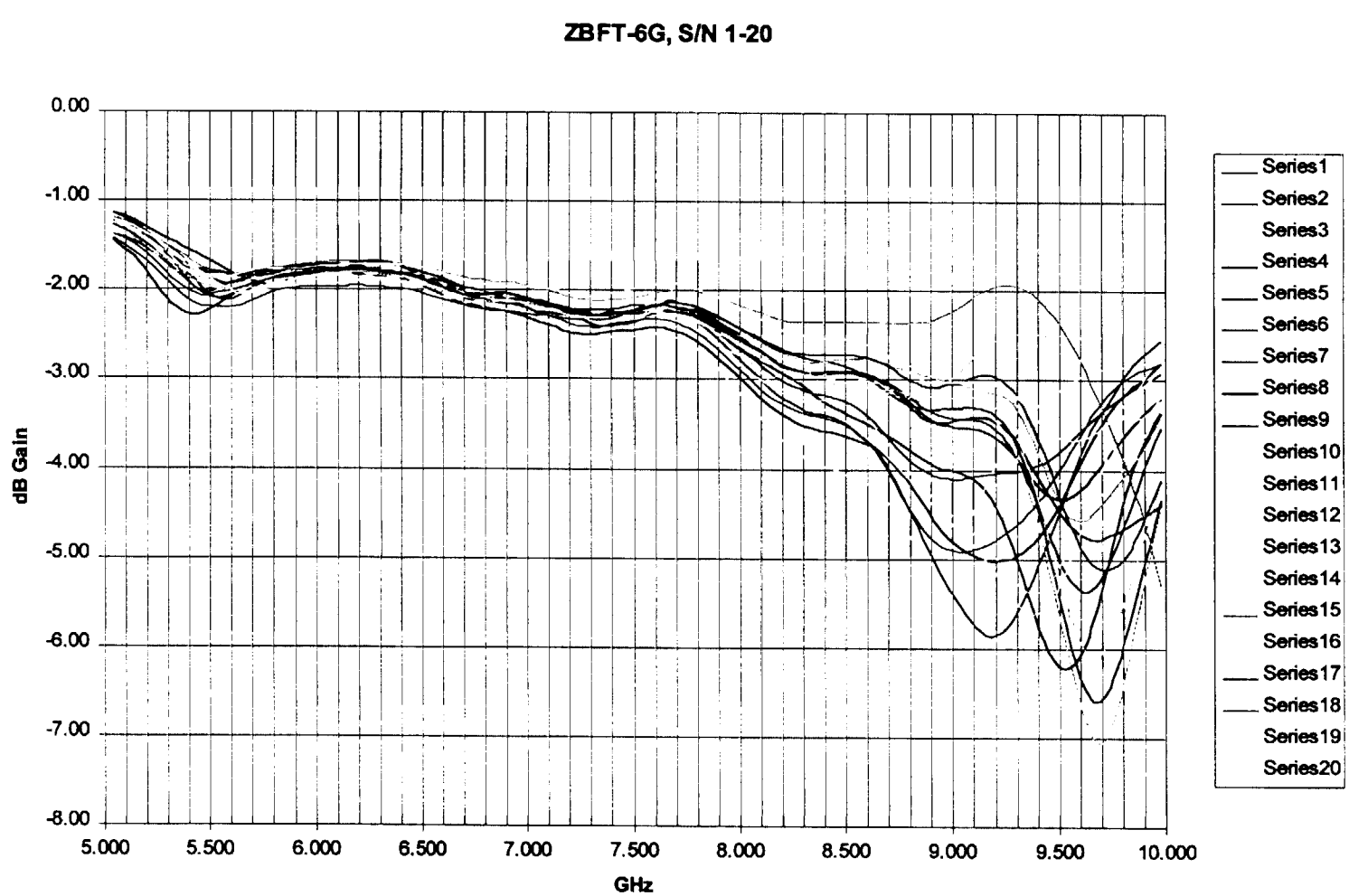
BIAS TEE FUNCTION



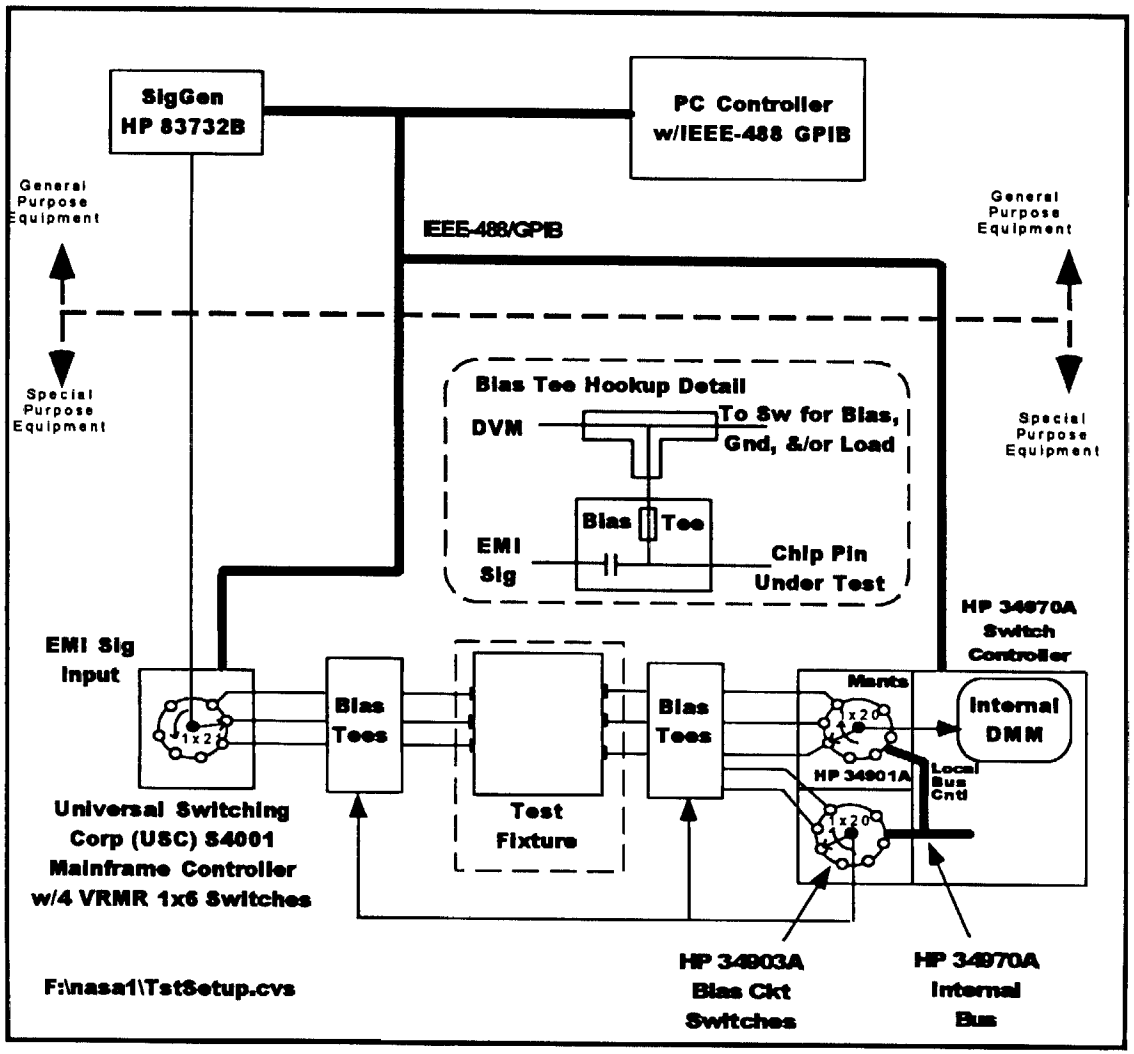
BIAS TEE CALIBRATION SET-UP



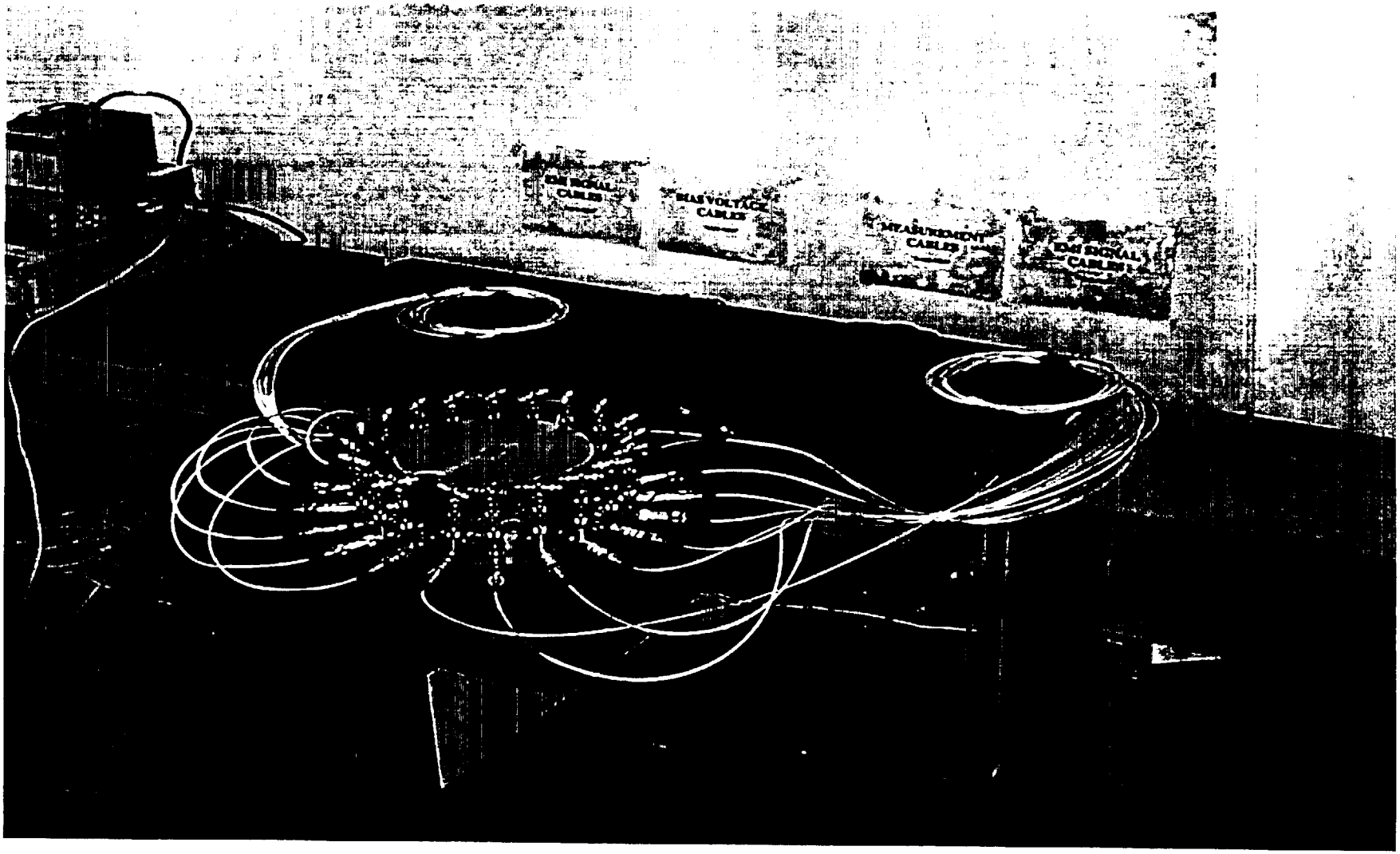
ZBFT-6G, S/N 1-20



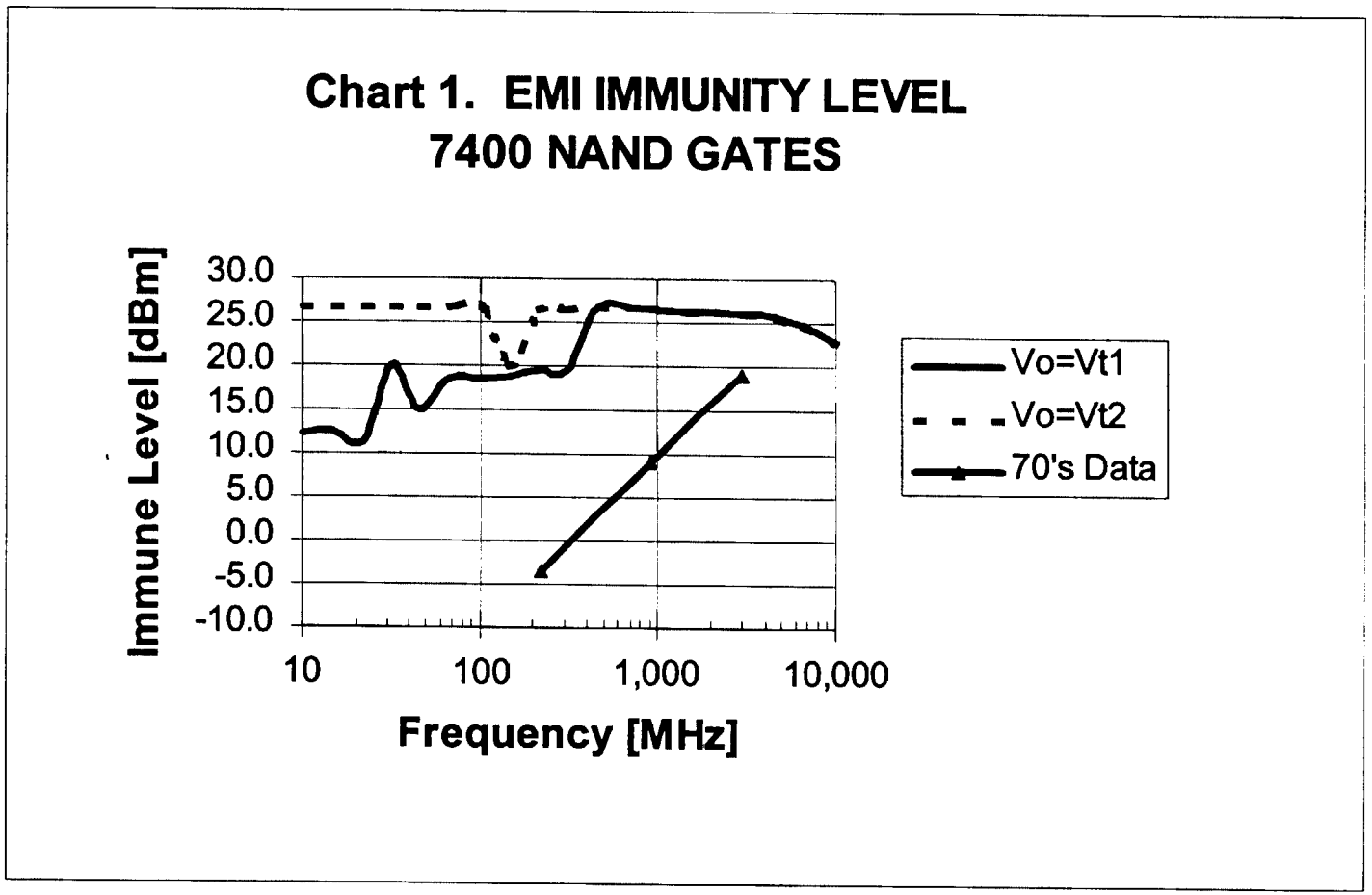
TEST SET-UP


SigGen
HP 83732B
PC Controller
w/IEEE-488 GPIB
General Purpose Equipment
IEEE-488/GPIB
General Purpose Equipment
Special Purpose Equipment
Special Purpose Equipment
Bias Tee Hookup Detail
DVM
To Sw for Bias, Gnd, &/or Load
Bias Tee
EMI Sig
Chip Pin Under Test
HP 34970A Switch Controller
EMI Sig Input
Bias Tees
Test Fixture
Bias Tees
Mants
Internal DMM
HP 34901A
Local Bus Cntl
Universal Switching Corp (USC) S4001 Mainframe Controller w/4 VRMR 1x6 Switches
F:\nasa1\TstSetup.cvs
HP 34903A Bias Ckt Switches
HP 34970A Internal Bus

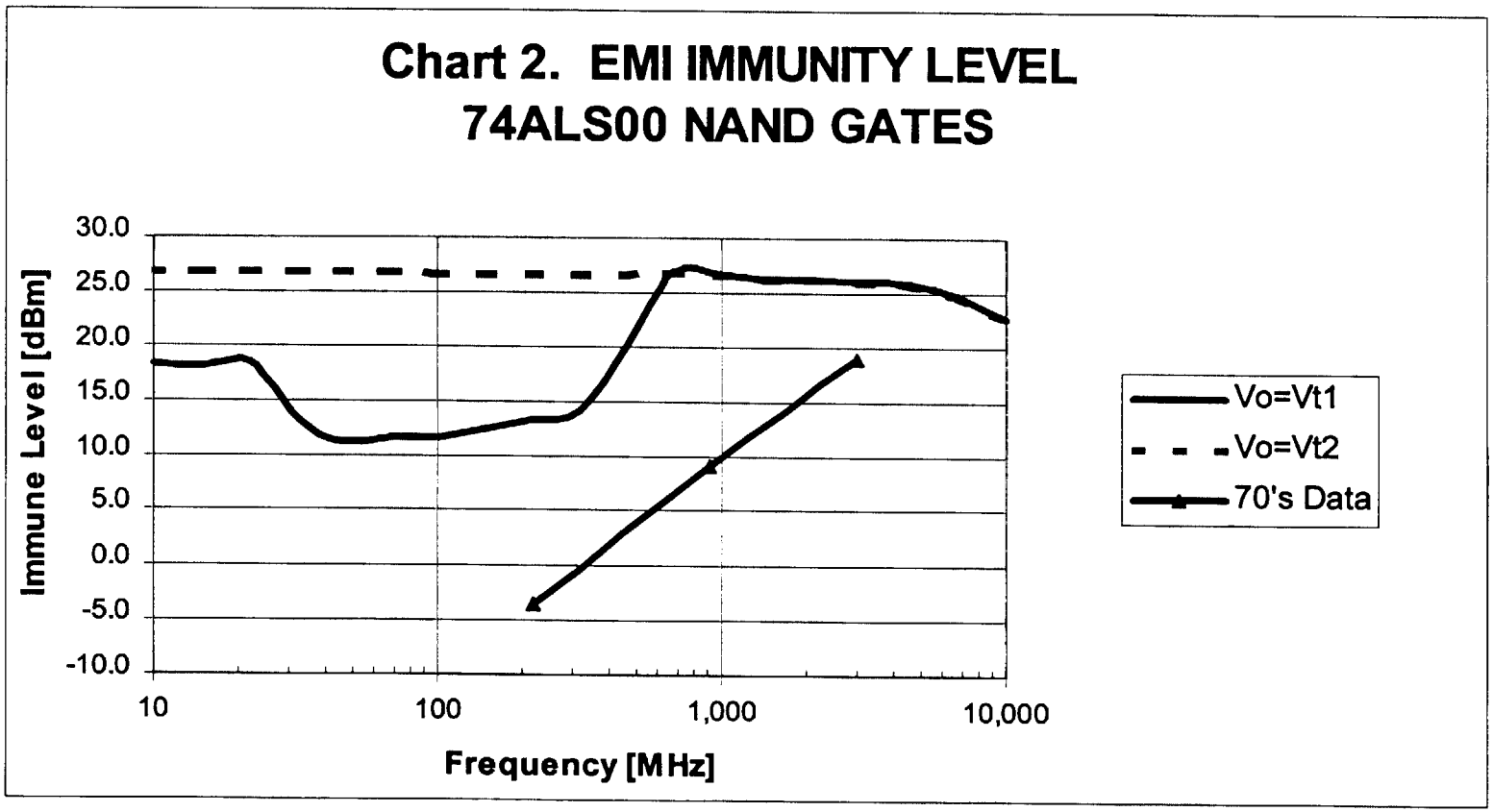
EDM TEST FIXTURE



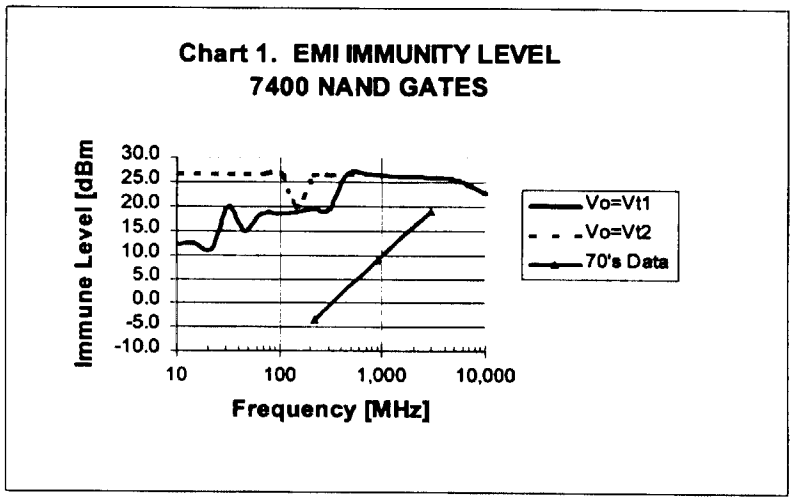
EMI IMMUNITY LEVELS

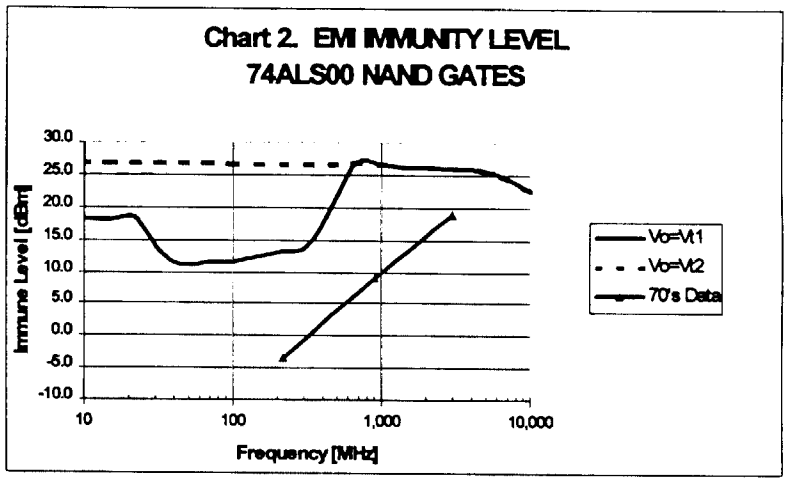


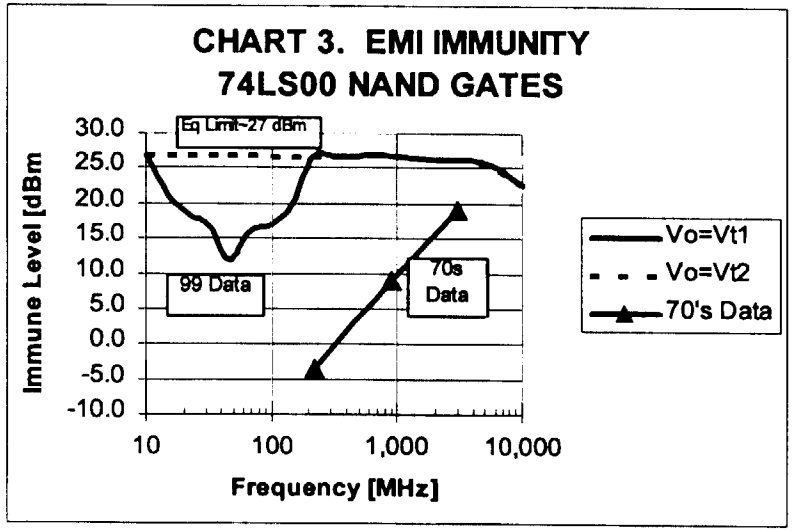
EMI IMMUNITY LEVELS

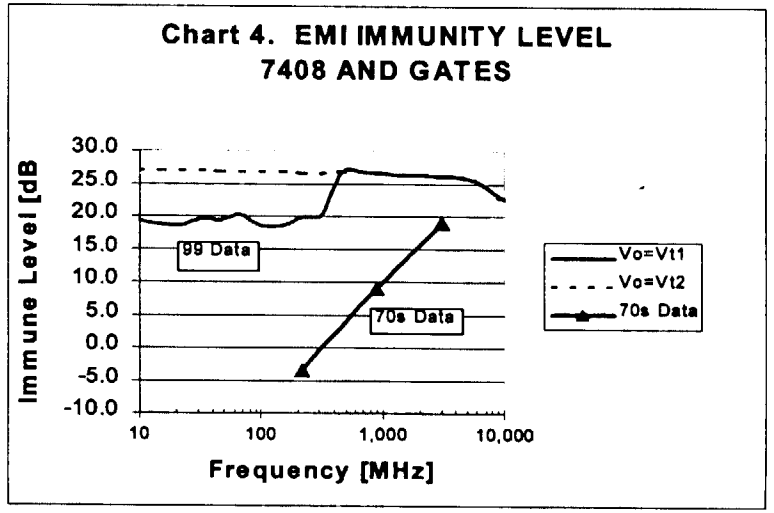


NAND vs. AND GATE IMMUNITY

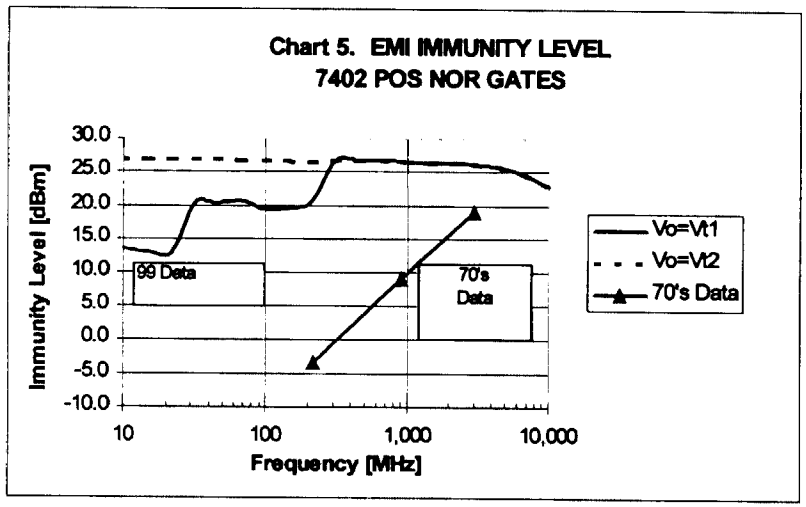


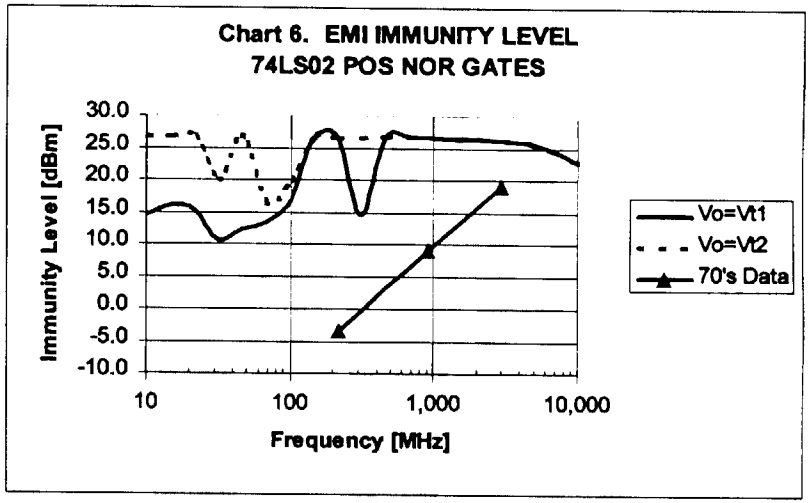


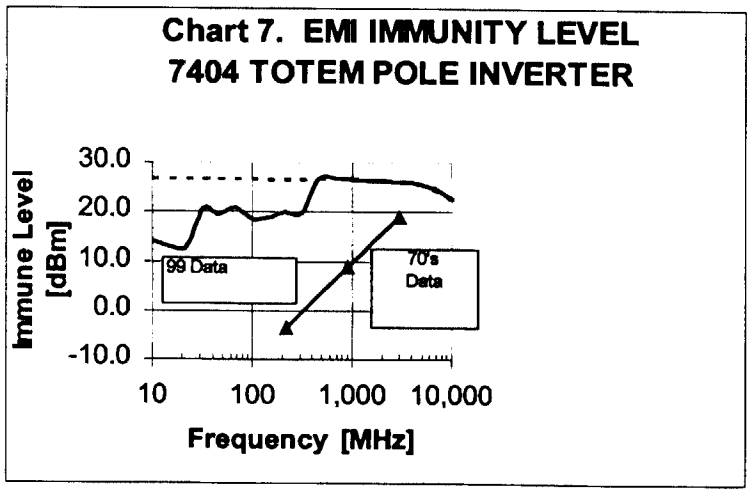


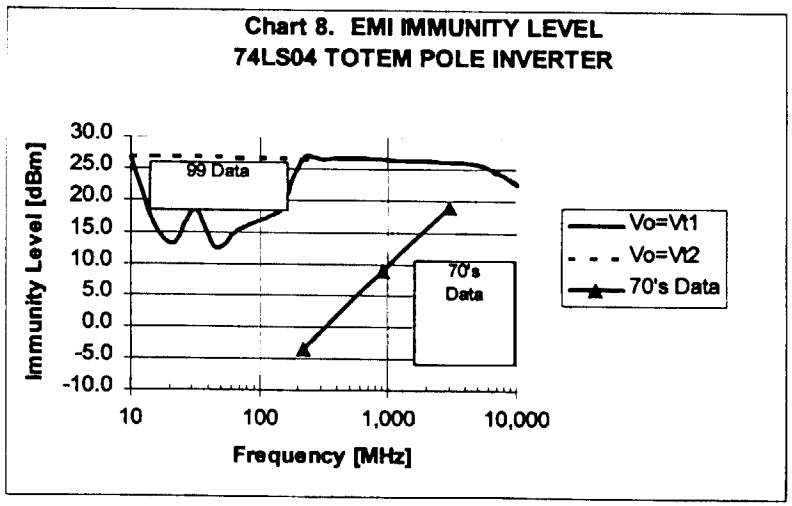


TTL vs. LS IMMUNITY LEVELS

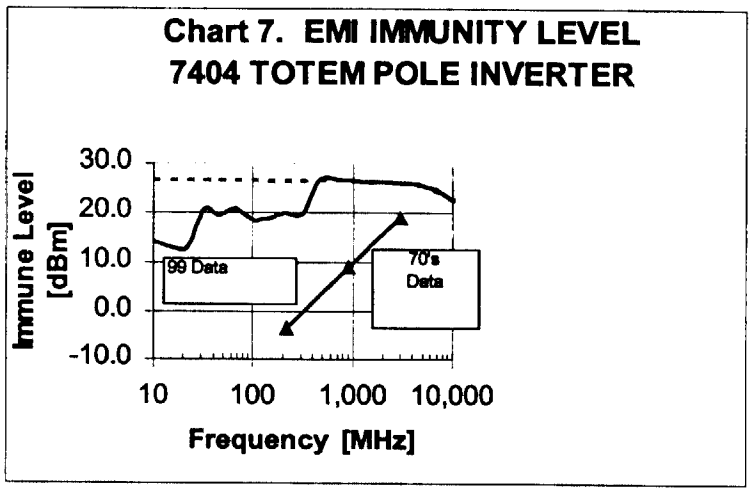


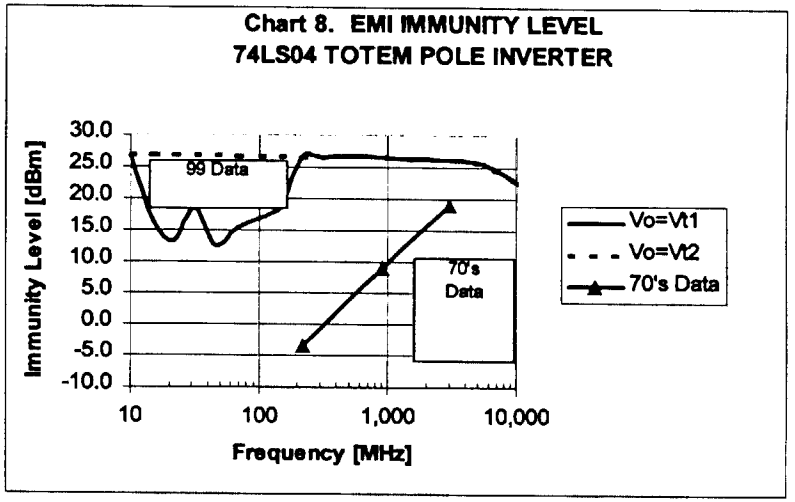


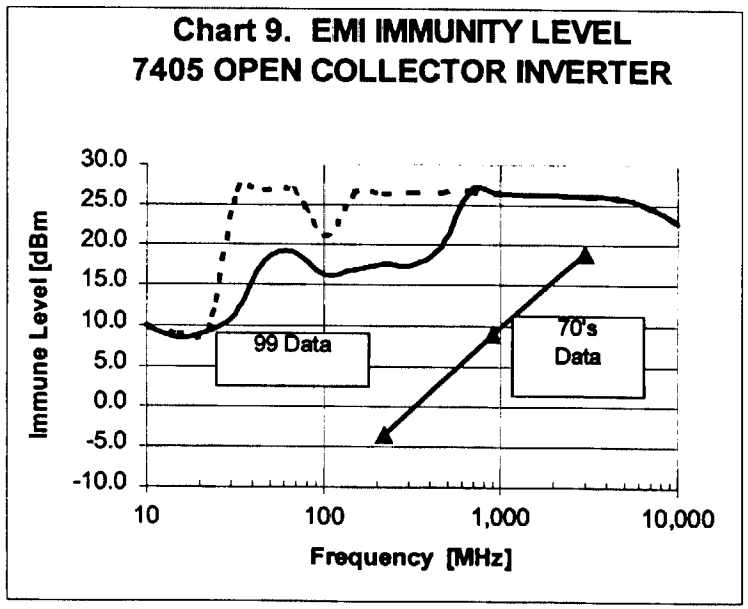


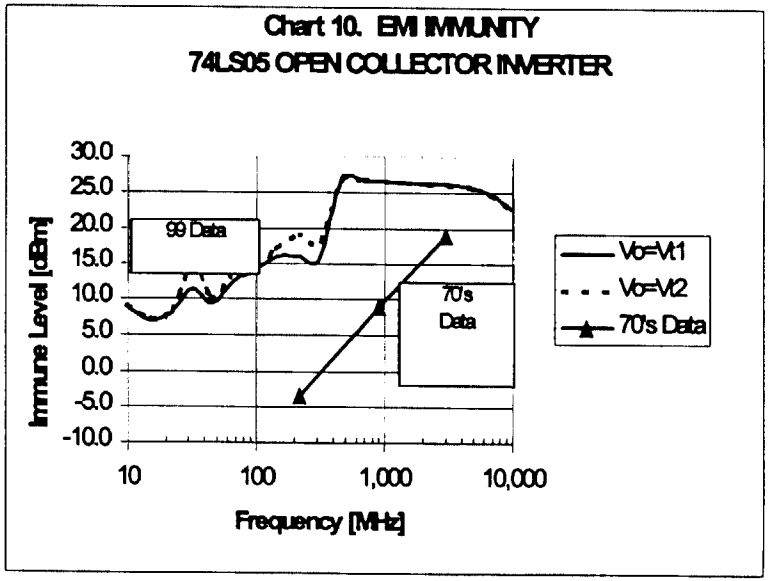


TP vs. OC IMMUNITY LEVELS

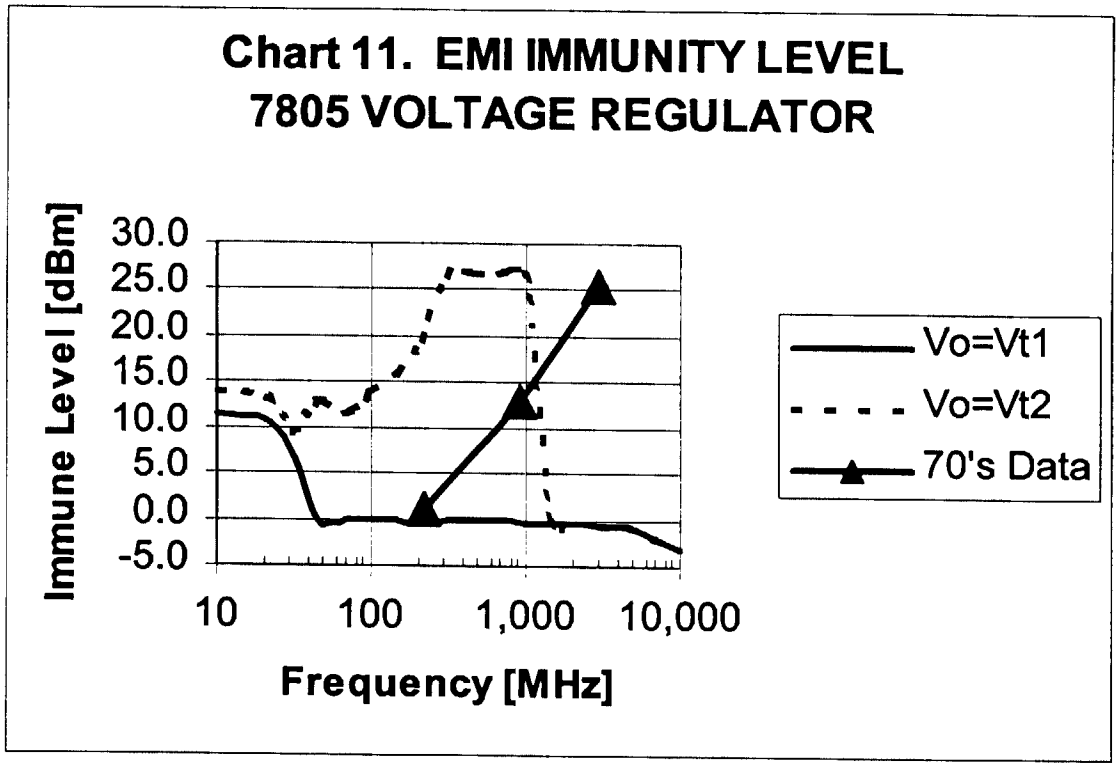








7805 VOLT REG IMMUNITY



SEVENTIES CHIP SET

TTL:	7400, 7402, 7404, 7405, 7408, 7432, 7450, 7473, & 7479	9
CMOS:	4011A, 4011B, 4007A, 4007B, 4001A, & 4013A	6
Line drvr/rcvr:	8830/8820, 9614/9615, 55109/55107A, & 55110	7
Op Amps:	741, 108A, 201A, 0042C, & 531	5
Voltage Reg:	(3-pin) 309, 320, 78M05, (8-pin) 300, & 305	5
Comparators:	306, 311, 339, 360, 710, & 760	6
	TOTAL NUMBER TYPES	**38**