# Some Improvements on Signed Window Algorithms for Scalar Multiplications in Elliptic Curve Cryptosystems 

San C. Vo<br>NASA Ames Research Center<br>Numerical Aerospace Simulation (NAS) Systems Division<br>svo@mail.arc.nasa.gov


#### Abstract

Scalar multiplication is an essential operation in elliptic curve cryptosystems because its implementation determines the speed and the memory storage requirements. This paper discusses some improvements on two popular signed window algorithms for implementing scalar multiplications of an elliptic curve point - Morain-Olivos's algorithm and Koyama-Tsuruoka's algorithm.


## Keywords

Elliptic Curve Cryptosystem, Scalar Multiplication, Double-and-Add Algorithm, Addition-Subtraction Algorithm. NAF, m-ary Algorithm, Sliding Window Algorithm, Signed Window Algorithm, Morain-Olivos's Algorithm, Koyama-Tsuruoka's Algorithm.

Public key cryptography based on elliptic curves defined over Galois fields (i.e. finite fields) was proposed independently by V. Miller and N. Koblitz in 1985. Since then, elliptic curves over finite fields have been used in many applications of cryptography, such as digital signatures (e.g. Standard ANSI X9.62-1999 [1]) and key agreement and transport (e.g., working draft ANSI X9.63-200x [2]).

Scalar multiplication is an essential operation in an elliptic curve cryptosystem because its implementation determines the speed and the storage requirements of elliptic curve cryptosystems. Several algorithms on this operation have been described in cryptography literature. The paper discusses some improvements that were developed for two popular signed window algorithms for implementing scalar multiplications of an elliptic curve point - Morain-Olivos's and Koyama-Tsuruoka's algorithms.

The first three sections provide a review of some basic information on the scalar multiplication operation in an elliptic curve cryptosystem and the signed window algorithms. Section 4 reviews Morian-Olivos's algorithm and shows a generalization of the algorithm for more general patterns of the scalar term involved in the scalar multiplication. Section 5 provides a review of Koyama-Tsuruoka's algorithm and show how to improve its applications for a more general binary expansion of the scalar term in a scalar multiplication operation.

1. Scalar multiplication in elliptic curve cryptosystems.

Given a point $P=(x, y)$ on an elliptic curve $E$ and an integer $k$, the scalar multiplication $k P$ is defined by the recursive addition:

$$
k P=\underbrace{P+P+\cdots+P}_{k \text { terms }} \text {, if } k>0 \text {, and } k P=(-k)(-P) \text {, if } k<0 \text {. }
$$

When $k=0$, then $0 P=O$, the point at infinity. Since the inverse point $-P$ is obtainable at almost no cost, as is shown in the following table, from now on, $k$ may be assumed to be positive.

| $y^{2}=x^{3}+a x+b \quad$ (over $F_{p}$ where prime $\left.p \neq 2,3\right)$ | $-P=(x,-y)$ |
| :--- | :--- | :--- |
| $y^{2}+x y=x^{3}+a x^{2}+b \quad$ (non-supersingular elliptic curve over $\left.F_{2^{m}}\right)$ | $-P=(x, y+x)$ |
| $y^{2}+c y=x^{3}+a x+b \quad$ (supersingular elliptic curve over $\left.F_{2^{m}}\right)$ | $-P=(x, y+c)$ |

Table 1. Additive inverse of an elliptic curve point $P=(x, y)$
Scalar multiplication is the analog to raising an element to the $k$-th power of a number, modulo $n$. The research problem is to compute the elliptic curve point $k P$ with the minimum number of addition operations. The information in Table 1 will be used when dealing with subtraction. The subtraction by a point $P$ is simply addition with its additive inverse point $-P$.

## 2. Some basic algorithms for implementing scalar multiplications

This section gives a brief review of some basic algorithms available in cryptography literature and implementations. More detailed information can be found in the referenced papers.
2.1. The most basic method is the double-and-add method that uses the binary expansion of the integer $k$. Let $k=\left(k_{r-1}, \ldots, k_{o}\right)$ in base 2 , where $r=\left\lfloor\log _{2} k\right\rfloor+1$. Let $P_{r-2}=P$. Compute $P_{i-1}=2 P_{i}+k_{i} P$, for all $i, r-2 \geq i \geq 0$. This produces $k P=P_{-1}=2 P_{0}+k_{0} P$. This method requires $(r-1)$ doublings and, probabilistically, about $(r-1) / 2$ additions. Observe that the number of arithmetic operations can be reduced when the number of bits 0 is increased. This is the basic idea for methods that try to improve on the implementation of scalar multiplications.
2.2. An improved method for implementing scalar multiplication is the additionsubtraction method. For elliptic curve implementations, the methods that include subtractions are more attractive than the corresponding methods that include divisions in calculating powers of an element in finite fields. The reason is that division or inversion in finite fields is a more costly operation than multiplication, while subtraction is just as costly as addition in elliptic curve operations.

An addition-subtraction method is recommended in the Standard ANSI X9.62-1999 [1] and the working draft for ANSI X9.63 [2] that uses the binary expansion of $k$ and $3 k$. Let $l=3 k=\left(l_{r-1}, \ldots, l_{o}\right)$ and $k=\left(k_{r-1}, \ldots, k_{g}\right)$ such that the leftmost bit $l_{r-1}$ must be 1 . Let $P_{r-1}=P$. Compute $P_{i-1}=2 P_{i}+\left(l_{i-1}-k_{i-1}\right) P$, for all $i, r-1 \geq i \geq 2$, resulting in $k P=P_{1}=2 P_{2}+\left(l_{1}-k_{1}\right) P$. This method requires $(r-2)$ doublings and, probabilistically, about $(r-2) / 2$ additions/subtractions.
2.3. Another improved version is called the $m$-ary (or $2^{d}$-ary) method. In this method, the $m$-ary expansion is used instead of the binary form, where $m=2^{d}$, and $d>1$. The
binary expansion of the scalar $k=\left(k_{s-1}, \ldots, k_{o}\right)$ is padded with an extra number of bits 0 to the left side of the bit string, if necessary, to make $s=d r$ for some integer $r$. This produces the $m$-ary expansion of $k$ of the form $k=\left(K_{r-1}, \ldots, K_{o}\right)$, where each $K_{i}$ is a $d$-bit string, and $K_{r-1} \neq(0 \cdots 0)$. Pre-compute all points $2 P, 3 P, \ldots,\left(2^{d}-1\right) P$. These points will include all possible points of the form $K_{i} P$. The algorithm is similar to the basic binary algorithm. This method requires $\left(2^{d}-2\right)$ pre-computations (and hence memory storage), $(r-1) d=s-d \quad$ doublings and, probabilistically, about $(r-1)\left(1-2^{-d}\right)$ additions. Observe that the larger $d$ is, the more pre-computations are needed. With a little calculus, the optimal value of $d$ can be found that will minimize the total number of additions.
2.4. Another algorithm is the addition-subtraction method using non-adjacent form (NAF). The canonical non-adjacent form of the scalar $k$ employs a signed binary expansion (using 0 and $\pm 1$ ) that has the property that no two consecutive coefficients are nonzero. The NAF of an integer is unique and has the fewest nonzero digits of any signed binary expansions. There are a few ways to construct the NAF. The additionsubtraction method requires $(r-1)$ doublings and, probabilistically, about $(r-1) / 3$ additions.

It is possible that the addition-subtraction method can be combined with the $2^{d}$-ary method. In particular, the addition-subtraction method using the $2^{d}$-ary NAF form is a binary form with the property that there is at most one non-zero term in $d$ consecutive coefficients. This form always uniquely exists and is easy to compute. The computing method is similar to that for the NAF form, except that the corresponding quotient corresponding to the non-zero remainder must be divisible by $2^{d-1}$. The pre-computation process must store the values of all points: $\pm P, \pm 3 P, \ldots, \pm\left(2^{d-1}-1\right) P$.

## 3. Signed window algorithms

More advanced methods for implementing scalar multiplications are referred to as window methods. The basic one is called the Sliding window method. (Refer to [6]). This method's purpose is to separate zero words so that an addition in the $m$-ary method discussed above can be skipped. (A zero word is defined as a bit string of only 0 's.)

Instead of decomposing $k=\left(k_{s-1}, \ldots, k_{o}\right)$ into $d$-bit length words of fixed $d$, decompose $k$ into zero and nonzero words, or windows $W_{i}$ of varying lengths $l_{i}$. Let $d$ be the maximum length of all nonzero windows. Then pre-compute only the points of odd scalar multiples: $3 P, 5 P, \ldots,\left(2^{d}-1\right) P$. Let $k=\left(W_{r-1}, \ldots, W_{o}\right)$, where $W_{r-1}$ is a non-zero window (or window number). There are two strategies to partition a binary expansion into windows: constant length nonzero windows and variable length nonzero windows.

- Constant length nonzero windows: This strategy tries to produce zero windows of arbitrary length and nonzero windows of a fixed length $d$. A nonzero window will start when a bit 1 is encountered as the bits are scanned from the rightmost bit to the leftmost bit.
- Variable length nonzero windows: This strategy tries to produce nonzero windows whose right-end and left-end bits are both 1 . Two parameters are to be chosen optimally in order to decrease the average number of nonzero windows:
the maximum length $d$ of nonzero windows and the maximum number $r$ of adjacent 0 's allowed inside any nonzero window.
Signed binary window algorithms are more advanced. Their strategy is to transform a binary expansion into a signed binary expansion. The algorithms using "signed" expansion are much more efficient in implementing elliptic curve operations than in implementing the modular exponentiation operations, since subtraction is just as costly as addition. The purpose of these algorithms is to skip a bit string of all 1's to reduce the number of additions. Two popular algorithms mentioned in cryptography literature are reviewed in the following sections - Morain-Olivos's and Koyama-Tsuruoka's algorithms. Improvements are developed for these two algorithms.


## 4. Morain-Olivos's algorithm and its improvement

### 4.1. Basic technique (Refer to [8])

This algorithm reduces the weight of the signed binary expansion, i.e., the number of non-zero digits in the expansion. The idea is that a block of $n$ bits 1 can be replaced by a bit string that is a block consisting of a bit 1 followed by $n$ bits 0 and then minus 1 . That is, $\underbrace{11 \cdots 1}_{n \text { bits }}=\underbrace{100 \cdots 0}_{n \text { bits }}-1$. This observation is extracted from an arithmetic equality, that can be easily verified: $2^{n+1}-1=\left(2^{n}+2^{n-1}+\cdots+2^{1}+2^{0}\right)$.

As a result, $(n-1)$ doublings and ( $n-1$ ) additions (i.e., $2(n-1)$ total additions) can be replaced by $n$ doublings and 1 subtraction (i.e., $(n+1)$ total additions). In other words, this method tries to construct two positive integers $k_{+}$and $k_{-}$such that $k_{+}-k_{-}=k$. The total computation for $\left(k_{+}-k_{-}\right) P$ is less than that for $k P$. Note that there are not two separate computations of $k_{+} P$ and $k_{-} P$, but actually the computations merge together: $k_{-} P$ only shows up in a few subtractions corresponding to the positions of its bits 1 in the scalar $k$.
Example: $183_{10}=\underline{1} \underline{011} \underline{0111_{2}}=\underline{1} \underline{100} \underline{1000}-\underline{0} \underline{001} \underline{0001}=k_{+}-k_{-}=\underline{1} \underline{101} \underline{100 \overline{1}}$. Then by a binary method, $183 P=2^{3}\left(2\left[2^{2}(2 P+P)-P\right]+P\right)-P$, where the two subtractions correspond exactly to the two bits 1 in the expansion of $k_{-}=00010001$, or two bits $\overline{1}$. Using a sliding window method, the following can be written:

$$
183_{10}=\underline{10110111_{2}}=\underline{11001000}-\underline{00010001}=\underline{110011001} \text { and } 183 P=2^{4}(12 P-P)+8 P-P .
$$

The same idea can deal with a special string that has isolated 0 's. Observe that: $k=\underbrace{1 \cdots 1}_{n \text { bits }} \underbrace{1 \cdots 1}_{m \text { bits }}=1 \underbrace{0 \cdots 0}_{n \text { bits }}-\underbrace{0 \cdots 0}_{(m-1) \text { bits }} \overline{1}=1 \underbrace{0 \cdots \cdots 0}_{n+m+1 \text { bits }}-0 \underbrace{0 \cdots 0}_{n \text { bits }} 1 \underbrace{0 \cdots \cdots 0}_{(m-1) \text { bits }}$, where $\overline{1}$ denotes -1 , and $m$ is assumed to be greater than or equal to 2 . This observation is extracted from the same equality described above and applied twice. Then the following formula can be obtained:

$$
k P=2^{m}\left(2^{n+1} P-P\right)-P
$$

That is, an isolated 0 inside a block of bits 1 will contribute only two subtractions/additions and one extra doubling, instead of ( $n+m-1$ ) additions.

Refer to [8] for detailed estimations of the implementation cost. In summary, the algorithm reduces the number of operations (additions and doublings) by about $3 \%$ for

100 -digit numbers and by $2.7 \%$ for 300 -digit numbers. Refer to the work of [4] for similar approaches.

### 4.2. Improvement: A generalization of Morain-Olivos's algorithm

In this section, I will generalize the Morain-Olivos's algorithm to show that it can be applied in additional cases of scalar multiplication operations. The above approach applies only to the case where the scalar $k=\underbrace{1 \cdots 1}_{n \text { bits }} \underbrace{1 \cdots 1}_{m \text { bis }}$. I will show that a more general pattern of $k$ is also applicable.

Indeed, for $k$ being a string of $(b-1)$ isolated bits 0 sandwiched among $b$ blocks of bits 1 , where $b$ is larger than 2 , the following can be written:

$$
k=\underbrace{1 \cdots \cdots 1}_{N_{1} \text { bits }} 0 \cdots \underbrace{1 \cdots \cdots \cdot 1}_{N_{b-b} \text { bits }} \underbrace{1 \cdots \cdots 1}_{N_{b} \text { bits }}=1 \underbrace{0 \cdots \cdots \cdots \cdots \cdots 0}_{N_{1}+\cdots+N_{b}+(b-1) \text { bits }}-0 \underbrace{0 \cdots 0}_{N_{1} \text { bis }} 1 \cdots \underbrace{0 \cdots \cdots 0}_{N_{b-1} \text { bits }} 1 \underbrace{0 \cdots \cdots 0}_{\left(N_{b}-1\right) \text { biis }} 1 \text {, }
$$

assuming $N_{b} \geq 2$. This observation is also derived from the above equality: $2^{n+1}-1=\left(2^{n}+2^{n-1}+\cdots+2^{1}+2^{0}\right)$. Then the following formula can be obtained:

$$
k P=2^{N_{b}}\left(2^{N_{b-1}+1}\left[\cdots\left(2^{N_{1}+1} P-P\right) \cdots\right]-P\right)-P
$$

This formula generalized dramatically the application of Morain-Olivos's algorithm for the case where $b=2$ only to the case where $b$ is any positive number that is greater than 2.

By applying this algorithm, $\left(N_{1}+\cdots+N_{b}-1\right)$ additions were replaced by only $b$ subtractions/additions and one extra doubling. The savings in the number of arithmetic operations are significant when the sum $\left(N_{1}+\cdots+N_{b}\right)$ is much larger than $b$, which should be obtainable for those cases of $k$.
Example: $183_{10}=\underline{1} 0 \underline{11} 0 \underline{111_{2}}=100000000-0 \underline{0} \underline{1001} \underline{001}=10 \overline{100} \overline{1} 00 \overline{1}$. Then

$$
183 P=2^{3}\left[2^{3}\left(2^{2} P-P\right)-P\right]-P
$$

$1467_{10}=\underline{10110} \underline{1110} \underline{1}_{2}=100000000000-0 \underline{0} \underline{000} \underline{1000} \underline{1} \underline{1}=10 \overline{1} 00 \overline{1} 000 \overline{1} 0 \overline{1}$. Then

$$
1467 P=2^{2}\left(2^{4}\left[2^{3}\left(2^{2} P-P\right)-P\right]-P\right)-P
$$

This improvement expands the domain of applicable patterns in the binary expansion of the scalar $k$ in the Morain-Olivos's algorithm. This generalized algorithm can be improved dramatically if the direct multiplication formulae (refer to [3]) can be employed to calculate the points $2^{d} P$, where $d$ is up to the maximum number $N_{i}$ needed in the above formula for $k P$.

## 5. Koyama-Tsuruoka's algorithm and its improvement

### 5.1. Basic technique (Refer to [7])

The Koyama-Tsuruoka's algorithm attempts to increase the average length of zeros and decrease the weight in the signed binary expansion using $\{\overline{1}, 0,1\}$, where $\overline{1}$ denotes -1 . Denote the number of bits 0 (and, respectively, bits 1 ) in a bit string $B$ by $\#_{0}(B)$ (and, respectively, $\#_{1}(B)$ ).

A binary string of a non-zero window $B=\left(1, b_{n}, \ldots, b_{1}, 1\right)$ in $k$ will be transformed to a signed binary string of the form $T=\left(1,0, t_{n}, \ldots, t_{1}, \overline{1}\right)$, where $t_{i}=b_{i}-1$, for all $1 \leq i \leq n$.

This transformation is effective (i.e. actually decreases the weight of the bit string) only when the difference between the numbers of bits 1 and bits 0 in $B$ is: $\operatorname{Diff}(B)=\#_{1}(B)-\#_{0}(B)>2$. However, keep in mind that the transformation also costs one extra doubling because of the extra bit. This transformation is extracted from an arithmetic equality that is also easily verified:

$$
2^{n+2}-2^{n+1}=2^{n+1}=\left(2^{n}+2^{n-1}+\cdots+2^{1}+1+1\right)
$$

Using the relationship $1=b_{i}-t_{i}$, for all $1 \leq i \leq n$, then

$$
2^{n+2}+\left(t_{n} 2^{n}+t_{n-1} 2^{n-1}+\cdots+t_{1} 2^{1}+1\right)=2^{n+1}+\left(b_{n} 2^{n}+b_{n-1} 2^{n-1}+\cdots+b_{1} 2^{1}+1\right)
$$

Using this method, one needs to pre-compute only $\pm 3 P, \pm 5 P, \ldots, \pm\left(2^{d}-3\right) P$, since the algorithm never allows terms of the form $\pm\left(2^{d}-1\right) P$ to appear.
Example: $183_{10}=\underline{1} 0 \underline{11} 0 \underline{111_{2}}=\underline{1} 0 \underline{11} 100 \overline{1}$. Then $183 P=16(11 P)+7 P$.
$743_{10}=\underline{101111} \underline{00111_{2}}=\underline{101001} \underline{0100-1}$. Then by a sliding window method with $d=5$,

$$
743 P=2^{5}\left(2^{5} P-9 P\right)+7 P
$$

A similar idea to Koyama-Tsuruoka's algorithm was also discussed in [5]. Refer to [7] for a detailed performance evaluation and comparison.

### 5.2. An Improvement of Koyama-Tsuruoka's algorithm

The Koyama-Tsuruoka's algorithm is applied for the non-zero window ( $1, b_{n}, \ldots, b_{1}, 1$ ) only. This limits its application. In this section, I will show how to significantly improve this algorithm for any binary expansion.

Instead of working only on a non-zero window, consider an arbitrary bit string $k=\left(b_{n}, \ldots, b_{1}, b_{0}\right)$, where, without loss of generality, $b_{n}=1$ can be assumed. For all $0 \leq i \leq$ $n$, let $t_{i}=b_{i}-1$, then insert the relation $1=t_{i}+b_{i}$ into the arithmetic identity: $2^{n+1}=\left(2^{n}+2^{n-1}+\cdots+2^{1}+2^{0}\right)+1$, to obtain the following identity:

$$
2^{n+1}+\left(t_{n} 2^{n}+t_{n-1} 2^{n-1}+\cdots+t_{1} 2^{1}\right)+t_{0} 2^{0}-1=\left(b_{n} 2^{n}+b_{n-1} 2^{n-1}+\cdots+b_{1} 2^{1}+b_{0} 2^{0}\right)
$$

When $b_{0}=1$, then $t_{0}=0$. This approach will transform $k$ into the string of digits $T=\left(1, t_{n}, \ldots, t_{1}, \overline{1}\right)$, where the last digit $\overline{1}=-1$. This is Koyama-Tsuruoka's algorithm for a non-zero window $B=\left(b_{n}, \ldots, b_{1}, 1\right)$.

When $b_{0}=0$, then $t_{0}=-1$. This approach will transform $k=\left(b_{n}, \ldots, b_{1}, 0\right)$ into the string of digits $T=\left(1, t_{n}, \ldots, t_{1}, \overline{2}\right)$, where the last digit $\overline{2}=-2$. This last digit does not affect the scalar multiplication $(k P)$ at all. In the very last step of a given scalar multiplication algorithm, subtract a double of a point $2 P$ instead of the point $P$ itself. The point $2 P$ is available for free since it is always computed during the process. If other $m$ ary methods or window methods are used, the digit $\overline{2}$ is obviously not a concern anyway. Only some minor changes are needed in the pre-computations.
Example: $742_{10}=1011100110_{2}=10 \overline{1} 000 \underline{1} \overline{1} 00 \overline{2}_{2}$. Then by a sliding window method, $743 P=2^{5}\left(2^{5} P-8 P\right)-26 P$.

The number of non-zero digits in $T$ is

$$
\#_{n o n-0}(T)=2+\sum_{i=1}^{n}\left|t_{i}\right|=2+\sum_{i=1}^{n}\left|b_{i}-1\right|=2+\sum_{1 \leq i \leq n, b_{i}=0} 1=2+\#_{0}\left(k^{\prime}\right) \text {, where } k^{\prime}=\left(b_{n}, \ldots, b_{1}\right) \text {. }
$$

Hence, this transformation is effective if the condition $2+\#_{0}\left(k^{\prime}\right)<\#_{1}(k)$ is satisfied. The transformation costs one extra doubling because of the extra bit. Since $\#_{0}\left(k^{\prime}\right)=\#_{0}(k)-1$, the condition can be rewritten as: $\operatorname{Diff}(k)=\#_{1}(k)-\#_{0}(k)>1$.

Therefore, Koyama-Tsuroka's algorithm can now be applied to any binary bit strings that satisfy the previous condition on Diff $(k)$. The improved algorithm dramatically expands the application of the original algorithm by removing the limit of applying only on non-zero window pattern.

## 6. Conclusions

The intention is to modify or to develop methods using some digits other than just bits 0 and 1 in the standard binary expansion of the scalar $k$ of the scalar multiplication. The benefit of using the 2 digit in the expansion is because of the availability of the point $2 P$ in the (pre-)computation processes. Using "negative" digits (e.g., $\overline{1}=-1$ or $\overline{2}=-2$ ) has great benefits since the cost of subtraction is the same as that of addition in elliptic curve implementations. This is a considerable improvement to the approaches for developing new algorithms to improve the efficiency of implementations of scalar multiplication operations in elliptic curve cryptosystems.

This paper presented two improvements on two popular signed window algorithms for scalar multiplications implemented in elliptic curve cryptosystems. The first improvement on Morain-Olivos's algorithm helped to expand its domain of applicable patterns in the binary expansion of the scalar $k$. The improvement on KoyamaTsuruoka's algorithm expands the application domain from non-zero windows to any binary expansion of $k$ satisfying the condition on the difference on the numbers of bits 0 and 1. These two improvements facilitate the efficiency of implementation of scalar multiplication in elliptic curve cryptosystems.

## References

[1] ANSI X9.62-1999, Public key cryptography for the financial services industry: The elliptic curve digital signature algorithm (ECDSA), American Bankers Association, 1999.
[2] ANSI X9.63-200x, Public key cryptography for the financial services industry: Elliptic curve key agreement and transport protocol, American Bankers Association, 2000.
[3] J. Guajargo \& C. Paar, Efficient algorithms for elliptic curve cryptosystems, Advances in Cryptology - Crypto '97, Lecture Notes in Computer Science 1294, 342356, Springer-Verlag, 1997.
[4] J. Jedwab \& C. Mitchell, Minimum weight modified signed-digit representations and fast exponentiation, Electronics Letters, Vol. 25, No. 17 (1989), 1171-1172.
[5] C. Koç, High-radix and bit recoding techniques for modular exponentiation, International Journal of Computer Mathematics, Vol. 40, 1991, 139-156.
[6] C. Koç, Analysis of sliding window techniques for exponentiation, Computers Math. Applications, Vol. 30, No. 10, 1995, 17-24.
[7] K. Koyama \& Y. Tsuruoka, Speeding up elliptic cryptosystems by using a signed binary window method, Advances in Cryptology - Crypto '92, Lecture Notes in Computer Science 740, 345-357, Springer-Verlag, 1993.
[8] F. Morain \& J. Olivos, Speeding up the computations on an elliptic curve using addition-subtraction chains, RAIRO-Informatique Théorique et Applications Theoretical Informatics and Applications, Vol. 24, No. 6, 1990, 531-544.

