**Directional Radio-Frequency Identification Tag Reader**

*John F. Kennedy Space Center, Florida*

A directional radio-frequency identification (RFID) tag reader has been designed to facilitate finding a specific object among many objects in a crowded room. The device could be an adjunct to an electronic inventory system that tracks RFID-tagged objects as they move through reader-equipped doorways. Whereas commercial RFID-tag readers do not measure directions to tagged objects, the device is equipped with a phased-array antenna and a received-signal-strength indicator (RSSI) circuit for measuring direction. At the beginning of operation, it is set to address only the RFID tag of interest. It then continuously transmits a signal to interrogate that tag while varying the radiation pattern of the antenna. It identifies the direction to the tag as the radiation-pattern direction of peak strength of the signal returned by the tag. An approximate distance to the tag is calculated from the peak signal strength. The direction and distance can be displayed on a screen. A prototype containing a Yagi antenna was found to be capable of detecting a 915.5-MHz tag at a distance of ≈15 ft (≈4.6 m).

This work was done by Pedro J. Medelius, John D. Taylor, and John J. Henderson of Dynacs, Inc., for Kennedy Space Center.

In accordance with Public Law 96-517, the contractor has elected to retain title to this invention. Inquiries concerning rights for its commercial use should be addressed to: Lynne R. Henkiel, KSC Industry Liaison KSC Technology Programs & Commercialization Office Mail Code YA-C1 Kennedy Space Center, FL 32899 Phone: (321) 867-8130 Fax: (321) 867-2050 E-mail: Lynne.Henkiel-1@ksc.nasa.gov Refer to KSC-12348, volume and number of this NASA Tech Briefs issue, and the page number.

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**Integrated Solar-Energy-Harvesting and Storage Device**

*Efficiency would be maximized for anticipated ranges of operating conditions.*

*NASA’s Jet Propulsion Laboratory, Pasadena, California*

A modular, integrated, completely solid-state system designed to harvest and store solar energy is under development. Called the “power tile,” the hybrid device consists of a photovoltaic cell, a battery, a thermoelectric device, and a charge-control circuit that are heterogeneously integrated to maximize specific energy capacity and efficiency. Power tiles could be used in a variety of space and terrestrial environments and would be designed to function with maximum efficiency in the presence of anticipated temperatures, temperature gradients, and cycles of sunlight and shadow. Because they are modular in nature, one could use a single power tile or could construct an array of as many tiles as needed. If multiple tiles are used in an array, the distributed and redundant nature of the charge control and distribution hardware provides an extremely fault-tolerant system.

The figure presents a schematic view of the device. High-efficiency photovoltaic cells would be attached to a thin-film array of thermoelectric devices, which, in turn, would be integrated to a multi-layer thin-film solid-state battery packaged in a thermally conductive envelope. The charge control circuitry would be integrated either onto the battery side of the device or into a protective frame that would enclose the device. The entire package is designed to be less than 2 mm thick.

The thermoelectric devices would harvest some of the thermal energy incurred when solar radiation raises the temperature on the photovoltaic-cell side relative to the shaded backside. The battery would be placed on that opposite side, and the outer surface of its thermally conductive envelope would be coated with a thermally emissive material to aid in creating the greatest possible temperature differential for optimum operation of the thermoelectric device. The same thermoelectric devices could also be operated in a power-consuming, heat-pump mode to keep the batteries within a desired operational temperature range during intervals of darkness/cold. Microthermoelectric devices that are no more than 500 µm thick are currently under fabrication for intended integration into the power tile device.

The solid-state battery system performs nominally in the temperature range of –20 to 60 °C, and has been shown to function at limited discharge rates to temperatures as low as –40 °C. These thin-film solid-state cells, based on mate-
The design of the present camera loosens this coupling to increase the achievable frame rate and functionality. From a host computer perspective, the readout operation in the prior camera was defined on a per-line basis; in this camera, it is defined on a per-ROI basis. In addition, the camera includes internal timing circuitry. This combination of features enables real-time, event-driven operation for adaptive control of the camera. Hence, this camera is well suited for applications requiring autonomous control of multiple ROIs to track multiple targets moving throughout the CCD field of view. Additionally, by eliminating the need for control intervention by the

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**Event-Driven Random-Access-Windowing CCD Imaging System**

**Regions of interest can be adapted to changes in the scene.**

_NASA’s Jet Propulsion Laboratory, Pasadena, California_

A charge-coupled-device (CCD) based high-speed imaging system, called a real-time, event-driven (RARE) camera, is undergoing development. This camera is capable of readout from multiple subwindows [also known as regions of interest (ROIs)] within the CCD field of view. Both the sizes and the locations of the ROIs can be controlled in real time and can be changed at the camera frame rate. The predecessor of this camera was described in “High-Frame-Rate CCD Camera Having Subwindow Capability” (NPO-30564) NASA Tech Briefs, Vol. 26, No. 12 (December 2002), page 26. The architecture of the prior camera requires tight coupling between camera control logic and an external host computer that provides commands for camera operation and processes pixels from the camera. This tight coupling limits the attainable frame rate and functionality of the camera.

The design of the present camera loosens this coupling to increase the achievable frame rate and functionality. From a host computer perspective, the true microthermoelectric devices, and extended lifetime testing.

The work was done by Jay Whitacre, Jean-Pierre Fleurial, Mohammed Mojarradi, Travis Johnson, Margaret Amy Ryan, Ratanakumar Bugga, William West, Subbarao Surampudi, and Julian Blosiu of Caltech for NASA's Jet Propulsion Laboratory. Further information is contained in a TSP (see page 1).

In accordance with Public Law 96-517, the contractor has elected to retain title to this invention. Inquiries concerning rights for its commercial use should be addressed to Intellectual Assets Office, JPL:

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**The RARE Camera** is a high-speed CCD-based imaging system that offers enhanced speed and functionality for tracking moving targets.