



Radiation Test Results on COTS & non-COTS Electronic Devices for NASA-JSC Spaceflight Projects



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ABSTRACT

This presentation reports the results of recent proton and heavy ion Single Event Effect (SEE) testing on a variety of COTS and non-COTS electronic devices and assemblies tested for the Space Shuttle, International Space Station (ISS) and Multi-Purpose Crew Vehicle (MPCV).

INTRODUCTION

NASA JSC has supported 3 programs (Shuttle, ISS, and MPCV) and must test electronic devices in a method that will meet the applicable program requirements.

LEO - Space Shuttle and ISS are Low Earth Orbit (LEO) applications and are tested using 200MeV protons as documented by Dr. O'Neill [1] The 200MeV beam has been used for over a decade with positive results and this method is especially valuable for testing board level assemblies and COTS units are commonly tested using this method.

Deep Space - Multi-Purpose Crew Vehicle (MPCV) or Orion 2 is a deep space mission profile and generally all hardware used for Orion-2 requires heavy ion characterization but Proton Testing has also been used to quickly screen out "soft" hardware and select the best candidate device when hardware was available from multiple vendors.

In 2008 through 2011 the Specialty Engineering Branch of the Avionics Division of the JSC Engineering Directorate tested at the Indiana University Cyclotron Facility (IUCF), Texas A&M University Cyclotron (TAMU), Lawrence Berkeley National Laboratory (LBNL), and NASA Space Radiation Laboratory (NSRL) at Brookhaven National Laboratory. A wide variety of COTS parts such as FPGAs, memories, wireless routers and processors were tested at board level using the high energy protons at IUCF.

TESTING & EXPERIMENTAL METHODS

A. PROTON TESTING

The majority of JSC hardware used for Shuttle and ISS is tested using 200MeV protons, at IUCF. The proton beam passes through the device losing less than 10% of the initial energy. While the incident protons themselves usually do not cause direct device upsets, they do collide with the nucleus of atoms inside the target device. This collision can fragment the nucleus and then generate a shower of high-energy secondary particles that can cause direct ionization with surrounding atomic nuclei [2]. It is these secondary particles that cause an electronic device to upset, if enough recoil energy is deposited in the sensitive volume. These reactions are rare, with approximately one nuclear collision in every 1e+6 incident protons. However, the primary drawback of proton testing is that the effective linear-energy-transfer (LET) of the secondary particles are limited to less than 14 MeV cm²/mg and have a short range [2]. Protons do not fully characterize the device's response to radiation compared to heavy ions with the same effective LET.

In preparation for proton testing an initial meeting is held with the potential project to understand the hardware and its application, as well as specific radiation success criteria. The hardware criticality, mission duration, and any mitigation methods are taken into account when planning the radiation test. A part list of the hardware is generated and a sequence of beam positions or target areas is mapped out for the candidate hardware. The general project information, parts lists, beam positions and hardware setup and configuration is captured in the project test plans and procedures for documentation.

ANALYSIS METHODS

A. PROTON ANALYSIS

To analyze the proton data, the SEEs are grouped by type, frequency and severity. The errors are counted and inputted into a program called PROTEST [3]. PROTEST derives the equivalent 10 year MTBF for the hardware. This software integrates the test data with the LEO radiation environment defined above. It typically assumes worst-case environmental conditions, with 0.1 inch shielding around the device to give a conservative result. The output of PROTEST is the calculated Mean-Time-Between-Failure (MTBF) rate expected for operating the hardware in LEO orbits (expressed in terms of days between failures). An MTBF is calculated for each beam position, as well as a final box-level composite rate. These estimates assume the hardware is operating continuously on-orbit and does not take into account the actual mission timeline in which it will be used. For those devices that show no SEE failures in a typical 1E+10 exposure, we estimate the LEO on-orbit MTBF to be greater than 10 years. This is the same methodology that has been used at JSC for more than 15 years to evaluate the radiation hardness of mostly COTS hardware.

SUMMARY OF RADIATION TESTING

The following section will summarize Proton radiation testing on select COTS, Non-COTS hardware and individual electronic parts in a series of summary tables. Also a summary of the ISS Laptop testing will also be shown in this section along with a summarized table of the hardware tested.

A. Lenovo T61P Laptop Testing

The T61P 15.4" Wide Screen Lenovo Thinkpad Laptop is a commercial off-the-shelf (COTS) device that was tested in 2008. A series of selected hardware which consisted of SDRAMs, Intel Dual Core Processors, and Hard Drives from different vendors was tested to determine the best hardware for a final flight laptop configuration. The T61P laptop was an upgrade to the A31P IBM Thinkpad laptops on ISS, which has been in use for the last 10 years on orbit. In preparation for testing the candidate laptop was completely disassembled down to the motherboard in order to create a detailed parts list of the components of the laptop and to determine the beam positions for testing. Figure 1 shows the beam positions of the laptop on the motherboard. The major components (processor, SDRAM, hard drive, north and south bridge processors, graphics processor, wireless communication chips and the power chips) of the laptop were isolated in single beam positions to evaluate their potential SEE without multiple active parts being considered as well.

Passmark's Burn In Test software was used to exercise the full capabilities of the laptop and external hardware of the laptop and custom Read/Write software, RWTEST.EXE, was used to test the SDRAM located in the laptop.



The T61P Laptop was powered using a modified AC adapter cord that was connected to a Sorenson power supply, with the operating parameters set at 16V out and max current at 4.5A. With this modified AC adapter we were able to monitor current to the laptop and voltage at the power supply and going to the laptop.

1) Results of the Lenovo Laptop Testing
Each position was irradiated with a total 600rads (SI) and SEEs were recorded in the data log for each position. For the positions where the hardware could be removed and switched to another vendor supplied component, these positions were retested based on the number of varied vendors for the SDRAM, Hard Drives, and Processors. Figure 2 show the laptop in the test cave at IUCF.

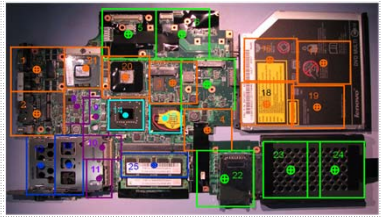


Fig. 1. Beam Position Layout of the Lenovo Laptop

Table II: Summary of COTS & GFE Hardware Tested with Proton Radiation

Beam #	Assembly	Part Number	Device	Part Description	Manufacturer	Beam Position #	WFOF Volume	Observed Radiation Events
1	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	1	26.8 rads	None
2	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	2	26.8 rads	None
3	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	3	26.8 rads	None
4	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	4	26.8 rads	None
5	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	5	26.8 rads	None
6	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	6	26.8 rads	None
7	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	7	26.8 rads	None
8	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	8	26.8 rads	None
9	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	9	26.8 rads	None
10	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	10	26.8 rads	None
11	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	11	26.8 rads	None
12	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	12	26.8 rads	None
13	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	13	26.8 rads	None
14	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	14	26.8 rads	None
15	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	15	26.8 rads	None
16	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	16	26.8 rads	None
17	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	17	26.8 rads	None
18	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	18	26.8 rads	None
19	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	19	26.8 rads	None
20	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	20	26.8 rads	None
21	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	21	26.8 rads	None
22	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	22	26.8 rads	None

Table I: Summary of Laptop Hardware using Proton Radiation Testing

Beam #	Assembly	Part Number	Device	Part Description	Manufacturer	Beam Position #	WFOF Volume	Observed Radiation Events
1	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	1	26.8 rads	None
2	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	2	26.8 rads	None
3	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	3	26.8 rads	None
4	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	4	26.8 rads	None
5	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	5	26.8 rads	None
6	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	6	26.8 rads	None
7	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	7	26.8 rads	None
8	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	8	26.8 rads	None
9	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	9	26.8 rads	None
10	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	10	26.8 rads	None
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12	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	12	26.8 rads	None
13	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	13	26.8 rads	None
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16	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	16	26.8 rads	None
17	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	17	26.8 rads	None
18	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	18	26.8 rads	None
19	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	19	26.8 rads	None
20	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	20	26.8 rads	None
21	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	21	26.8 rads	None
22	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	22	26.8 rads	None

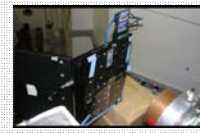


Fig. 2. Lenovo Laptop setup in the Test Cave at IUCF

Table I: Summary of Laptop Hardware using Proton Radiation Testing (Cont)

Beam #	Assembly	Part Number	Device	Part Description	Manufacturer	Beam Position #	WFOF Volume	Observed Radiation Events
1	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	1	26.8 rads	None
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20	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	20	26.8 rads	None
21	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	21	26.8 rads	None
22	IBM Thinkpad Laptop	4438	Processor	Intel Core Duo Processor	Intel	22	26.8 rads	None

CONCLUSIONS

Presented in this body of work is a small summary of the ongoing testing the NASA-JSC Radiation Effects Group has tested over the past years. We frequently test many commercial microelectronic devices, boards and assemblies for short-term use in LEO applications. Actual on-orbit radiation performance obtained has also been very consistent with our proton-based predictions. Caution must be used in interpreting these results as the data we measured is very dependent on the part's lot-date code, the host board circuit design, DUT setup, and test software used. The duty cycle, input/output signals, and DUT resource utilization are directly related to the device's SEE performance. As NASA continues to develop plans for returning to deep space, new radiation-related challenges exist. Mission durations will be longer and the radiation environments are harsher and the avionics used will therefore need to be more reliable, fault-tolerant, and autonomous. The JSC Radiation Effects Team has implemented changes to our current test philosophy and analysis methods in order to meet this challenge.

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