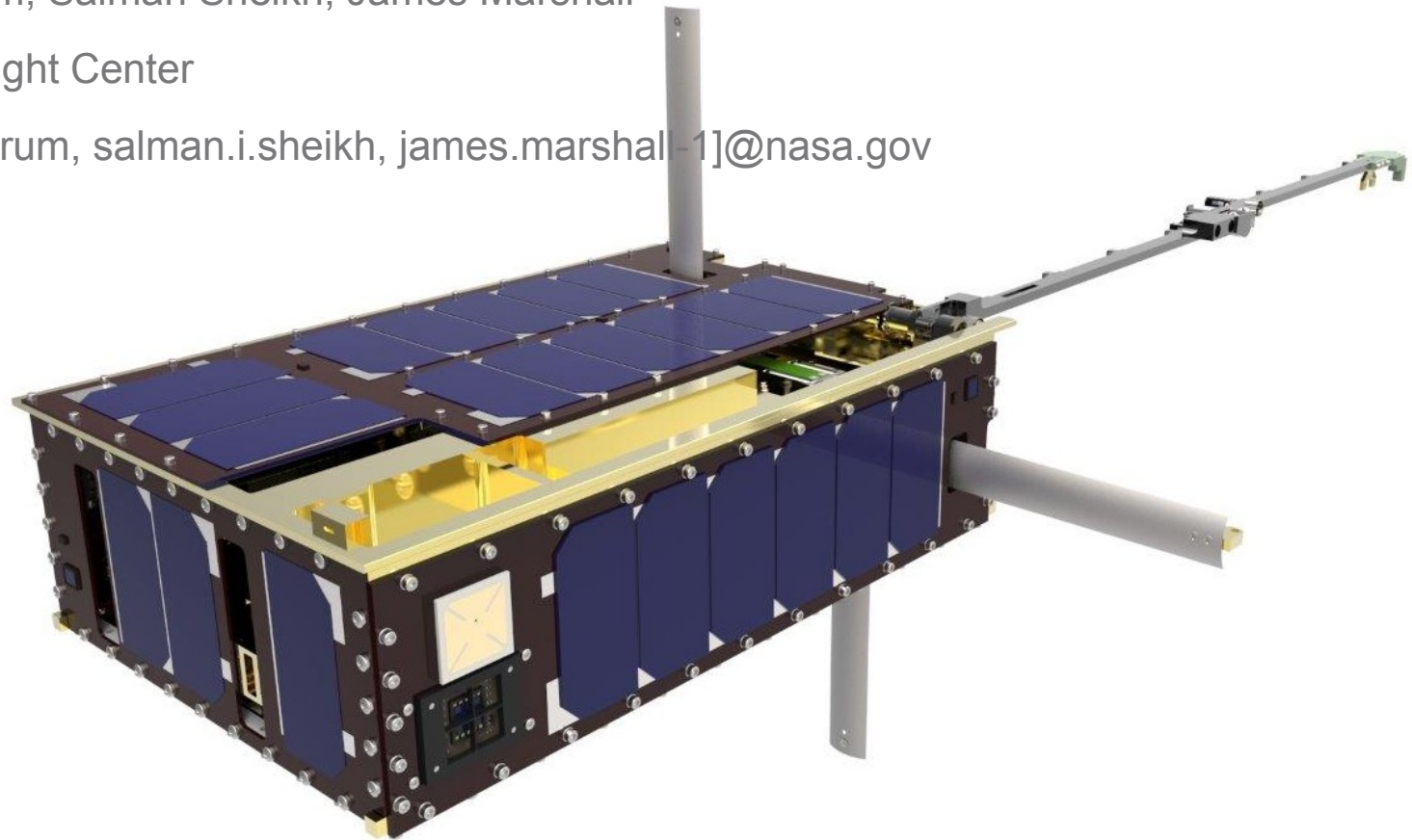


Big Software for SmallSats: Adapting CFS to CubeSat Missions

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Outline

- Motivation
 - What is CFS?
 - Experience: CSP / CeREs
 - Experience: Dellinger
 - Performance
 - Future Work
 - References
- NOTE: All images courtesy of NASA

Motivation

- Expanding requirements
 - Science
 - Risk tolerance
 - This stresses software (and teams!)
- Budgets are not expanding
- “Small” Satellite does **not** mean “small” software
- Solution: a trusted framework with reusable components

CFS: Core Flight Software

- NASA recognized a need to move away from “Clone and Own”
- Developed to tackle the very issues that SmallSats now face
- Framework and core services (cFE)
- Common set of applications and libraries
- (McComas, 2012) (Fesq, Dvorak, 2012)

“At Goddard the main driver for changing the development process is cost, [...] An obvious way to reduce cost and schedule is to increase the amount of software reuse.”

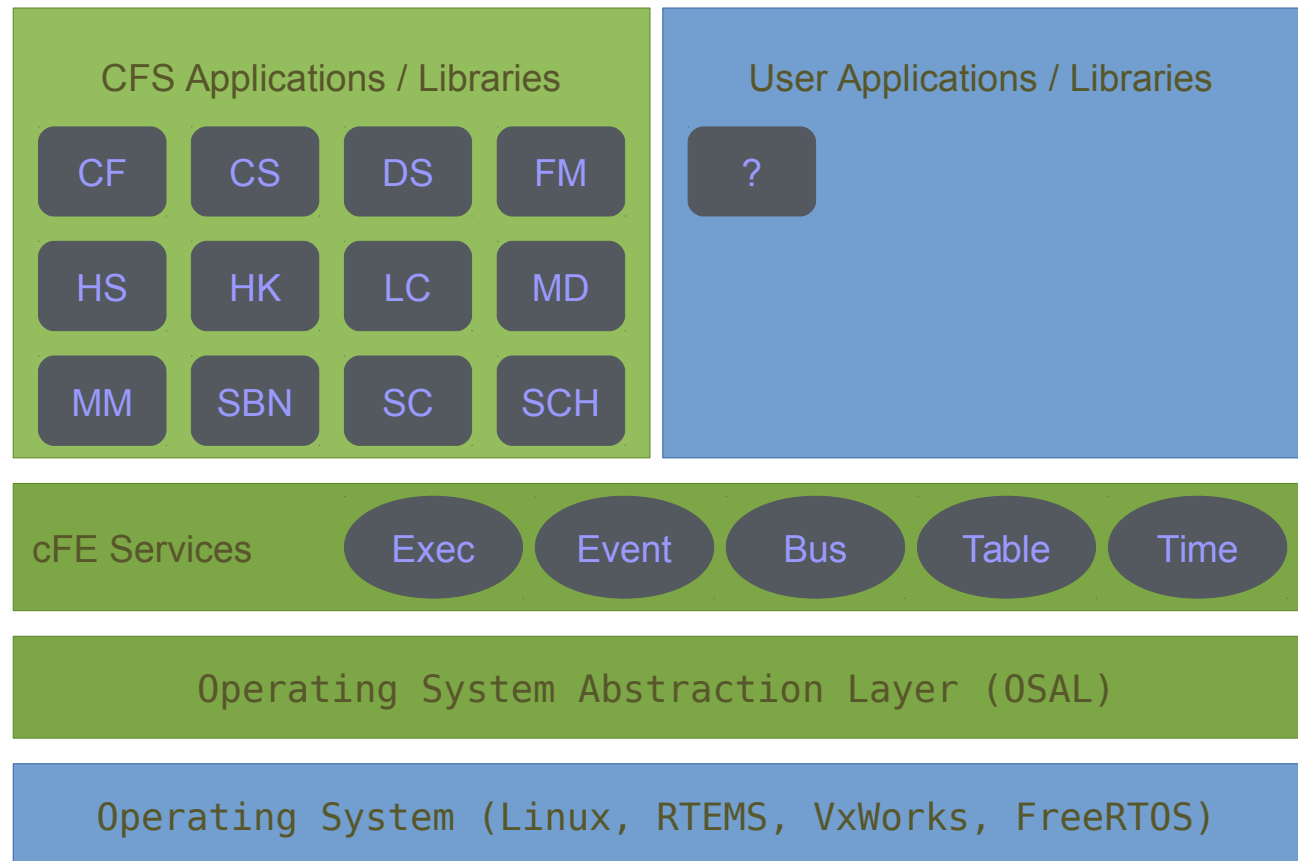
(Wilmot, 2006)

The CFS follows a product line approach with the goal to support systematic reuse.

(Ganesan, Lindvall, Ackermann, McComas, Bartholomew, 2009)

Framework and Core Services (cFE)

- Layered architecture
- Supports Publish / Subscribe Applications
- Events
- Tables
- Time



Libraries and Applications

- Currently 12 Applications are available (<http://cfs.gsfc.nasa.gov/>)
- Optional, depends on mission needs.
- Easy to create
 - Sample application demonstrates messaging, events, and application loop

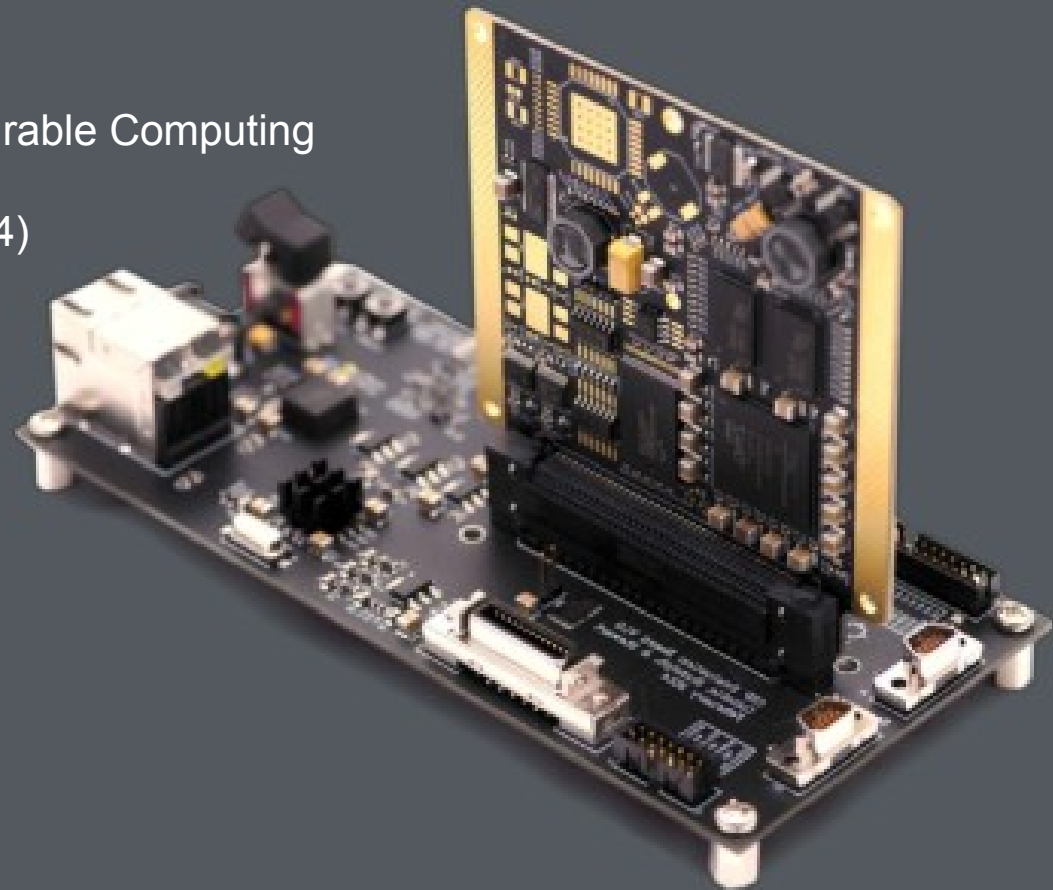
Heritage



- cFE:
 - Lunar Reconnaissance Orbiter
 - Living With a Star / Radiation Belt Storm Probes
- CFS
 - Global Precipitation Measurement
 - Magnetospheric MultiScale
 - Lunar Atmosphere and Dust Environment Explorer

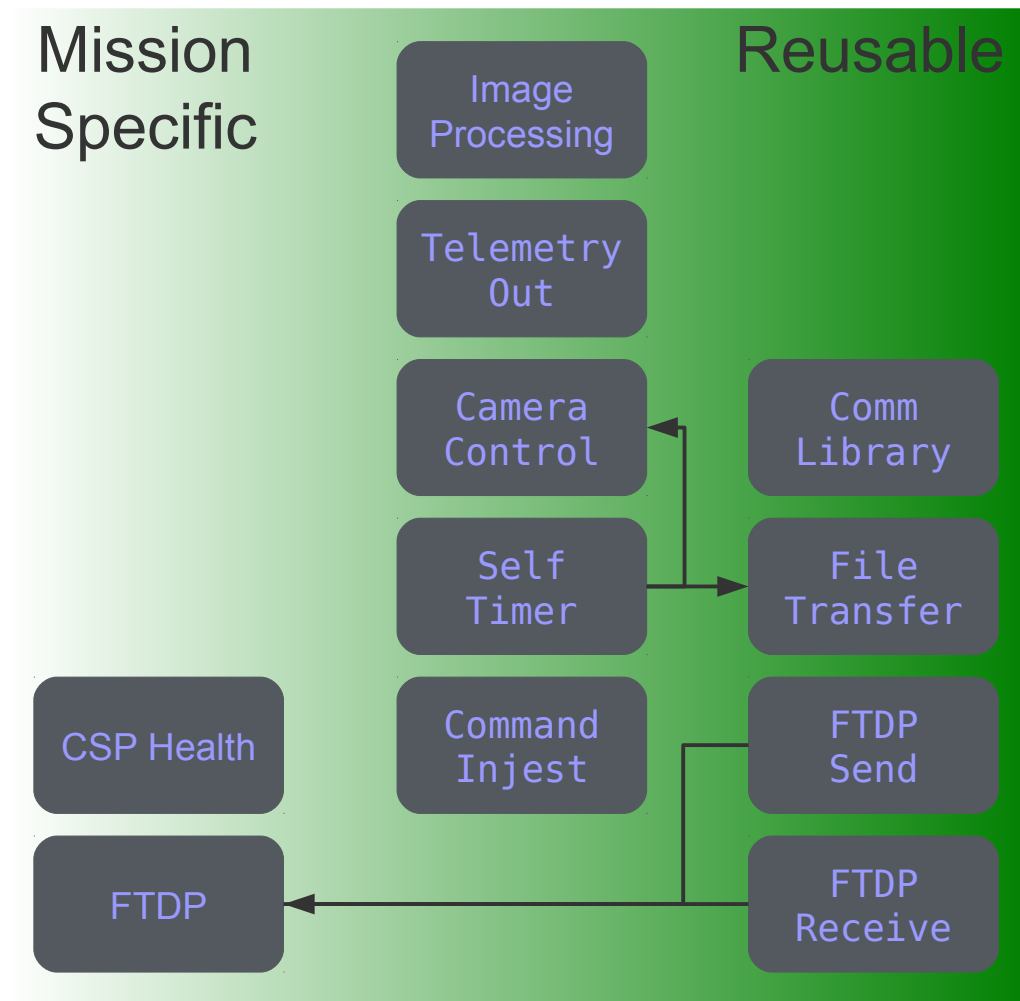
CHREC Space Processor

- Space Test Program, Houston 5 / ISS SpaceCube Experiment Mini
- CHREC Space Processor Experiment
- NSF Center for High-Performance Reconfigurable Computing
- Presented here last year (Rudolph et al, 2014)
- Xilinx Zynq 7020
 - Arm Dual Core Cortex A9
 - Artix-7 FPGA
- Runs CFS!



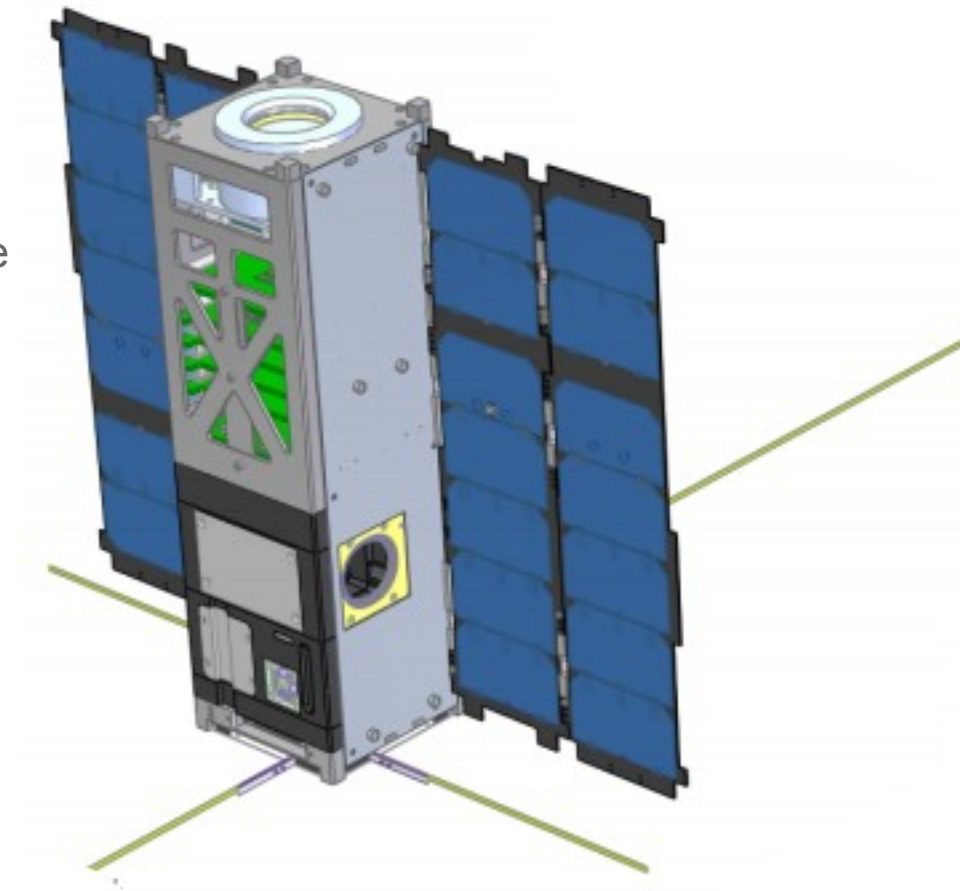
CFS on the CHREC Space Processor

- Work spread over 3 employees
- Created 11 custom applications / libraries
- Code is in well defined applications
- Vary in level of reusability
- This is in addition to existing CFS functionality



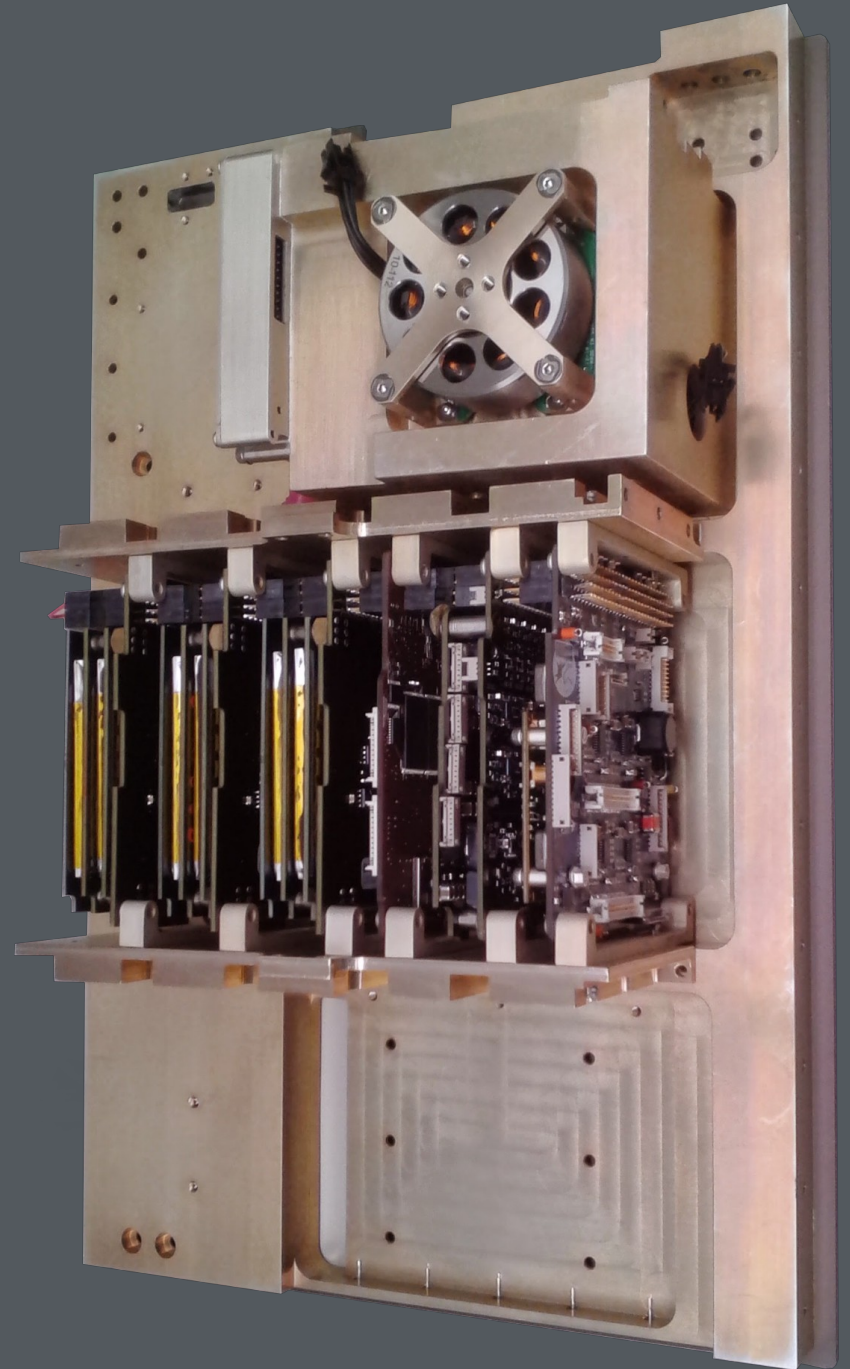
CeREs

- Compact Radiation Belt Explorer
- MERiT: Miniaturized Electron and pRoton Telescope
- Flight computer is a CSP
- CFS used for flight software
- (Kanekal, 2014)



Dellingr

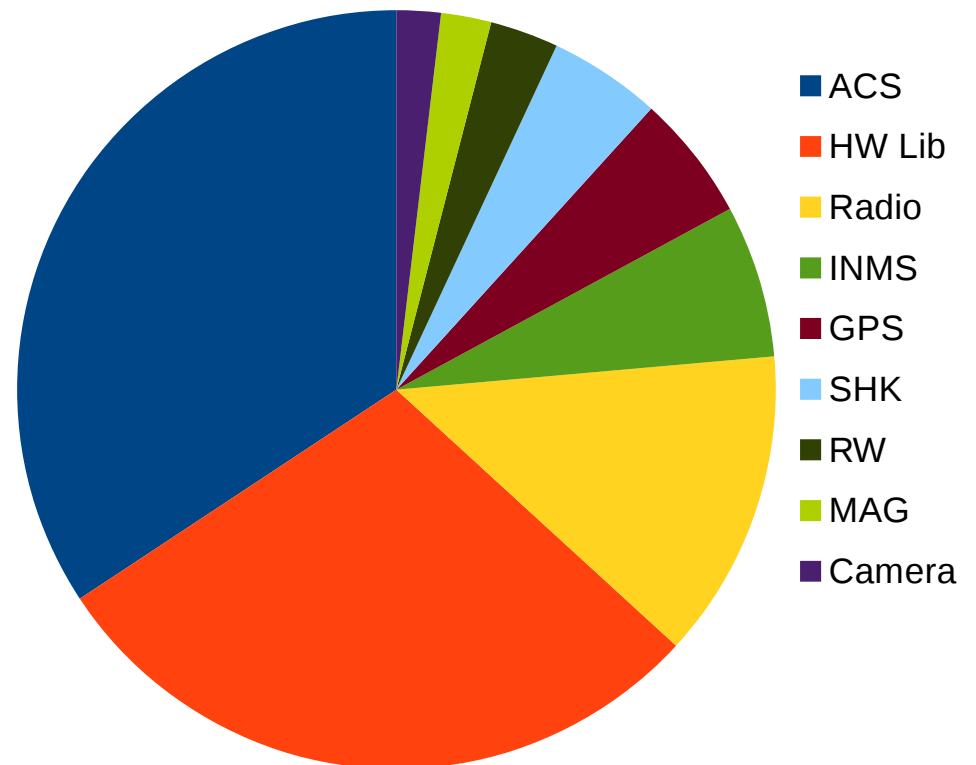
- Hardware:
 - ARM7 processor (40 Mhz 2Mb RAM)
 - Reaction Wheels
 - Magnetorquers
 - Sensors (FSS)
- Science
 - INMS
 - Magnetometer
 - Thermal Louvre



Dellingr and CFS

- Work spread over three employees
- Ported OSAL to FreeRTOS
- Integrate with GomSpace software
- Custom
 - Hardware Library
 - Hardware telemetry
 - Radio
 - ACS
 - Science instruments
- Generated using David A. Wheeler's 'SLOCCount'

Custom Code for Dellingr Approx. 10k SLOC



Performance

- CFS imposes some performance costs
- Compared build with just FreeRTOS vs CFS
- Code available: <https://github.com/jcmarsh/cpek>

	FreeRTOS	CFS
Whetstone (KWIPs)	10426.4	4448.2
Dhrystone (per second)	798.6	330.8
Hardware ping (per second)	753.5	489.7
Application ping (per second)	1481.9	285.7

Future Work

- 42 Simulator integration: <http://fortytwospacecraftsimulation.sourceforge.net/>
- CFS SDK
- Man Rated

Summary

- CFS is a mature framework
 - Strong heritage
 - Reduces personnel requirements
 - Available on a variety of platforms
 - Well suited to CubeSat missions
- Open Source (<http://cfs.gsfc.nasa.gov/>)
- Already being used on NASA CubeSats

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