

TTEthernet Development and CFS Integration

DSG CFS / Open SW Multi-lateral TIM NASA JSC, Houston, TX 11 – 13 Sept. 2017

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TTE Chip-IP





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TTE Dataflow Concept

- NASA (RES
- A TTEthernet network is composed of end systems and switches.
 The end system contains the host and network controller (NIC).



TTE Dataflow Concept (ports)



- TTE dataports represent the end points for all communication.
- Ports are Sampling or Queuing, and use UDP, IPv4, or MAC layers.



TTE Dataflow Concept (VLs)



- Ports are connected to one another by Virtual Links (VLs).
- Virtual Links are logically point-to-point one sender, N receivers.



TTE Dataflow Concept (VLs)



Traffic from different VLs is multiplexed over the same cabling.
 Logically the same as ARINC 429, except the "links" aren't real.



TTE Dataflow Concept (partitions)

- Ports are grouped in device partitions (8 per end system).
- Different partitions can be accessed simultaneously no mutexes.



TTE Dataflow Concept (partitions)

- Device partitions can be mapped to the RTOS partitions.
- Thus partitioning is end to end from app \rightarrow network \rightarrow app.



TTE CFS App



- The TTE application enables TTE communication in CFS.
- Includes pre-compiled drivers for supported targets and OSs.
- Provides API to claim ports and send or receive messages.



TTE CFS App (design)





Andrew Loveless (NASA JSC/EV2)

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TTE CFS App (design)





SCH_TT CFS App



- SCH_TT is a drop-in replacement for the standard SCH scheduler.
- Drives the FSW execution according to scheduled interrupts.
- Aligns the task scheduling with the TT message scheduling.



SCH_TT CFS App (design)





Andrew Loveless (NASA JSC/EV2)

Telemetry Example



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Telemetry Example (cont.)





Telemetry Example (cont.)



0:13.988 FIF0: cmd SCH_TT_MODE_ENABLE_CC						demo_tt	e_fcc1						_ = ×
1:14.013 echoPacket: 18d1c00000010006	DATAPORT NAME		DIRECTION	PORT TYPE	PORT MODE	TASK NAME	SENT MS	GS RECV MSGS	SENT ERR RECV E	IRR RECV CHAN	RECV DROP TIME LA	AST SENT	TIME LAST RECV
D:17.387 FIFO: cmd SCH_TT_MODE FNAM F CC	ES1_COTS_MAC_PHY1_TX												
			Congrate		10001115	ueino_tte_ittz							
DATAPORT NAME	DIRECTION	PORT TYPE	PORT MODE	Т	ASK NAME		SENT MSGS	RECV MSGS	SENT ERR	RECV ERR	RECV CHAN	RECY DROP	TIME LAST SENT
ES4 COTS MAC PHY1 TX	Output	MAC RAW	Queuing										
ES4 COTS MAC PHY2 TX	Output	MAC RAW	Queuing										
ES4_COTS_MAC_PHY3_TX	Output	MAC RAW	Queuing										
ES4_VL400_SCH_SVNC_SEND	Output	TP COM	Queuing	c	сц тт		Ĕ						17+19+42 ma+829
ES4_VL40C_OMPR_MPRIM_DOTO_CEMP	Output	TP_COM	Queuing		-								13,19,42 08,029
ES4_VL408_HHFS_HBSUI_DHTH_SEND	Output	IF_COM	Queuing										
ES4_VL407_HNPS_NBSUZ_DHTH_SEND	Output	IP_COM	Queuing	-									
ES4_VL408_HMPS_PDU1_DHTH_SEND	Uutput	IP_COM	Queuing										
ES4_VL409_AMPS_PUU2_DATA_SENU	Uutput	IP_COM	Queuing										
ES4_VL411_APC_COMBINED_SEND	Output	IP_COM	Queuing										
ES4_VL421_ACAWS_FD_TEST_RESULTS_VOTED_SEND	Output	IP_COM	Queuing										
ES4_VL422_ACAWS_DE_DIAG_DATA_VOTED_SEND	Output	IP_COM	Queuing										
ES4_VL423_PPA_HYDE_OUTPUT_VOTED_SEND	Output	IP_COM	Queuing										
ES4_VL424_ACAWS_FIR_OUTPUT_VOTED_SEND	Output	IP_COM	Queuing										
ES4_VL431_TLM_COMBINED_SEND	Output	IP_COM	Queuing										
ES4_COTS_MAC_PHY1_RX	Input	MAC_RAW	Queuing										
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demo_tlm_main _ D						demo_tt	e_fcc2						×
Demo Main Telemetry Page	DATAPORT NAME											AST SENT	TIME LAST RECY
2017 Andrew Loveless (JSC/L2)	ES4_COTS_MAC_PHY1_TX ES4_COTS_MAC_PHY2_TX												
FDC 1 [Aitech SP0]	ES4_COTS_MAC_PHY3_TX												
	ES4_VL400_SCH_SYNC_S											12 ns:829 is:527	
SCH_TT Housekeeping Th. Housekeeping	ES4 14 407 OMES MEETS												
State Vector 0x1f State Vector 0x17ff/	CONTACTOR HILL OF IDDOG												/
	ES4_VL409_AHPS_PDU1_												
Processing Mode 1 Command Counter 198	ES4_VL409_AMPS_PDU1_ ES4_VL409_AMPS_PDU2_ ES4_VL411_APC_COMBIN												
Processing Mode 1 Соммалd Coll or 198 Соммалd Counter 1 Соммалd Error 2 nter 0 Соммалd Error Counter 0	ES4_VL400_AMPS_PDU1_ ES4_VL409_AMPS_PDU2_ ES4_VL401_APC_COMBIN ES4_VL421_ACAMS_FD_1												
Processing Mode 1 Command Control 198 Command Counter 1 Command Error 198 Command Error Counter 0 Local Time 10591	ES4_VL408_AMPS_PDU2 ES4_VL408_AMPS_PDU2 ES4_VL409_AMPS_PDU2 ES4_VL401_APC_COMBIN ES4_VL421_ACRMS_DE_T ES4_VL422_ACRMS_DE_T ES4_VL422_MCMS_DE_T												
Processing Mode 1 Conward Con 198 Command Counter 1 Command Error Inter 0 Command Error Counter 0 Local Time 10991 Micor Frame 200	E54_VL408_PMFS_PDU2 E54_VL409_PMFS_PDU2 E54_VL409_PMFS_PDU2 E54_VL421_PCC_DDMBIN E54_VL422_PCNHS_PD_1 E54_VL422_PCNHS_DE_1 E54_VL422_PCNHS_DE_1 E54_VL424_PCPHS_FTR_												
Processing Mode 1 Command Cont 198 Command Counter 1 Command Error oter 0 Local Time 10591 Micro Frame 30 Major Frame 200	E54_VL406_MPS_PDU1_ E54_VL409_MPS_PDU2_ E54_VL409_MPS_PDU2_ E54_VL401_MPS_CDU2_ E54_VL421_MCMS_FD_1 E54_VL422_MCMS_FD_ E54_VL423_MCMS_FTR_ E54_VL424_MCMS_FTR_ E54_VL431_ML0MB5_FTR_												
Processing Hode 1 Conward Convertige Commend Courter 1 Command Error Duffer 0 Local Time 10591 Minor Frame 30 Major Frame 200	55, V.4.06, PMS, PDU, 554, V.4.06, PMS, PDU, 554, V.4.09, PMS, PDU, 554, V.421, PCC, CMBI, 554, V.422, PCMS, TE, 554, V.422, PCMS, TE, 554, V.422, PCMS, TE, 554, V.424, PCMS, TE, 554, V.444, PCMS, T	_DATA_SEND DATA_SEND DATA_SEND ED_SEND ED_SEND MAG_DATA_VOTED_SEND UTFVT_VOTED_SEND DUTFVT_VOTED_SEND ED_SEND	Output Output Output Output Output Output Output Output Output Input	IP_COM IP_COM IP_COM IP_COM IP_COM IP_COM IP_COM IP_COM IP_COM IP_COM IP_COM	Useuing Queuing Queuing Queuing Queuing Queuing Queuing Queuing Queuing Queuing Queuing Sawiing						0 0 0 0 0 0 0 0 0 0 0 0 0 0 0		
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Processing Hode I Command Control 199 Command Counter 1 Command Counter 0 Local Time 10591 Huror Frame 30 FCC 2 [Aitech SP0] SOLIT Housekeeping TTE Housekeeping State Vector 0x1F State Vector 0x17ff1 Processing Hode 1 Command Counter 198 Command Error Counter 0 Command Error Counter 0 Command Error Counter 0 Command Error Counter 0 Command Error Counter 109	 SS, U.V.466, APPS, PULL SS, U.V.466, APPS, PULL SS, U.V.466, APPS, PULL SS, U.V.466, APPS, PULL SS, U.V.421, APPS, APPL SS, U.V.421, APPS, APPL SS, U.V.421, APPL, APPL SS, U.V.421, APPL, APPL SS, U.V.421, APPL, APPL SS, U.V.431, TLN, COMBINERS SS, U.V.431, TLN, COMBINERS SS, U.V.431, APPL, APPL SS, U.V.51, APPL, APPL SS, U.V.52, APPL, APPL 	_DATA_SEND DATA_SEND DATA_SEND ED_SEND ED_SEND SET_PESULTS_VOTED_SEND UTPUT_VOTED_SEND UTPUT_VOTED_SEND ED_SEND ED_SEND ED_SEND ED_SECV ED_RECV SET_PESULTS_RECV UTPUT_RECV DUTPUT_RECV DUTPUT_RECV DUTPUT_RECV DUTPUT_RECV	Output Output Output Output Output Output Output Output Input Input Input Input Input Input Input Input Input Input Input	IP_COM IP_COM IP_COM IP_COM IP_COM IP_COM IP_COM IP_COM IP_COM IP_COM IP_COM IP_COM IP_COM IP_COM IP_COM IP_COM	Unexamp Demaining Demaining Demaining Demaining Demaining Demaining Demaining Demaining Demaining Demaining Demaining Demaining Demaining Demaining Demaining Demaining Demaining Demaining Demaining Demaining Demaining Demaining Demaining Demaining Demaining		8 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0				0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0		
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SH, M. 1992, ACMAS, FEL, SH, M. 1992, ACMAS, FEL, SH, M. 1994, ACMAS, FEL, SH, M. 2004, ACMAS, FEL, SH, M. 2004	DATA_SEND DATA_SEND DATA_SEND DATA_SEND ED_SEND ED_SEND ED_SEND DATA_VOTED_SEND DUTPUT_VOTED_SEND DUTPUT_VOTED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND ED_SEND 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App Availability

- Both planned to be open source, but are not yet.
- Both are fully documented.
 - Doxygen used for all sources.
- So far only tested on nonpartitioned operating systems.
 - Linux, RTLinux, VxWorks.
- Currently only supports PowerPC and x86 targets.

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Network and FSW Scheduling





Integrating with Other Buses





Steiner, Wilfried, "Towards Autonomous Time-Triggered Ethernet", *Workshop on Spacecraft Flight Software*, Pasadena, CA, 2010.

TTE Hardware Status

- TTTech developing space ASIC for switches and end systems.
 - Testing plastic ASIC on the boards now.
 - Ceramic samples scheduled for Feb. 2018.
 - Fully qualified ceramic (QML-V) planned for Q3/2018.
- SEPHY targeting rad-hard PHY prototype for Dec. 2017.
 - Will perform network-level testing.
 - Electrical testing will have already been done.
- TI is in final development of DP83561R rad-tolerant PHY in ceramic.
 - First samples available in Q1/2018.
- Focus now is on 100/1000Base-T.
 - Some plans for Base-CX down the line.





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Questions?

Andrew Loveless (NASA JSC/EV2)