**Search Tree Pruning for Progressive Neural Architecture Search**

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**Background**

Neural networks are increasing in use over the last decade, but such networks require human experts to define a specific architecture to generate optimal results. However, since the results of the network can be quantified, the search for better networks can be automated. There are several methods of automatically finding architectures of neural architectures, like Neural Architecture Search (NAS) by Zoph and Le [2], the but the algorithm explored in this poster uses a tree structure which will stop when a satisfactory architecture is created.

**Progressive Neural Architecture Search Algorithm**

Our algorithm is a progressive neural architecture search derived from Levin search [Schmidhuber, 1997]. Depth First Search (DFS) is used in the generation of the search tree. We expand the search tree by adding new layers into leaf-node networks up to a maximum depth. Networks which do not perform better than their parent in accuracy after training for a fixed number of epochs are removed (pruned) from the search tree. This makes our algorithm greedy in nature.

Child nodes inherit the layers, weights, and hyperparameters of their parents. New layers are drawn from the transition graph (Figure 1) given an insertion point within the parent network and the new layers’ hyperparameters are optimized from a predefined list of values. Multiple networks of the same architecture can be generated but contain varying hyperparameter values. Figures 2 and 3 show an example search tree with additional layers added in blue and the same architecture paths in red.

**Transition Graph**

One of the most important steps of the algorithm is the selection of a layer. Every layer within a model must be appropriate to follow the previous layer. This entails the algorithm must eliminate having, for example, a Dropout layer next to another Dropout layer, or a MaxPooling layer next to a Dense layer. Figure 1 shows a transition graph used in the addition of network layers. The edges of the graph were chosen by surveying neural network literature. An extra state called Start is added to signal which layers are most appropriate to begin the neural network architecture, and Output represents a 10-neuron, Dense, softmax layer.

**Experiments**

After four days of running on an Intel i7 8th generation CPU, 61 different network architectures were created with the best having an accuracy of 91.9% on the Fashion-MNIST dataset. Figure 4 compares our algorithm’s best accuracy to other networks trained on the same dataset. Our model has <2% difference in accuracy compared to GoogleNet, the best non-preprocessed model with an accuracy of 93.7% [1]. Finally, our algorithm produced a network with less parameters in less time on a CPU compared to other benchmark models-VGG16, GoogleNet, and AlexNet. This can be seen in Table 1.

**Future Work**

In the future, varying pruning and algorithms will be implemented to compare performance of the generated networks as well as time. Adding Bayesian optimization for hyperparameter selection to speed up the algorithm.

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**References**