

Excess Heat Capacity in Mo/Au Transition Edge Sensor Bolometric Detectors

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Abstract—Excess heat capacity in a bolometric detector has the consequence of increasing or leading to multiple device time constants. The Mo/Au bilayer transition edge sensor (TES) bolometric detectors intended for the high resolution mid-infrared spectrometer (HIRMES) have two response time, one of which is a few times longer than that estimated from materials used in the design. The relative contribution of this settling time to the overall time response of the detectors is roughly proportional with the pixel area, which ranges between ~ 0.3 and 2.6 mm². Use of laser ablation to remove sections of the silicon membranes comprising the pixels results in a detector response with a smaller contribution from the secondary time constant. Additional information about the nature of this excess heat capacity is gleaned from glancing incidence x-ray diffraction, which reveals the presence of molybdenum silicides near the silicon surface from the bi-layer deposition. Quantitative analysis of the concentration of excess molybdenum, estimated with secondary ion mass spectroscopy, is commensurate to the additional heat capacity needed to explain the anomalous time response of the detectors.

Index Terms— Microfabrication, superconducting photodetectors, surface contamination, thin film sensors.

I. INTRODUCTION

THE high resolution mid-infrared spectrometer (HIRMES) was slated to be a facility-class instrument for the stratospheric observatory for infra-red astrophysics (SOFIA). HIRMES was designed to probe several important aspects of protoplanetary formation by providing low (~ 600) to very high ($\sim 100,000$) spectroscopy in the 25-122 micron spectral regime [1].

Central to the HIRMES instrument were its two types of high filling fraction detector arrays [2, 3]: One was comprised of two 32x16 sub-arrays of 1 mm x 1 mm transition edge sensor (TES) pixels with a filling fraction of 84.7% in the spectral direction and 99.0% in the spatial direction, and the other was

comprised of an 8x16 detector array of TES with eight rows of pixels of different dimensions for each row and a filling fraction ranging between 97.6% and 99.3% in the spatial direction for the smallest and largest pixels, respectively.

Two requirements for the latter, high resolution, detector pixels are a noise equivalent power (NEP) $\sim 3 \times 10^{-18}$ W/rt(Hz) and a response time ~ 5 ms. With a simple thermal model [4], the effective response time of a transition edge sensor (TES) bolometric detector $t_{\text{eff}} = t_0/(1+P\alpha/GT)$, where P is the bias power of the voltage biased TES, and α is the steepness of the transition region and is defined as $T/R \, dR/dT$, where T is temperature, R is the TES resistance, and $t_0 = C/G$, where C is the heat capacity and G is the thermal conductance between the TES and bath, is the natural time constant. Thus, in the absence of an excess heat capacity, the time response of a TES bolometer is linearly proportional to its heat capacity.

Initial development of the high resolution detectors for the HIRMES instrument involved their fabrication on low stress silicon nitride membranes. Unfortunately, the HIRMES detectors fabricated on silicon nitride membranes had much higher effective time constants (hundreds of ms) than expected, and the excess heat capacity of silicon nitride was believed to be the primary contribution. In fact, it is documented in the literature that silicon nitride has been found to have heat capacity that is much higher than the Debye model estimate. One group [5] attributes this excess heat capacity to the presence of surface micro-states, and another [6] posits that two-level systems are responsible for this anomaly.

On account of the long time constants exhibited by the silicon nitride devices, a decision was made to abandon use of this material as a substrate. Subsequent detector development was undertaken on single crystal silicon substrates. Unexpectedly, these detectors also had much higher than expected time constants, and, in this manuscript, a discussion of possible sources

Manuscript receipt and acceptance dates will be inserted here. Funding was provided by National Aeronautics and Space Administration, Grant No. NNH15ZCA001N-S3GI. (*Corresponding author: Ari Brown.*)

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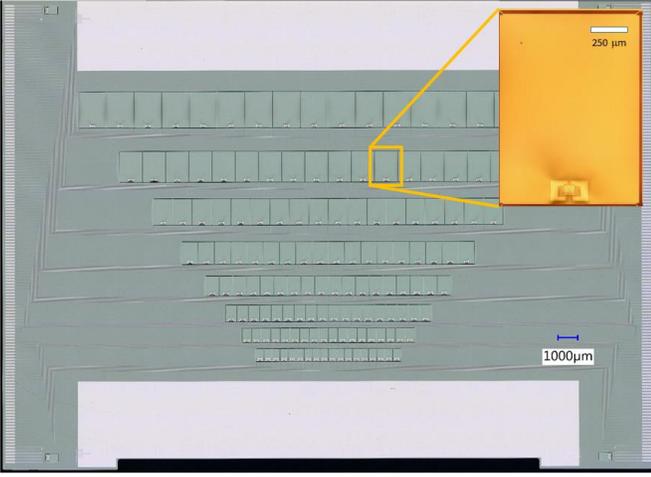


Fig. 1. Stitched micrograph of a HIRMES high resolution detector array.

and means to mitigate excess heat capacity on Mo/Au TES bolometric detectors fabricated on silicon membranes is provided.

II. HIRMES HIGH RESOLUTION DETECTORS

Figure 1 shows a micrograph of a HIRMES high resolution detector array. There are eight 1×18 detector pixel arrays, in which the central 16 pixels of each array can be read out. The pixels are fabricated on 450 nm thick Si(001) substrates, which are suspended with 5 micron wide \times 30 micron long thermal isolation structures. The TES are comprised of Mo/Au bilayer thin films with a superconducting transition temperature $T_c \sim 110$ mK and are read out via superconducting Nb leads. Additional components of the detector pixels include Ti/Au “heat-sinking” structures contacting the TES at two corners, which are used to engineer the pixel heat capacity, and NbTiN absorber coatings [7, 8], which coat the entire surface of the membrane on the side opposite the TES and which have AlN strain matching and Al₂O₃ stress compensating layers. Additional details regarding the fabrication of these detectors are provided in Ref. 3.

The largest pixels measure 1.40 mm \times 1.84 mm and are, thus, ~ 2.6 times larger than the largest leg-isolated single pixel silicon bolometers fabricated at NASA Goddard [9]. As discussed later in this manuscript, the contribution of bulk impurities distributed within the silicon membrane, coupled with a large pixel size, resulted in an excess heat capacity, which was several orders of magnitude greater than the Debye estimate of the silicon heat capacity.

III. HIGH RESOLUTION DETECTOR TEMPORAL RESPONSE

The detector and time-domain multiplexing (TDM) chips were packaged and cooled in a cryogenic free system that reaches ~ 70 mK using an adiabatic demagnetization refrigerator. Detector arrays were readout using TDM based upon superconducting quantum interference device (SQUID) electronics [10]. The detector signal is further amplified by a SQUID series array stage placed at 3K. The warm readout electronics is made by the University of British Columbia and referred to as a

Multi-Channel Electronics (MCE) box. The detector signal is sampled at a rate of 4167 Hz. A square waveform with a 2 sec period is added to the DC TES bias signal to excite the electrothermal circuit and measure the pixel response functions. The amplitude of the square wave is less than 0.05% of the DC bias value.

IV. IDENTIFICATION OF A ROOT CAUSE AND MITIGATION APPROACHES

From the measurements taken and described in Section 3, it was hypothesized that the root cause of the additional time constant was a dangling heat capacity [11]. Some possible sources of this dangling heat capacity were either: (1) The Ti/Au heat-sinking structures, which may have been weakly thermally coupled to the TES through a superconducting Nb joint, or, (2) one or more layers of surface contamination.

In order to evaluate the contribution of the former, we laser ablated the Ti/Au heat-sinking structure – TES *overlap region* on some pixels, shown in Figure 2(a), and removed both of the Ti/Au heat-sinking structures and the underlying silicon membranes on others. Figure 2(b) shows the time response of a pixel in which the overlap region was laser ablated. The time response of the modified pixel, which has a second pole and is shown in red, is largely unchanged relative to that on another, unmodified, pixel with the same surface area. A similar effect is seen on pixels, which had both of the Ti/Au structures as well as the underlying silicon substrate removed; whereas the first,

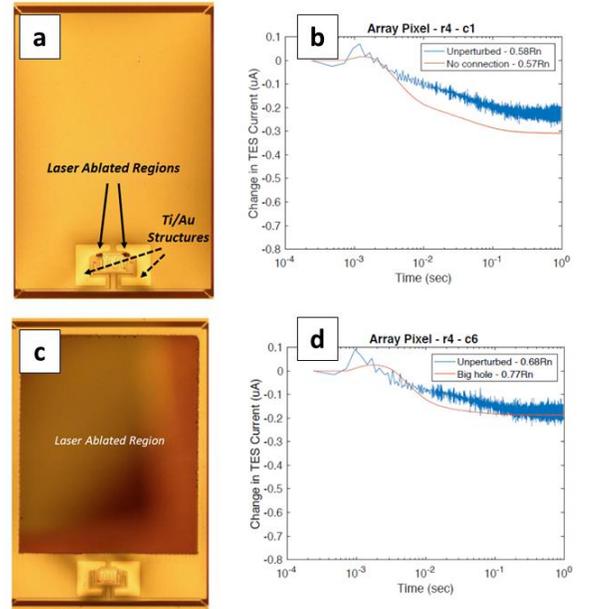


Fig. 2. (a) Image of a detector pixel, in which the Ti/Au heat-sinking structure/ TES overlap regions were laser ablated. (b) Time response of the pixel after laser ablation (red curve), illustrates that there are multiple decay times. (c) Image of a detector pixel in which the central region of the detector pixel has been laser ablated. (d) Time response reveals that the second time constant has largely been eliminated. In both (a) and (c), the pixel dimensions are 0.68 mm \times 0.98 mm.

natural time constant is smaller in these cases, the weight of the second time constant was not appreciably impacted. As an

aside, it is worth noting that the superconducting transition temperature, saturation power, and transition shape were unaltered by laser ablating regions only tens of microns away from it.

We further exploited the ability to laser ablate regions to identify the location of the dangling heat capacity on the pixels. One modification to certain pixels consisted of ablating regions near the pixel perimeter. This modification resulted in no appreciable change to the detector's time response, and was in disagreement with one hypothesis, in which condensation of fluoropolymers formed during the reactive ion etching process, used to define the membranes, contributed to excess heat capacity.

Another modification consisted of ablating the central portion of the pixel, in which 73% of the membrane was removed (see Fig 2(c) and (d)). This modification resulted in a sharp decrease in the weight of the second time constant. Thus, it was inferred that the source of the excess heat capacity resided either on the silicon surface or within its bulk.

In order to look for the presence of surface contamination, scanning electron microscopy (SEM) was performed. Conventional electron microscopy, using an electron voltage of 10 kV, did not reveal any contamination. However, by using low voltage electron microscopy, with voltage ≤ 5 keV, a layer of residue resided near the perimeter of some pixels was observed. Because this residue could not be imaged at higher voltage, it was believed that the residue was organic in nature. This led to the application of a cleaning process demonstrated to remove organic residues on the detector pixels. The process consisted of a succession of intense UV-light exposure, immersion in a commercially available organic base, and oxygen plasma ashing in a reactive ion etcher. After cleaning the pixels, the residues could no longer be seen in the SEM. Furthermore, and more importantly, the amplitude and time constant of the second component was reduced by a factor of four.

Whereas the cleaning process reduced a substantial fraction of the dangling heat capacity, it did not fully eliminate it. This suggested that there were contaminants inside the silicon bulk. Regions near the surface of the silicon membrane were probed with glancing incidence x-ray diffraction. Using an incidence angle $\sim 0.2^\circ$, which corresponds to an x-ray penetration depth of ~ 7 nm in silicon [12], evidence of the presence of a molybdenum silicide, Mo_5Si_3 , was seen. This result was unexpected, because the estimated amount of interdiffusion between Mo and Si after the Mo was deposited is < 1 nm [13]. Thus, athermal mechanisms may have contributed to implantation of sputtered Mo atoms into the single crystal silicon substrate.

In order to quantify the amount of the molybdenum contaminant in the silicon bulk, secondary ion mass spectroscopy (SIMS) was performed. Although the SIMS cannot accurately provide atomic concentration as a function of depth into the bulk, because of resputtering of the sample's constituent atoms, one can obtain an accurate value of the areal concentration by integrating the volume concentration over the sample thickness.

What is the impact of molybdenum contaminants on the time response of our pixels? From integrating the SIMS-obtained concentration profiles, the molybdenum areal concentration is 1.8×10^{15} atoms/cm² which, for the largest HIRMES pixels, translates into an excess heat capacity of ~ 1.4 pJ/K at 0.1 K [14]. This value is approximately 17 times larger than the expected heat capacity of the sum of the heat capacity of all the other components on the pixels. Furthermore, the magnitude of

the heat capacity contribution from the excess molybdenum is similar to that extracted from $G \times t_0$. For the fifth-largest pixels, whose dimensions = 0.82 mm x 1.14 mm, the estimated Mo contribution to heat capacity is 0.5 pJ/K at 0.1 K. This value is the same, within error, to $G \times t_0$.

Does the second time constant disappear when the excess Mo is eliminated? We have some evidence of this in two pixels, in

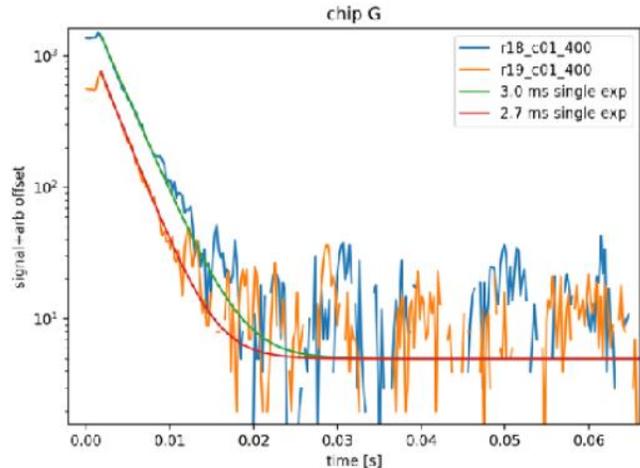


Fig. 3. Time response of two detector pixels after the top 10 nm of silicon substrate had been, inadvertently, been etched. Note the presence of a single pole response.

which the top ~ 10 nm of silicon had been, inadvertently, ion milled. Their time response exhibited a single exponential decay, $t_{\text{eff}} \sim 2.85$ ms.

Therefore, it is quite likely that molybdenum contaminants near surface regions of the silicon membranes are a major contributor to an excess, dangling, heat capacity. Some methods to mitigate or eliminate the presence of the molybdenum contaminants might include use of a lower power Mo sputtering deposition process, depositing the Mo in another manner [15], depositing the Mo through a lift-off mask, or employing a low heat capacity diffusion barrier.

V. CONCLUSION

High filling fraction TES bolometric detector pixels with Mo/Au TES bilayers deposited on crystalline silicon substrates were found to have two time constants, one of which was much larger than the other and whose weight scaled with pixel area. We attribute this additional time constant to a dangling heat capacity, which takes the form of organic residue and molybdenum atoms embedded in the silicon membrane. We have demonstrated fabrication methodologies to remove both of these contaminants, and future large area and high filling factor bolometric detectors with millisecond time constants may need to follow these methodologies in order to achieve their expected performance.

ACKNOWLEDGMENT

The SIMS characterization of our samples was provided by Eurofins EAG Materials Science.

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