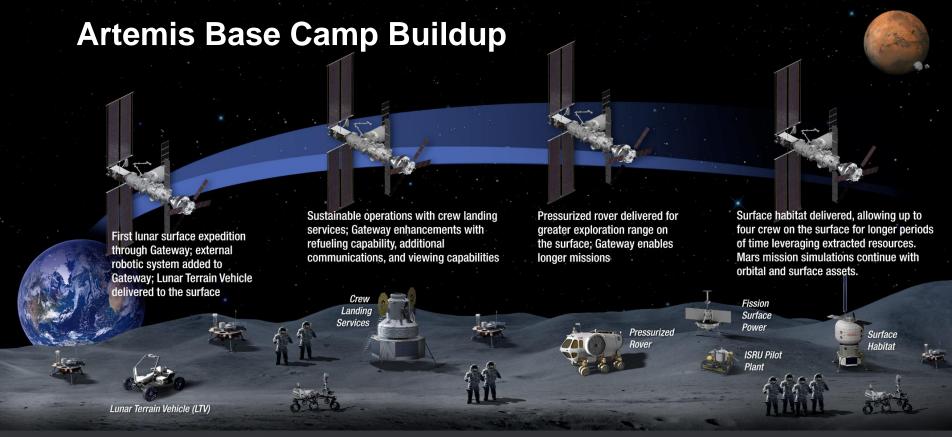




On the Moon to Stay

Challenges Presented to Power Electronics Technology by Sustained Operations on the Lunar Surface

John H. Scott
Principal Technologist, Power and Energy Storage
NASA Space Technology Mission Directorate



SUSTAINABLE LUNAR ORBIT STAGING CAPABILITY AND SURFACE EXPLORATION

MULTIPLE SCIENCE AND CARGO PAYLOADS | U.S. GOVERNMENT, INDUSTRY, AND INTERNATIONAL PARTNERSHIP OPPORTUNITIES | TECHNOLOGY AND OPERATIONS DEMONSTRATIONS FOR MAR

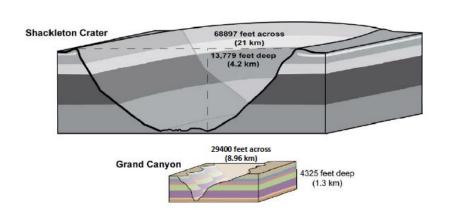


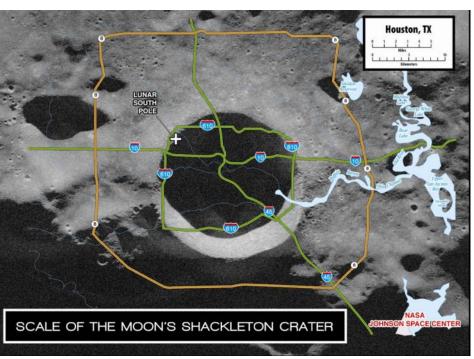
Shackleton Crater



- ~20 km in diameter
- ~4 km deep and ~3x deeper and wider than the Grand Canyon at Enfilade Point
- Located at Lunar South Pole
- Rim and Connecting Ridge are primary targets for future lunar landings

SHACKLETON CRATER vs. GRAND CANYON





Artemis Base Camp Zone



POWER



The key commodity needed to exploit the Lunar Surface

Equatorial Illumination Limits

- Cyclical periods of 14 days illuminated, 14 days dark
- Consistent

Illumination

The scarce resource needed to produce power

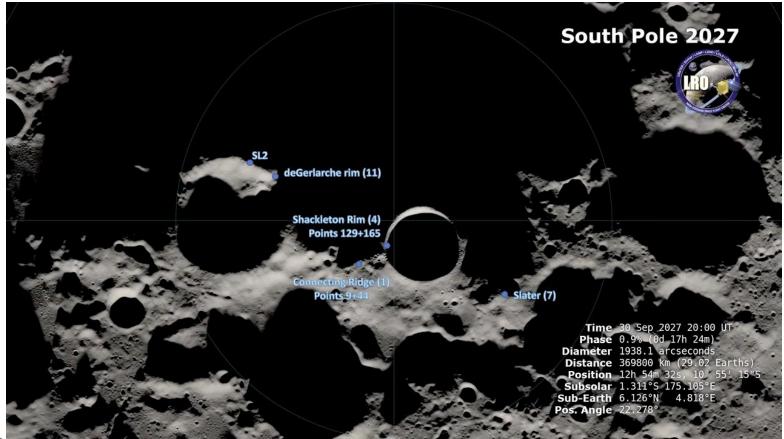
Polar Illumination Limits

- Intermittent with up to 100 hours darkness
- Highly dependent on location/elevation



90-Day Illumination Cycle at South Pole





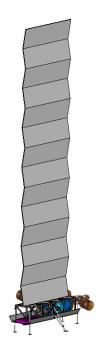


Building Blocks: Generation





Photovoltaic Arrays
50 kW_e increment, vertical deployment
~200 VDC output

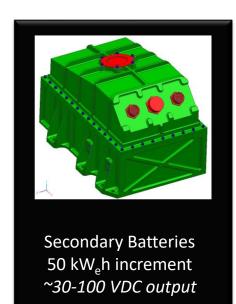


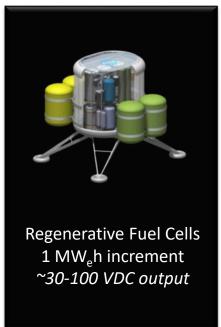
Fission Power 40 kW_e increment ~240 VAC/50 Hz output

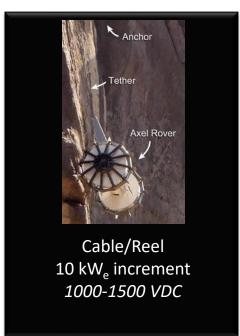


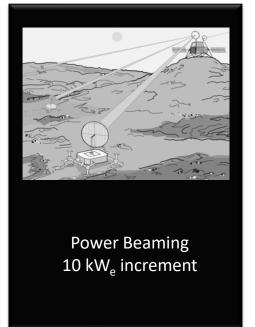
Building Blocks Energy Storage and Power Transmission

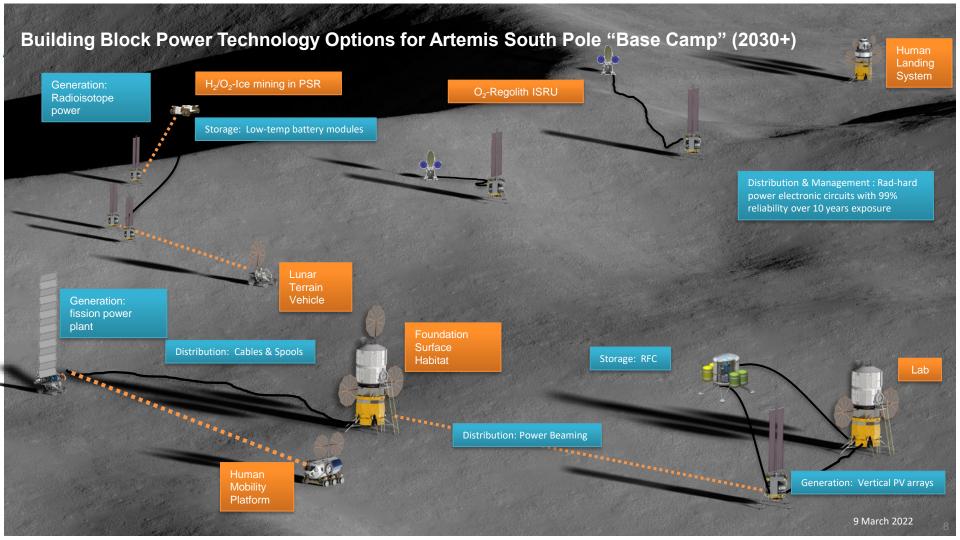






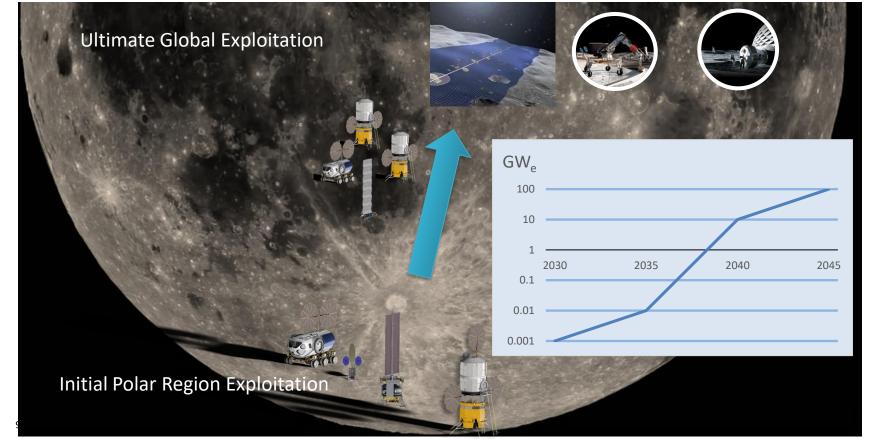


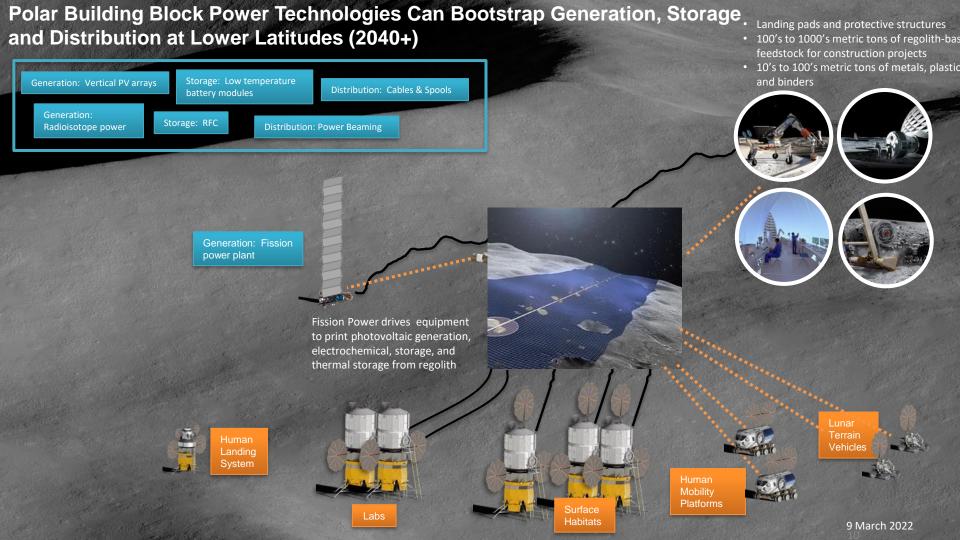




Power demand will incrementally increase as surface exploitation grows from initial Artemis installations at the South Pole to intensive industrial and settlement development across the Lunar surface







APEC Envisioned Future Capabilities



11

Power Electronics:

Bring to TRL 6 by 2030 a suite of power management, control, and regulation circuits & software operating at up to 1000 V and at maximum specific power and which are <u>maintainable</u> in the Lunar dust environment and 0.99 reliable for 10 years in the relevant Lunar radiation and thermal environments and in the Lunar hard vacuum and Mars atmosphere environments.



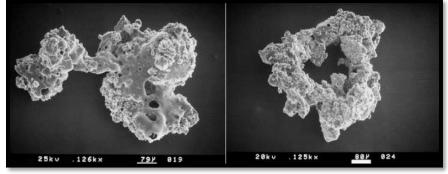


Lunar Environments



Lunar Surface Temperatures

-173 C to 130 C (-250 C in Permanently Shadowed Regions)



Lunar regolith (i.e. lunar dust) is angular, abrasive, irregular in shape, small in size, and adheres to surfaces

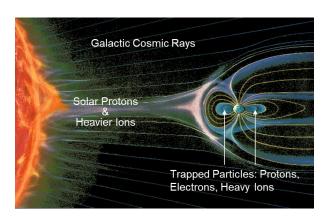


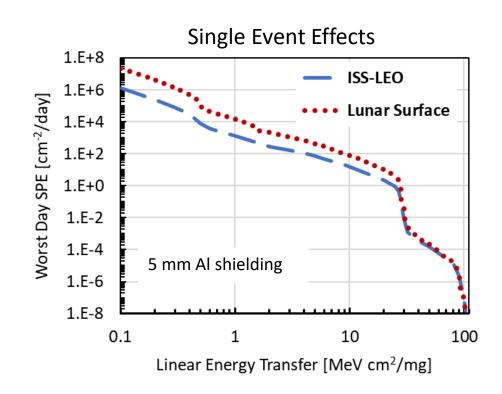
APEC Lunar Radiation Environments



Total Ionizing Dose

Orbit	~1-year TID (2.5 mm AI) krad(Si)
Jovian	1000
GEO	150
Lunar Orbit	4
Lunar Surface	2
ISS LEO	2
Earth Surface	< 0.001

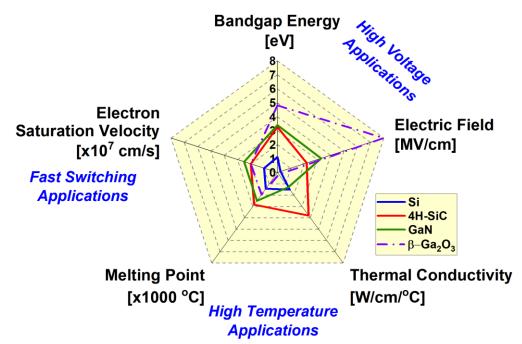






Power Electronics Materials





Graphic: courtesy J.-M. Lauenstein, NASA/GSFC

"Achilles' Heel" of Wide Band Gap Materials: Single Event Effects(SEE)

SiC Diode

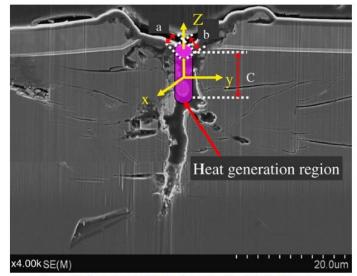


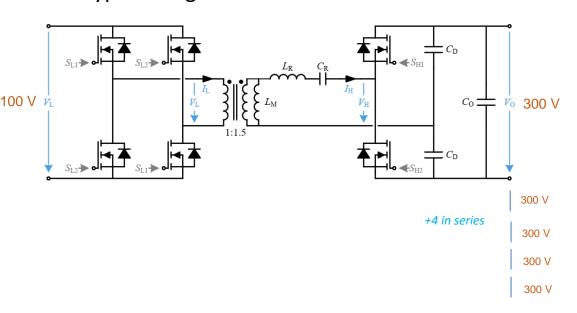
Image: Shoji, et al., © (2014) The Japan Society of Applied Physics, used with permission.



APEC Power Management Circuit Topology



Typical Staged Boost Convertor Module



Breadboard PCB



Barchowsky et al, NASA JPL, IEEE Aerospace Conf. 2022



Challenge



Bring to TRL 6 by 2030 a suite of power management, control, and regulation circuits & software operating at up to 1000 V and at maximum specific power and which are maintainable in the Lunar dust environment and 0.99 reliable for 10 years in the relevant Lunar radiation and thermal environments and in the Lunar hard vacuum and Mars atmosphere environments.

NASA Proposal Opportunities:

Announcement Links:

2022 STMD Tipping Point Announcement:

https://tinyurl.com/2022TippingPoint

2022 STMD Announcement of Collaboration Opportunity (ACO):

https://tinyurl.com/2022ACO

Next Key Date: Mini Proposals Due: 3/31/2022



