

Toward accurate thermal modeling of phase change material based photonic devices

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ABSTRACT

Reconfigurable or programmable photonic devices are rapidly growing and have become an integral part of many optical systems. The ability to selectively modulate electromagnetic waves through electrical stimuli is crucial in the advancement of a variety of applications from data communication and computing devices to environmental science and space explorations. Chalcogenide-based phase change materials (PCMs) are one of the most promising material candidates for reconfigurable photonics due to their large optical contrast between their different solid-state structural phases. Although significant efforts have been devoted to accurate simulation of PCM-based devices, in this paper, we highlight three important aspects which have often evaded prior models yet having significant impacts on the thermal and phase transition behavior of these devices: the enthalpy of fusion, the heat capacity change upon glass transition, as well as the thermal conductivity of liquid-phase PCMs. We further investigated the important topic of switching energy scaling in PCM devices, which also helps explain why the three above-mentioned effects have long been overlooked in electronic PCM memories but only become important in photonics. Our findings offer insight to facilitate accurate modeling of PCM-based photonic devices and can inform the development of more efficient reconfigurable optics.

Keywords. Phase change materials, Amorphization, Temperature, Thermal conductivity.

1 Introduction

Reconfigurable optics ranging from zoom lenses to tunable optical filters have recently garnered great interests for more compact and energy efficient systems¹⁻⁵. One of the quintessential examples of the necessity for reconfigurable optics lies in the most powerful telescope ever built, *The James Webb Space Telescope (JWST)*. At the heart of JWST there is a near-IR camera that enables imaging a wide variety of electromagnetic spectra from 0.6 to 5 μm . In order to selectively choose different electromagnetic wave spectra, there are 29 different passive optical filters sitting on two separate filter wheels which are mechanically swapped depending on the operation mode⁶⁻⁸. As amazing as this technology is, it requires bulky and complex components with mechanically moving parts that are not ideal for applications in space. Ideally, for adjusting the operation mode and imaging at different wavelengths, all 29 filters should be replaced by one unique tunable filter that can be electrically switched between different passband wavelengths with high speed and fidelity⁹. Light detection and ranging (LiDAR) is another technology that could benefit from non-mechanical light modulators for higher efficiency beam steering and scanning capabilities¹⁰. Thus far, depending on the application, a wide variety of techniques have been proposed to modulate the electromagnetic waves using thermo-optical^{11,12}, electro-optical¹³, magneto-optical^{14,15}, opto-mechanical¹⁶, and acousto-optical effects¹⁷. Nonetheless, there is still a dire need for more efficient non-mechanical tunable optics that are fast, robust, and compatible with standard semiconductor foundry fabrication processes.

One of the potential material candidates for reconfigurable optics is chalcogenide-based phase change materials (PCMs) that undergo a solid-state phase transformation upon thermal stimuli. These materials have large optical property contrast between their amorphous and crystalline states, which makes them suitable for modulating electromagnetic waves. Another important feature of PCMs is their analog nature which enables a continuous range of properties depending on the amorphous to crystalline ratio in the material. For instance, it has been demonstrated that $\text{Ge}_2\text{Sb}_2\text{Te}_5$ (GST) can be used as a coating

material to thermally camouflage objects from the background via its variable emissivity at different phases¹⁸. More recently, PCMs have been integrated into metasurfaces in order to develop tunable properties^{1,19–24}.

To better understand the operation of these devices and rationally guide their design, considerable efforts have been dedicated to precise modeling of PCMs' phase transition behavior^{25,26}. However, to date, the vast majority of the investigations have focused on electronic memory configurations, which do not apply to photonic devices where electrothermal switching via external micro-heaters rather than direct current injection is employed²⁷.

In addition, for the application of PCMs in functional photonic devices, it is preferred to switch a larger volume of PCM to increase its overlap with the optical mode, thereby maximizing the optical contrast^{28,29}. While this is less of a concern for memory applications where the volume of phase change units are on the order of a few nanometers^{30,31}, achieving reversible switching of PCM at large length scales on the order of microns or greater presents several challenges in comparison to conventional nanoscale memory cells. These challenges include slower heat dissipation, greater atomic migration, higher power consumption, and potential delamination, all of which can negatively impact the uniformity and durability of the phase transition^{32,33}. Therefore, thermal management plays a critical role in improving the performance of PCM-based devices, especially as their length scales increase.

Furthermore, while several studies have explored thermal and kinetic modeling of the PCM crystallization process^{34–38}, the amorphization transition has received much less attention. We argue that the ability to accurately characterize the amorphization process is equally—if not more—important. Amorphization involves raising the PCM above its melting point ($>600^\circ\text{C}$) followed by rapid cooling ($\sim 10^9\text{--}10^{12}\text{ K s}^{-1}$)^{39,40}, which is a far more thermally vigorous process than crystallization. Therefore, the reliability and endurance of PCM devices are largely impacted by amorphization⁴¹. It has also been shown that the amorphization process can influence the crystallization speed in the subsequent cycle^{40,42,43}, and thus it plays a consequential role in the repeatability of the switching process, especially during multi-level operation^{44,45}.

In this paper, we examined the impact of three previously largely overlooked effects—the enthalpy of fusion, temperature-dependent heat capacity, as well as the thermal conductivity of liquid-phase PCMs—on its phase transformation. The results presented here would enable accurate modeling of PCM devices while providing insight into the development of large-scale, robust, and energy-efficient PCM-based devices.

2 Device Configuration and Thermal Modeling

In our models, we have adopted a doped Si micro-heater design similar to that reported by Rios *et al.*⁴⁶. Figure 1(a) illustrates 3D schematic of the heater device configuration and the layer structures. The top-view image of the fabricated device is presented in Fig. 1(b,c) using two imaging techniques, scanning electron microscopy (SEM) and near-IR (NIR) camera. In this configuration, the PCM pixel size is 70% of the underneath Si heater, with lateral sizes of $140\ \mu\text{m}$ and $200\ \mu\text{m}$, respectively. We choose Si heaters because they are transparent in both the near-infrared (NIR) and mid-infrared (MIR) regimes and have shown to have reliable stability^{45–47}. This enables operation in the transmissive mode instead of reflective, a critical factor in the development of reconfigurable optics such as meta-lenses, filters, and beam steering. Moreover, Si heaters are also compatible with standard foundry manufacturing⁴⁸ to facilitate scalable deployment of PCM-based devices. In order to switch PCM between amorphous and crystalline phases, we use a function generator and a power supply to deliver the high current necessary to heat up the Si heater to sufficiently high temperatures. The resistance of the devices varies in the range of $40\text{--}50\ \Omega$, which requires voltages from $36\ \text{V}$ to $44\ \text{V}$ for amorphization depending on the device resistance. The conclusions drawn from our study are equally applicable to other electrothermal PCM switching configurations as well.

Table 1. Room temperature properties of materials used in the simulations.

| Film | Thickness (nm) | Thermal Conductivity ($\text{W m}^{-1}\text{ K}^{-1}$) | Specific Heat ($\text{J kg}^{-1}\text{ K}^{-1}$) | Density (kg m^{-3}) |
|--------------------------------|----------------|--|--|--------------------------------|
| SiN _x | 875 nm | 2.0^{49} | 1631 | 3100 |
| Al ₂ O ₃ | 25 nm | 2.0^{50} | 880 | 3950 |
| GSST | 210 nm | $\kappa(T)^{51}$ | $240^{51,52}$ | 6200 |
| SiO ₂ | 30 nm | 1.35^{53} | 1000 | 2650 |
| Si heater | 180 nm | 135^{53} | 710 | 2329 |

In order to examine the thermal transport and temperature distribution within the proposed device architecture, we utilized finite element simulation software (COMSOL Multiphysics). A 2D model of the device was created based on its dimensions, which were established through scanning transmission electron microscopy and are illustrated in Fig. 1(b,e). The device's

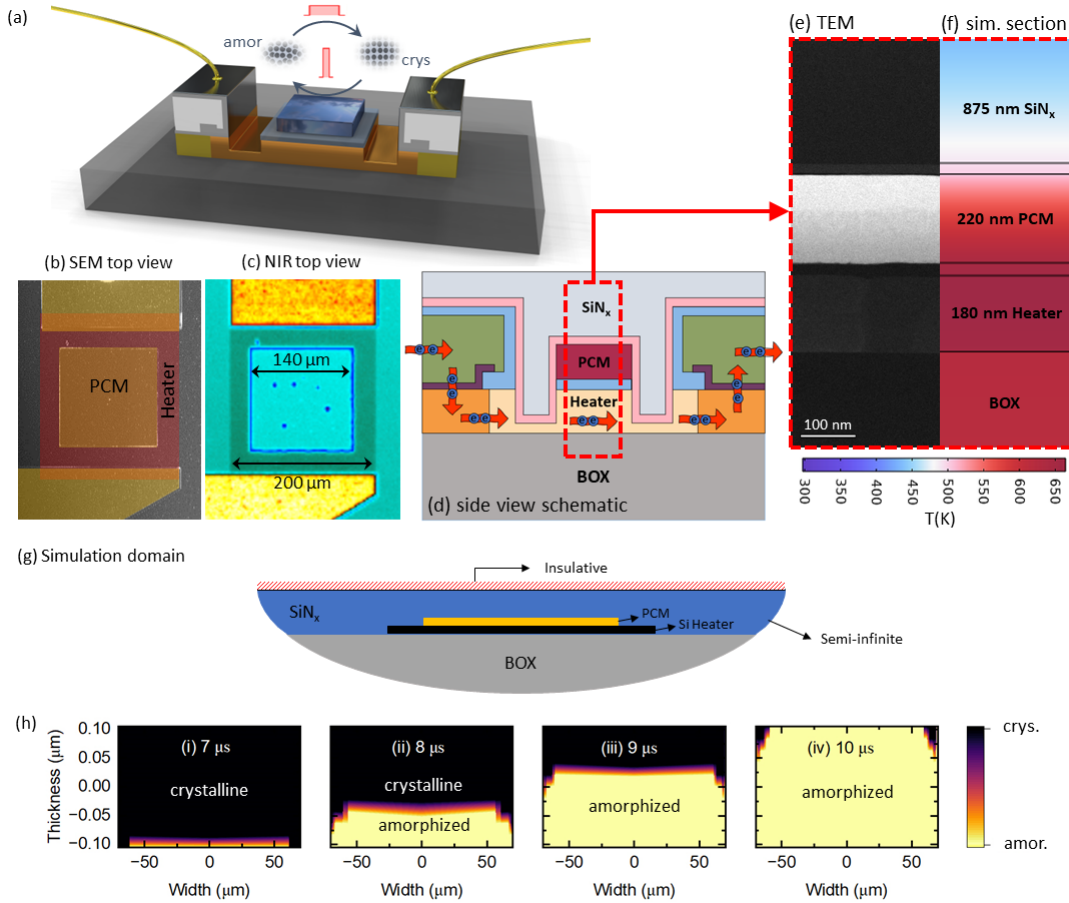


Figure 1. This figure illustrates the configuration of PCM-based device studied in this paper. Panel (a) provides a 3D schematic of the device configuration, while panels (b) and (c) show top view images obtained using scanning electron microscopy (SEM) and near-infrared (NIR) camera (FLIR A6262) with a wavelength range of 600 - 1700 nm. Panels (d-f) depict cross-sectional view of the device with all the layers adjacent to the PCM, accompanied by corresponding transmission electron microscopy (TEM) image and simulation sections that are the focus of this study. The simulation domain, along with the relevant boundary conditions used in this study, are presented in panel (g). (h) The expansion of the amorphous region within the crystalline state due to thermal excitation from the heater underneath. The time stamp denotes the duration since the initiation of the heating pulse. Note that the images are not drawn to scale.

cross-section schematic was used as the basis for the model, as shown in Fig. 1(d). Our primary focus in this study is investigating the amorphization process, so we assumed that the initial state of the PCM is fully crystalline, unless stated otherwise. The simulations were performed at room temperature, with constant temperature set to 24 °C at the boundaries of the simulation domain far away from the device (Fig.1(g)). To ensure accurate modeling, we selected a domain size 10 times larger than the device width, and we validated this assumption by testing matrix lengths ranging from 10 to 50 times larger, finding no impact on the device's temperature distribution. Given the microscale dimensions and fast thermal transport timescale in addition to the exceptionally low thermal conductivity of air compared to other materials within the device, it is a reasonable assumption to disregard convection and radiation effects and set the top surface as an insulative boundary condition. Further, we assumed a constant thermal boundary conductance of $100 \text{ MW m}^{-2} \text{ K}^{-1}$ across all interfaces to capture interfacial thermal resistance between different materials³². The thermal conductivity of the PCM layer, denoted as $\kappa(T)$, was assumed to vary with temperature based on Refs.⁵¹, while the remaining material thermal properties for the simulated non-PCM layers were considered to be temperature-independent.

In our investigation of the amorphization process, we take $\text{Ge}_2\text{Sb}_2\text{Se}_4\text{Te}$ (GSST) which is a widely used PCM for optical applications due to its low loss absorption in the infrared regime^{54,55}. We presume that once the temperature of the PCM surpasses the melting point 600 °C, the liquid part completely transitions into the amorphous phase once cooled down. The simulation results depicted in Fig. 1(h) shows the progression of the amorphized region in relation to the base crystalline state

upon thermal excitation. It is important to note that the contour plots are not drawn to scale, as the width of the PCM is 1000 times greater than its thickness.

In order to experimentally illustrate the amorphization process, we deposited $140\ \mu\text{m} \times 140\ \mu\text{m}$ of GSST on a $200\ \mu\text{m} \times 200\ \mu\text{m}$ transparent silicon heater. To prevent oxidation and improve heat dissipation during the amorphization process, we encapsulated the device with 875 nm sputtered SiN_x . While the device configuration utilized in this study is primarily intended for transmission measurements, it is important to note that for the purpose of this particular investigation focused on illustrating the growth of the amorphized region, we are solely examining the reflectivity of the surface. Using a near-infrared camera (FLIR A6262), which operates in 600-1700 nm wavelength range, we monitored changes in the PCM's reflectivity as we sent successive amorphization pulses. The GSST is initially in the crystalline phase. Upon sending incremental amorphization pulses that are separated by 30 seconds, we ensured the device reached equilibrium at room temperature before each image was collected.

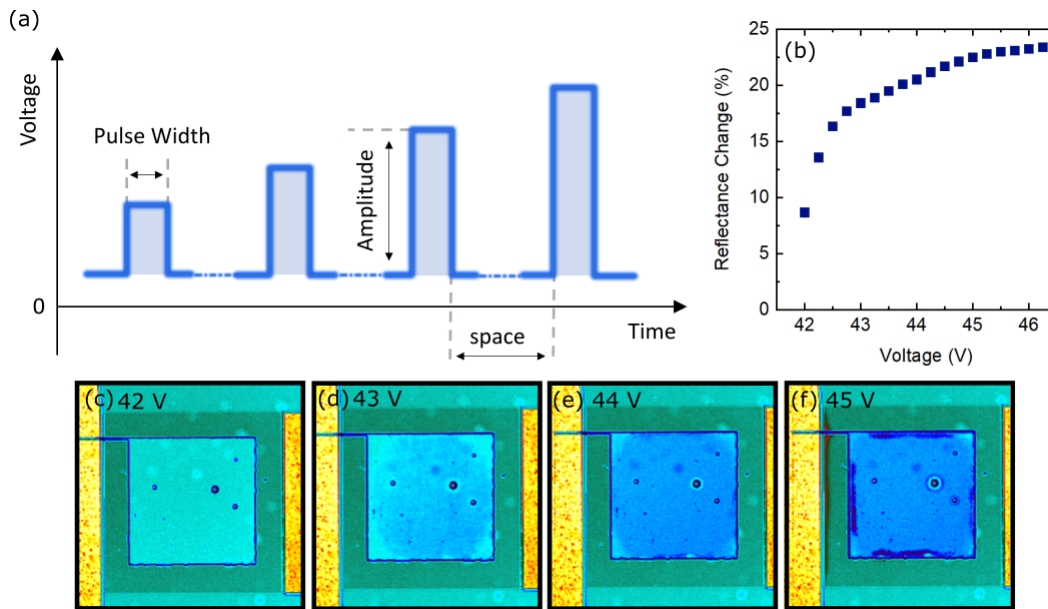


Figure 2. (a) Incremental sequential pulses that are applied to GSST on a heater with $10\ \mu\text{s}$ pulse width and 30 seconds interval period. (b) Changes in the reflectivity of the GSST with respect to base crystalline phase as a function of applied voltage. (c-f) Images from the surface of the GSST upon application of different voltages and progression of the amorphous phase.

Figure 2 shows the incremental pulses applied to the PCM and the percent change in the reflectivity of the surface with respect to the applied voltage. As can be seen in the images presented in Fig. 2(c), the sample did not show any change for pulses with amplitudes up to 42 V. As soon as the voltage exceeded this threshold, the reflectivity of the center of the sample began to decrease which is indicative of partial amorphization. On the other hand, at the boundaries of PCM and near the corners, Fig. 2(d), we observed no change in the color of PCM, which we believe is due to non-uniform temperature distribution across the PCM. This is consistent with our simulated temperature distribution in the PCM for low amplitude pulses that lead to partial amorphization, consistent with the results presented in Fig. 1(h). Upon increasing the pulse voltage, the change in the color became more pronounced and the unswitched crystalline regions closer to the edges began to amorphize. From these results we can see the reflectivity plateaus at 45.5 V where upon increasing the voltage no observable change in the reflectivity of the PCM was detected. By approximating the changes in the reflectivity of the PCM with respect to its initial crystalline phase, we observed an $\sim 23\%$ change in the reflectivity upon complete transformation to the amorphous state. In the following section, we will elaborate on several important factors that should be properly considered to enable accurate modeling of the progressive amorphization process shown here.

3 Enthalpy of Fusion and Heat Capacity of Supercooled Liquids

In this section, our focus is on examining the impact of enthalpy on the phase transformation of PCMs, specifically GST and GSST. As experimental data for the enthalpy of GSST is unavailable, we utilize the values from GST and apply them to both

cases. Figure 3(a) plots the classical enthalpy-temperature curves of a glass-forming solid. ΔH_c and ΔH_f denote the enthalpies of crystallization and fusion in GST, respectively. The former gives the enthalpy difference between a solid's amorphous and crystalline states whereas the latter, also known as the latent heat of melting, represents the energy required to change the state of a crystalline solid unit mass to liquid at the melting point, without a temperature increase. The other important feature evidenced by this figure is that while the heat capacity of the crystal solid (represented by the slope of the curve) is relatively insensitive to temperature up to its melting point, the heat capacity of the amorphous phase increases considerably from the glassy state near room temperature to the supercooled liquid state past glass transition. This is attributed to the increasing degrees of freedom that the atoms can access in the liquid phase versus in the solid phase⁵⁶.

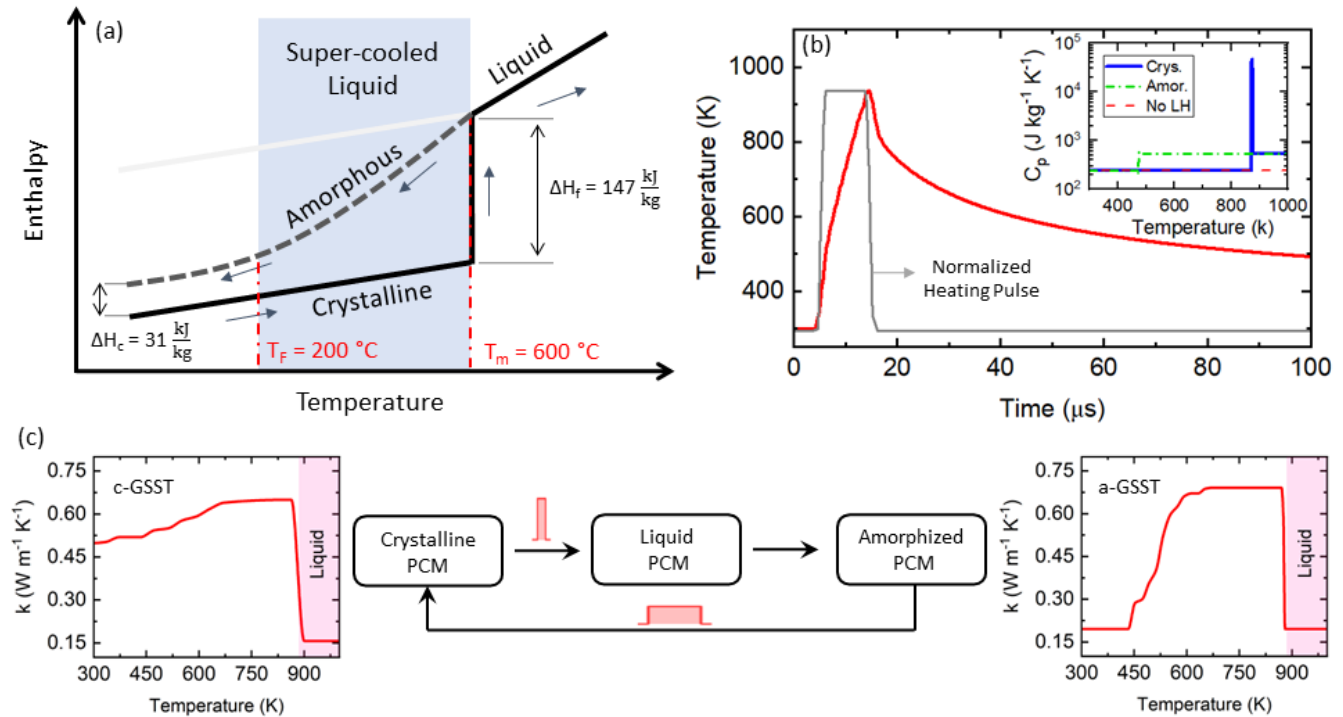


Figure 3. (a) The enthalpy-temperature curves for GST, showcasing classical patterns observed during solid-liquid phase transition as well as glass transition, and (b) maximum temperature profile and pulse shape within the PCM during the amorphization cycle. The inset shows heat capacity trend as a function of temperature for crystalline and amorphous phases, as well as when latent heat (LH) of melting is not considered. (c) The switching cycle and the corresponding thermal conductivity trend as a function of temperature for the amorphous and crystalline phase of GSST. The thermal conductivity data below the melting point is sourced from reference⁵¹, while for temperatures above the melting point, we have made informed speculation.

The significance of the enthalpy-temperature relation in the PCM switching process is two-fold. First, during amorphization, additional heat must be supplied to convert the solid phase into liquid and during the process, the temperature at the solid-liquid interface is held constant. This effect, even though not accounted for in some prior studies^{57,58}, can significantly alter the temperature distribution and solid-liquid interface location. For PCMs, the ΔH_f ranges from 98 to 147 kJ/kg^{52,59-61}, and the corresponding latent heat is equivalent to the energy needed to raise the temperatures by nearly 600 K. Therefore, the enthalpy of fusion is an important factor in thermal modeling especially when the PCM device size is large, as we will explain later.

The second implication is more subtle and to our knowledge has not been explicitly discussed in literature. In previous thermal simulations of PCM-based photonic devices⁶²⁻⁶⁹, the heat capacity of amorphous PCM has been quoted as a constant up to the melting point. However, as can be seen from Fig. 3(a), the heat capacity of amorphous PCM is not a constant and must increase when transitioning into the supercooled liquid state, or when heated to above its Fictive temperature. Assuming a constant heat capacity will underestimate the amount of energy needed to bring the amorphous PCM to the melting point, roughly by $\Delta H_f - \Delta H_c$. In PCMs, ΔH_c ranges between 31–65 kJ/kg^{52,70-73}. For GST, $\Delta H_f - \Delta H_c = 116 \text{ kJ/kg}$. The temperature-dependent heat capacity can therefore significantly impact thermal simulation results in PCM devices, as we will show later.

In order to demonstrate the impact of enthalpy of fusion on the phase transformation during the amorphization process, we

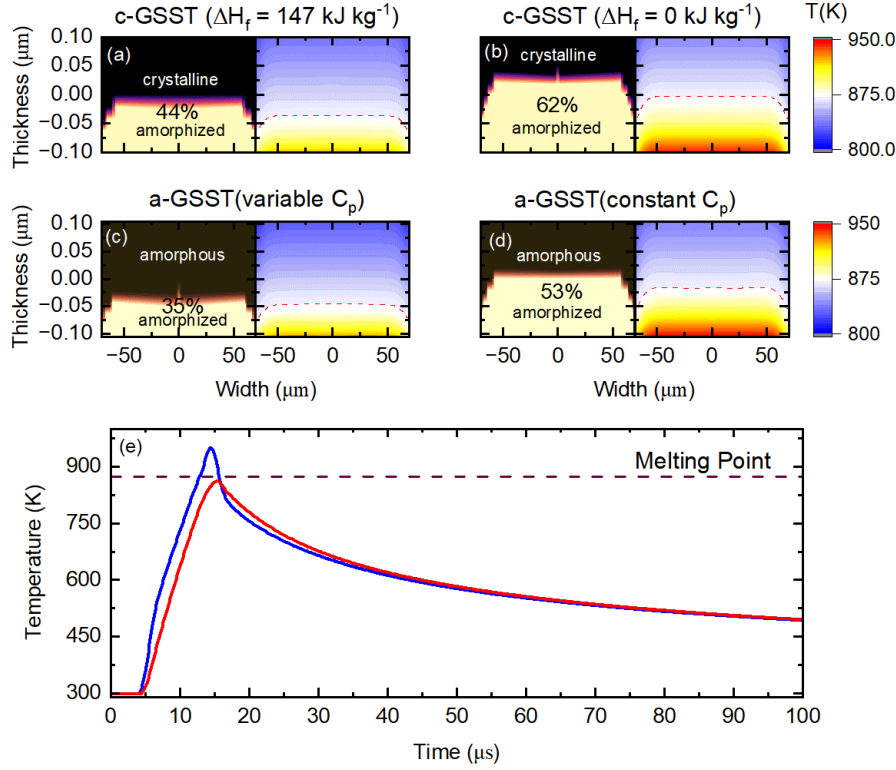


Figure 4. This figure illustrates the phase transformation and temperature distribution in PCM for GSST after amorphization pulse under various conditions, including: (a) considering the enthalpy of fusion ($\Delta H_f = 147 \text{ kJ kg}^{-1}$), (b) disregarding the enthalpy of fusion ($\Delta H_f = 0 \text{ kJ kg}^{-1}$), (c) variable heat capacity and (d) constant heat capacity as a function of temperature. The contour plots are not drawn to scale, as the width of the PCM is 1000 times greater than its thickness. The melting temperature threshold is denoted by the dashed red line in the 2D temperature contour. Additionally, the maximum and minimum temperatures in PCM and the corresponding temperature gradient with respect to time are shown in (e).

consider two cases: one where latent heat of melting is taken into account ($\Delta H_f = 147 \text{ kJ kg}^{-1}$), which appears as a spike in heat capacity between 870 K and 876 K as shown in the inset in Fig. 3(b), and one where it is (erroneously) not considered ($\Delta H_f = 0$). Figure 3(c) illustrates the switching cycles in PCM-based devices that undergo a liquid phase during amorphization, as well as the corresponding thermal conductivity trend as a function of temperature for both the crystalline and amorphous phases.

Figures 4(a-b) show the different modeling outcomes of the amorphization cycle in the GSST device in the two cases. The contour plots provide a comparison of the percentage of amorphized volume and temperature distribution for GSST when considering the latent heat versus ignoring it. When considering the effect of latent heat, Figs. 4 (a,b) illustrate that at the same input power, a smaller PCM volume undergoes solid-liquid phase transformation. This is because some of the energy in the crystalline phase is utilized to overcome the latent heat of melting, resulting in a lower temperature increase, less molten GSST, and consequently, less amorphization. Figures 4(a,b) demonstrate that neglecting the impact of latent heat would result in a nearly 18% overestimation of the amorphized area and temperature distribution in the PCM for an input power of 130 mW.

Next we examine the influence of the temperature-dependent heat capacity of PCMs. Unfortunately, experimental data on the heat capacity data of the (supercooled) liquid phase of PCMs are not available, and therefore we will need to make some educated assumptions. We start by assuming that the heat capacity remains the same as the room temperature value up to the Fictive temperature T_F , beyond which the heat capacity abruptly increases to the (supercooled) liquid phase value. This is a valid approximation as glass transition typically occurs over a narrow temperature window compared to the entire amorphization process. We further assume a Fictive temperature of $T_F = 200 \text{ }^\circ\text{C}$, which corresponds to the approximate temperature range of glass transition measured in GST. If we further quote the $\Delta H_f = 147 \text{ kJ/kg}$ and $\Delta H_c = 31 \text{ kJ/kg}$ values from GST, then the heat capacity jump at T_F can be obtained from $\Delta C_p = (\Delta H_f - \Delta H_c)/(T_m - T_F)$, where T_m represents the melting point. Following this, we take a constant heat capacity of $240 \text{ J kg}^{-1} \text{ K}^{-1}$ and $528 \text{ J kg}^{-1} \text{ K}^{-1}$ for below and above T_F , respectively. In this case, since we are studying the melting of an amorphous phase, we assume that the amorphization

pulse is sent to an already amorphous PCM. This may seem redundant, but it provides critical insights for investigating the thermal properties of the amorphous phase in multi-level programming, where a mixture of amorphous and crystalline phases is present. As shown in Fig. 4(c-d), assuming a constant heat capacity as a function of temperature, similar to the crystalline phase, over-predicts the amorphization and temperature rise in the PCM. Figure 4(e) shows the temperature rise as a function of time during crystalline-to-amorphous phase transition for location points in the GSST; center and closest to the heater (blue), and center farthest from the heater (red). According to this plot, the temperature gradient along the thickness of the GSST can reach as high as ~ 104 K at the peak temperature. This large temperature gradient could lead to non-uniformity and reduced lifetime. Further, it is important to note that, in all cases, the crystalline phase leads to a higher amorphization rate in the PCM than the amorphous phase, which is attributed to the higher thermal conductivity of the crystalline phase compared to the amorphous phase. The subsequent section will discuss the importance of the thermal conductivity of PCM during the amorphization process.

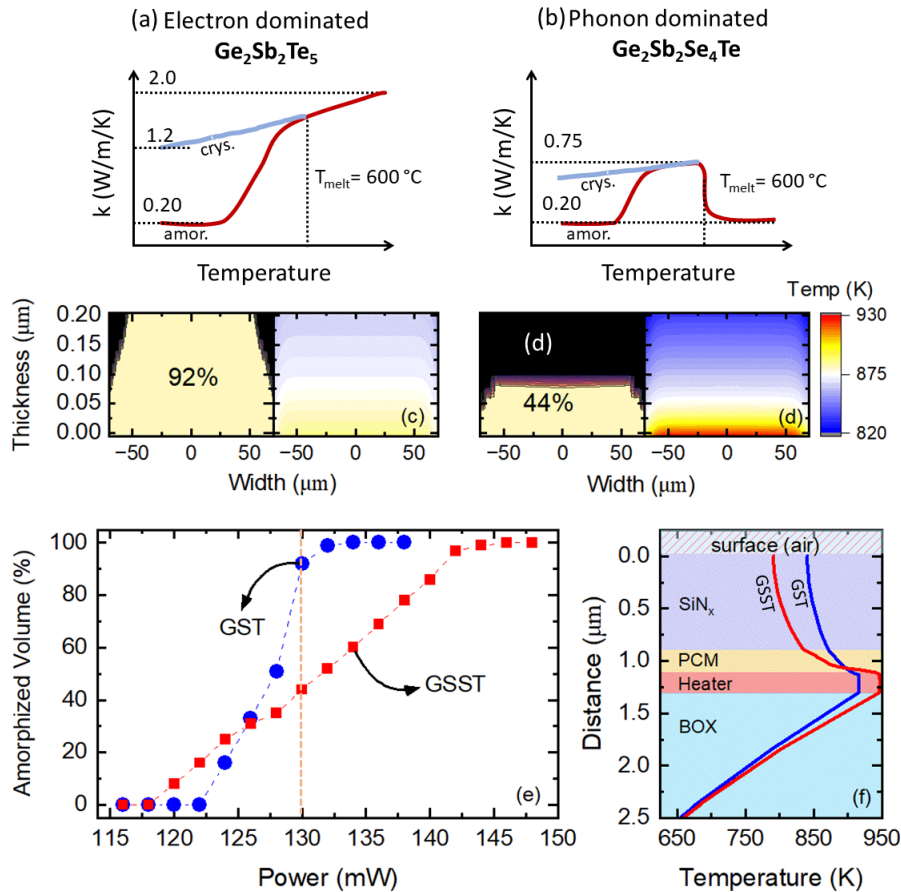


Figure 5. (a,b) Qualitative thermal conductivity trend for GST and GSST with respect to temperature. The actual values for temperature dependant thermal conductivity that are used in the simulations can be found in Ref.⁵¹. (c,d) PCM thermal conductivity effect on the phase transformation and temperature distribution. (e) Percentage of amorphized PCM at different input power for GST and GSST. (f) The through-plane temperature profile at the midpoint of the device for GSST and GST at the end of the amorphization pulse (after 10 microseconds).

4 Thermal Conductivity of Liquid Phase

From previous section, we observed that thermal conductivity of the PCM phase plays an important role in the degree of amorphization. For developing reliable devices with an extended lifetime, a uniform temperature distribution across the PCM during the amorphization cycle is a necessity. Theoretically, for triggering amorphization of the PCM, we need to deliver enough power to uniformly raise its temperature a few degrees above the melting point. Nonetheless, considering

most PCMs have typically low thermal conductivity (< 1 W/m/K), as the device length scale increases, the formation of a non-uniform temperature profile is inevitable. Using the right combination of materials and device design, we can mitigate this non-uniformity and potentially help improving the device durability. For the PCM with embedded heater configuration which is the focus of this study, we demonstrated that the center of the device at the interface between the PCM and heater reaches the highest temperature during the amorphization cycle leading to a flat-dome shaped amorphized region. Thus, for increasing the amorphized volume, typically the heater must operate tens of degrees higher than the melting temperature of the PCM to amorphize regions away from the heater. This, however, could be a major issue as a higher temperature would create a localized hot spot and impose a larger thermal strain on the device. In this section, we discuss the importance of PCM intrinsic thermal conductivity for a more uniform temperature distribution and greater phase transformation.

Although most phase change materials have intrinsically low thermal conductivity, depending on their compositions, they behave significantly differently at high temperatures, especially in the liquid phase. For instance, it has been shown that the thermal conductivity of fully crystalline GST is driven by electrons whereas in fully crystalline GSST, the electronic contribution is significantly suppressed and the thermal transport is driven by phonons⁵¹. This means that the GST thermal conductivity increases with temperature as depicted in Fig. 5(a) even in the liquid phase due to the increased contribution of electronic carriers which is also consistent with prior electrical conductivity measurements in the liquid phase⁷⁴⁻⁷⁷. On the other hand, the thermal conductivity of GSST is expected to drop to its amorphous value due to disruption of periodicity and emergence of disorder in the atomic structure (see Fig. 5(b)). It is noted that there are no reports for the thermal conductivity of GSST in the liquid phase to validate or refute our hypothesis, yet, we can make an educated speculation about how GSST behave in the liquid phase. Considering this and using temperature-dependent thermal conductivities from Ref.⁵¹, an estimation for the amorphization volume and temperature distribution in GST and GSST is presented in Fig. 5(c-d). From these results, during the amorphization period, we observe a more uniform temperature distribution in the GST layer where the temperature gradient in the entire layer only reaches $\Delta T = 31$ K, whereas for the case of GSST it reaches $\Delta T = 104$ K. As a result of the large temperature gradient in GSST, heat transport and hence amorphization across its volume are hampered. This is evident from Fig. 5(e), where the amorphization fraction in GSST consistently lags behind that in GST even for identical heater configuration and voltage pulse parameters, with an amorphization fraction difference up to 48% at 130 mW power. Furthermore, Fig. 5(f) displays the highest temperature attained at the end of the amorphization pulse (after 10 microseconds) for GSST and GST across various layers in the stack. As per the plot, the maximum temperature recorded for the GSST case is 947 K, whereas for GST, it is 916 K. Thus, if GST is employed as the PCM under identical conditions, the Si heater temperature would remain 31 K lower than in the case of GSST. This marked difference highlights the critical importance of quantifying liquid-phase thermal conductivity to allow for material-specific thermal management and reliable PCM switching in photonic devices.

5 Switching Energy and Size Scaling in PCM-based Devices

This section aims to examine the effect of scaling PCM-based devices on their energy consumption and overall performance. According to Fourier's law, the energy required to heat a material is proportional to its cross-sectional area (A), with $Q = -kA(dT/dx)$, assuming a constant thermal conductivity (k) and temperature gradient (dT). Thus, doubling the area of a material results in doubling of the energy required to generate the same temperature gradient. Nonetheless, in a device configuration, it is not only the PCM that heats up during the switching cycles but also the surrounding materials. The rate of heat loss from PCM to the surrounding materials depends on its surface area, which increases less rapidly than the volume as the material grows in size. Consequently, smaller volumes like PCM-based memory cells in storage devices have a larger surface-to-volume ratio and more contact areas with the surroundings, resulting in a higher proportion of heat being transferred to the surroundings and more intense thermal leakage. This means that, the scaling of PCM-based devices is expected to follow a sub-linear trend. As the length scale of the PCM device increases (more quantitatively as the heater size reaches a few tens of microns), the switching energy is increasingly being dominated by the intrinsic properties of the PCM rather than those of surrounding materials. It therefore comes as no surprise that the aforementioned effects resulting from enthalpy of fusion, supercooled liquid heat capacity and liquid phase thermal conductivity have long been neglected in electronic memories and only become relevant in photonic devices. In order to investigate this, we use our finite element model and change the lateral dimension of the heater and PCM in order to assess the impact of size on the power consumption.

Figures 6(a,b) depict the temperature distribution during the amorphization pulse for two different device sizes. In this particular device architecture, the power consumption is determined by the heater size. To perform the simulations, the device size, or more accurately, the heater size, is varied from $1.5 \mu m$, where the thickness of the PCM (~ 210 nm) is comparable to the device lateral dimension, to $300 \mu m$, where the thickness of PCM is negligible compared to the heater lateral dimension. To enable a meaningful comparison of power consumption among devices with varying heater sizes, we select a $200 \mu m$ heater as a benchmark. For this, an input power of 140 mW is required for a $200 \mu m$ heater to attain 86% amorphization and a maximum temperature rise of around 1000 K when subjected to a $10 \mu s$ pulse. Upon changing the device size, we adjust the input power to get the same level of amorphization and temperature rise in the PCM. Theoretically, since heat (Q) and effective area (A)

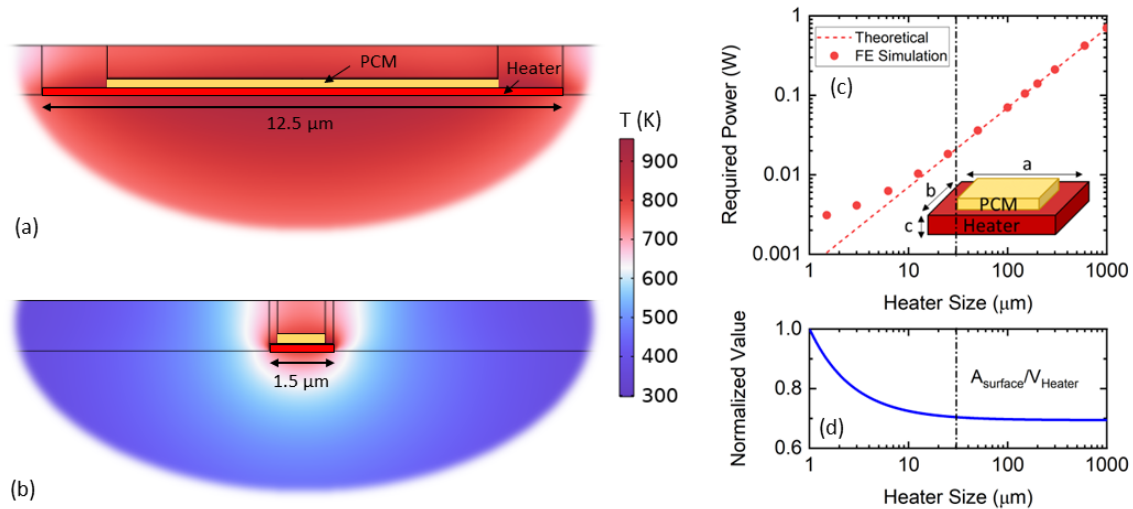


Figure 6. Temperature distribution in the PCM device with embedded heater with lateral dimension of (a) $12.5 \mu\text{m}$ and (b) $1.5 \mu\text{m}$. (c) The theoretical value and the simulation results for device power consumption with respect to the heater size from 1 to $500 \mu\text{m}$. (d) Normalized ratio for surface area relative to volume as the lateral width of the Si heater increases from 1 to $1000 \mu\text{m}$. In the FE simulation, the input power is adjusted to produce a similar temperature rise across all heater sizes.

have a linear relation, as the areal size of the heater decreases, we would expect a linear reduction in the power consumption, where for a heater with a smaller size of $1.5 \mu\text{m}$, 1 mW of power is expected to generate the same temperature rise as in the $200 \mu\text{m}$ heater. This is shown as the dashed line in Fig. 6(c). However, simulations indicate that three times more power input (3 mW) is necessary for the $1.5 \mu\text{m}$ heater to generate the same temperature rise and phase transformation in the smaller heater. In other words, our simulation results for various device lateral dimensions depicted by solid red circles reveal that the power consumption deviates from the linear trend as the heater size decreases below $30 \mu\text{m}$. This deviation occurs due to the fact that the surface area to volume ratio is higher for smaller devices as depicted in Fig. 6(d), resulting in greater thermal leakage from the boundaries to the surrounding environment. Consequently, creating the same temperature rise for the smaller heater size requires more power. This significant difference in power requirements is attributed to the larger surface area of smaller heaters relative to their volume, which leads to greater thermal leakage and, thus, a higher power demand to attain the same temperature rise. On the other hand, our results indicate that for heaters larger than $30 \mu\text{m}$, the surface area to volume ratio remains relatively constant. Consequently, the power consumption follows a linear trend, as expected.

6 Conclusion

In this study, we examined the amorphization process in large-area PCM-based photonic devices. The study specifically examined the impact of enthalpy of fusion, temperature-dependent heat capacity, thermal conductivity of liquid PCM, and scaling of device size on the phase transformation and power consumption. Our results demonstrated the importance of accounting for the enthalpy of fusion and the heat capacity change during glass transition when modeling the amorphization process. Furthermore, according to our simulations, in order to increase the dimensions of PCM-based photonic devices, PCMs with electron-dominated thermal conductivity such as $\text{Ge}_2\text{Sb}_2\text{Te}_5$ are more favorable as their thermal conductivity increases with temperature even after the solid-liquid phase transition. We compared the phase transformation degree and temperature distribution in the PCM layer for an electron-dominated PCM such as $\text{Ge}_2\text{Sb}_2\text{Te}_5$ and a phonon-dominated PCM such as $\text{Ge}_2\text{Sb}_2\text{Se}_4\text{Te}$ and showed that the electron-dominated PCM leads to less temperature rise and more uniform temperature distribution in the PCM layer while providing more than two times higher amorphization volume. Furthermore, we discovered that for devices with lateral dimensions smaller than $30 \mu\text{m}$, the power consumption does not scale linearly with the size of the device due to the increased ratio of PCM surface area to volume. However, for devices with lateral sizes larger than $30 \mu\text{m}$, power consumption increases linearly with device size. Overall, our findings contribute to understanding and accurate modeling of thermal transport phenomena in PCM-based photonic devices and could lead to the development of reconfigurable metasurfaces with functional properties like filters, lenses, and beam steering.

7 Methodology

Transmission Electron Microscopy. The specimen for STEM observation was prepared by lift out via ion-beam technology by using a Focused Ion-beam (FIB) system (Helios G5 UX, ThermoFisher Scientific). Protective amorphous carbon and Pt layers were applied over the region of interest before ion milling in the FIB system. HAADF STEM images were acquired with a transmission electron microscope (Titan cubed G2 60-300, ThermoFisher Scientific) at 300kV with a spherical aberration(Cs) corrector (CEOS GmbH).

Device Fabrication. The specimen for TEM and NIR optical analysis was fabricated on a doped Si-on-insulator platform. The GSST was deposited via thermal evaporation from a pre-weighted fresh source in a custom built deposition chamber. The film was subsequently encapsulated with 20 nm of Al_2O_3 via atomic layer deposition and further encapsulated with 875 nm of reactively sputtered silicon nitride.

8 Disclaimer

Specific vendor and manufacturer names are explicitly mentioned only to accurately describe the test hardware. The use of vendor and manufacturer names does not imply an endorsement by the U.S. Government nor does it imply that the specified equipment is the best available.

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Data availability.

The data that support the findings of this study are available from the corresponding author upon reasonable request.

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Author contributions

K.A., H.J.K., and J.H. designed the study. K.A. performed the simulations. C.C.P, T.G., and J.H. fabricated the device. K.A. performed the electrical biasing measurement. H.B.B. and T.L. performed the SEM and TEM measurements. K.A. and J.H. wrote the manuscript.

Competing interests

The authors declare no competing interests.

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