A BIBLIOGRAPHY ON DIGITAL COMPUTER-AIDED
CIRCUIT ANALYSIS AND DESIGN

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The potential use of digital computer in the analysis and design of electric circuits has been recognized for some time. The first organized effort in technical meetings on the subject seems to be a session on 'Computers in Network Synthesis' in 1957 WESCON Convention when three papers (12, 17, 132) were presented. In 1961 the IRE Transactions on Circuit Theory issued a special number on Network Design by Computer (49, 60, 78, 98, 184). A Computer Program Department has since been inaugurated to the Transactions (84) which collects and publishes titles and reviews of available programs on circuit theory problems. There was a symposium on the Design of Networks with a Digital Computer (27, 47, 160, 171) at 1962 IRE International Convention. In 1963 Lockheed Missiles and Space staff prepared an annotated bibliography on computer-aided analysis and design (142). More recently at the Third Allerton Conference on Circuit and System Theory, October 20-22, 1965, six papers (85, 110, 128, 145, 165, 178) were presented at the session of Network Analysis and Design by Digital Computers.

This bibliography attempts to list all pertinent literature on computer-aided techniques and programs directly connected with circuit analysis and design. Related background material (for example, the Monte Carlo method in digital computation), thought important as they are, are deleted from this compilation. The entries are arranged in the alphabetic order of the last name of the first author of each paper. A subject index and an chronological index are appended.

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</tbody>
</table>

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<th>Page Numbers</th>
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</thead>
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</tr>
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<tr>
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</tbody>
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</tr>
</tbody>
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</tr>
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<tr>
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<tr>
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</tr>
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</tr>
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</tr>
</tbody>
</table>

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<tr>
<th>Page Numbers</th>
</tr>
</thead>
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</tr>
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</tbody>
</table>

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</tr>
</tbody>
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</tbody>
</table>

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</tr>
</thead>
<tbody>
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<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
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<td>180</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
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<td></td>
<td></td>
<td></td>
</tr>
<tr>
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<td></td>
</tr>
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<td></td>
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</tr>
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<td></td>
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</tr>
<tr>
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<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
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<td>101</td>
<td>156</td>
<td></td>
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<td></td>
</tr>
<tr>
<td>Year</td>
<td>159</td>
<td>99</td>
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<td>1953</td>
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<td>1954</td>
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<td>1955</td>
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<td>1956</td>
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<td>1957</td>
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<td>1958</td>
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<td>1960</td>
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<td>1961</td>
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<td>1962</td>
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<td>1963</td>
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<td>84</td>
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<td>102</td>
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<tr>
<td>1965</td>
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<td>53</td>
<td>72</td>
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<td>79</td>
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