General Disclaimer

One or more of the Following Statements may affect this Document

- This document has been reproduced from the best copy furnished by the organizational source. It is being released in the interest of making available as much information as possible.

- This document may contain data, which exceeds the sheet parameters. It was furnished in this condition by the organizational source and is the best copy available.

- This document may contain tone-on-tone or color graphs, charts and/or pictures, which have been reproduced in black and white.

- This document is paginated as submitted by the original source.

- Portions of this document are not fully legible due to the historical nature of some of the material. However, it is the best reproduction available from the original submission.

Produced by the NASA Center for Aerospace Information (CASI)
This report was prepared to document work sponsored by the United States Government. Neither the United States nor its agents the United States Department of Energy, the United States National Aeronautics and Space Administration, nor any federal employees, nor any of their contractors, subcontractors or their employees, make any warranty, express or implied, or assume any legal liability or responsibility for the accuracy, completeness, or usefulness of any information, apparatus, product or process disclosed, or represent that its use would not infringe privately owned rights.
# TABLE OF CONTENTS

<table>
<thead>
<tr>
<th>Report</th>
<th>Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>First Quarterly Report</td>
<td>1</td>
</tr>
<tr>
<td>Second Quarterly Report</td>
<td>9</td>
</tr>
<tr>
<td>Third Quarterly Report</td>
<td>18</td>
</tr>
</tbody>
</table>

iii
RHO SIGMA, INC.

Contract NAS8-32256

First Quarterly Report (DRL-10)

February 1, 1977
TO: James D. Hankins
FROM: Joseph L. Imholte
       Rho Sigma, Inc.
SUBJECT: Monthly Status Report
CONTRACT: NAS9-32256 Month Ending 30 November 1976

1. TECHNICAL PERFORMANCE

During the month ending 30 November 1976, Rho Sigma completed the preliminary research for the design of the microprocessor control subsystem. An industry survey was conducted for the requirements to be met by a microprocessor controller. Meetings were held with other companies in the solar industry and discussed their needs and requirements for the future. This industry survey is discussed further in Section IV.

A study was also started on the microprocessor field to enable Rho Sigma to best determine the type of microprocessor to be used which will allow the greatest system flexibility and still be competitive with the remainder of the industries.

Rho Sigma has completed their preliminary table of specifications for the controller and has an initial block diagram matching the requirements as determined by the above study.

cc: McMurray
II. Schedule:

Enclosure A shows, via bar chart, the progress Rho Sigma has made toward the completion of the contract. The requirement study is complete and work on the microprocessor study is nearing completion. The preliminary work on the system specification and block diagram is also complete.

III. Cost:

This item has been deleted.

IV. Industry Survey:

During the course of this reporting period a number of industrial organizations were contacted to discuss requirements for advanced control systems. These organizations included:

IBM, Huntsville, Alabama
Westinghouse, Baltimore, Maryland
General Electric, King of Prussia, Pennsylvania
A.O. Smith Company, Kankakee, Illinois
TRW Systems, Los Angeles, California
Solaron, Inc., Denver, Colorado

From these discussions several aspects of control systems requirements became apparent. One of the major problems with current controls results from the use of "two-stage" thermostats. This type of thermostat turns on the solar heat when a demand is present. If the room temperature continues to drop, indicating insufficient solar heat capacity available, the second stage cuts in generally about $2^\circ F$ difference, turning on the back-up system. The problem arises when the thermostat is "set-back" at night time, and the house cools to a relatively low temperature ($60-63^\circ F$). In the morning, the
thermostat is manually advanced to 70° F. Since the thermodynamic lag of the house is long, the difference between the instantaneous thermostat setting is 70° F - 63° F = 7° F. Hence, even if adequate solar energy is in storage, the back-up heater is energized to raise the room temperature.

Another key point that became apparent is the increasing use of air systems. All of the major companies visited were using air for one or more applications.

In many of the designs currently on the drawing board, the air systems need proportional position control of dampers. This will require servo-feed-back loops under operation of the controller.

A summary of the characteristics which are currently believed to be representative of needs are given in the matrix below:

<table>
<thead>
<tr>
<th>INPUTS</th>
<th>#</th>
<th>CHARACTERISTICS</th>
<th>OUTPUTS</th>
<th>#</th>
<th>CHARACTERISTICS</th>
</tr>
</thead>
<tbody>
<tr>
<td>Analog</td>
<td>16</td>
<td>mv D.C.</td>
<td>Servo</td>
<td>4</td>
<td>0 ± 24 VAC</td>
</tr>
<tr>
<td>Switch</td>
<td>12</td>
<td>0-24V AC/DC</td>
<td>Switch Closures</td>
<td>8</td>
<td>0-10 AMPs/110VAC</td>
</tr>
<tr>
<td>Time</td>
<td>1</td>
<td>(Internal)</td>
<td>110VAC</td>
<td>4</td>
<td>0-20 AMPs/24VAC</td>
</tr>
<tr>
<td>Digital</td>
<td></td>
<td>Pulse Trains 4 0-10kc</td>
<td>Time Display</td>
<td>1</td>
<td>Visual (Sonic)</td>
</tr>
<tr>
<td></td>
<td></td>
<td>(Plug-In)</td>
<td></td>
<td></td>
<td>110VAC @ 10 AMPs</td>
</tr>
</tbody>
</table>
Most of the companies interviewed had little or no idea of what the control needs would be for advanced solar systems that incorporated air conditioning (refrigeration) and energy conservation techniques in an integrated package. The characteristics shown on page three must be treated as tentative and will be refined over the course of this work.
January 10, 1977

TO: James D. Hankins
FROM: Joseph L. Imholte
SUBJECT: Monthly Status Report
CONTRACT: NAS8-32256
PERIOD: Month Ending 31 December 1976

TECHNICAL PERFORMANCE

1. During the month ending 31 December 1976, Rho Sigma completed the system specification and the microprocessor study for the Model 600 microprocessor controller. Rho Sigma is presently negotiating with a subcontractor for the hardware necessary for the controller.

The data required for the preliminary design review was delivered along with additional data to better describe Rho Sigma's approach to the contract.

SCHEDULE

II. Enclosure A shows, via a bar chart, the progress Rho Sigma has made toward the completion of the contract. No schedule problems are foreseen at this time.

COST

III. This item has been deleted.
TO: James D. Hankins
FROM: Joseph L. Imholte
SUBJECT: Monthly Status Report
CONTRACT: NAS8-32256
PERIOD: Month Ending 31 January 1977

I. TECHNICAL PERFORMANCE

During the month ending 31 January 1977, Rho Sigma has completed negotiations with a subcontractor to design and build the necessary circuit boards to be used in the Model 600 microprocessor controller.

The preliminary design review was also completed at Rho Sigma's facility in Van Nuys, California.

Fairchild Instrumentation in San Jose, California has been given a purchase order to design the F8 based microprocessor. The contract with Fairchild will enable Rho Sigma to expand an existing system into a solar controller. This approach will allow for the final production units to be produced at a competitive price. A copy of the Fairchild proposal and Rho Sigma's purchase order is enclosed.

The preliminary design review was attended by:

- Robert J. Schlesinger - Rho Sigma
- Ed S. Peltzman - Rho Sigma
- James D. Hankins - NASA
- Ken Rinaldo - Fairchild
- Dominic Norcia - Fairchild

II. SCHEDULE

The enclosed Bar Chart has been modified to encompass Fairchild's delivery date along with Rho Sigma's task.
### Voltmeters

<table>
<thead>
<tr>
<th>Full Scale Range</th>
<th>Resolution</th>
<th>Impedance (Ohms)</th>
<th>Bias Current TYP</th>
<th>Bias Current MAX</th>
<th>Overvoltage Protection VDC VRMS</th>
</tr>
</thead>
<tbody>
<tr>
<td>±1.9999V</td>
<td>±100 V</td>
<td>&gt;1000</td>
<td>2.5µA</td>
<td>7.5µA</td>
<td>200 240</td>
</tr>
<tr>
<td>±19.999V</td>
<td>±2.003V</td>
<td>10</td>
<td>250pA</td>
<td>750pA</td>
<td>500 350</td>
</tr>
<tr>
<td>±199.99V</td>
<td>±21.000V</td>
<td>10</td>
<td>250pA</td>
<td>750pA</td>
<td>500 350</td>
</tr>
</tbody>
</table>

### ANALOG INPUT

- Configuration: True instrumentation type floating differential or single ended input with external reference, or comparison voltage.
- Ratiometric Operation: With external reference, or comparison voltage.
- Common Mode Voltage: 600 V between input and earth ground.
- AC Powered Models: 1 Volt P-P between input and ground.
- DC Powered Models: 1 Volt between input and earth ground.
- Common Mode Rejection: With Line Source Imbalance.
  - AC Powered Models: 50 to 60HZ; 100dB, slight rolloff above 60HZ.
  - DC Powered Models: 50 to 60HZ, f/0Hz, slight rolloff above 60HZ.
- Normal Mode Rejection: 30dB @ 50Hz; 32dB @ 60Hz; -6dB/octave thereafter.
- Overvoltage Protection: Max. input without damage – see table above.

### PERFORMANCE

- Accuracy (120 Days): ±0.02% of reading ±1 digit.
- Linearity: 0.05%
- Max Temperature Coefficient:
  - (0°C to +55°C): 30 ppm/°C.
  - of full scale: 0.0005% F S/°C.
- Setting Time: 900msec to 0.1% error from a (+) or (-) F S step input.

* higher filtering for increased NMR or reduced setting time is available, contact factory.

### DISPLAY

- Type: 7-segment planar solid state (LED): 0.5 in (1.27 cm) high numerals, readable at 30 ft.
- Polarity Indication: Automatic (+) or (-) symbol.
- Overload Indication: "1" digit flashes when count exceeds 19999.
- External Hold: Last conversion is held and displayed when EXT HOLD is maintained at logic "0". Reverts to internal trigger control when EXT HOLD input is open.
- Decimal Point: 3 position selectable (PC card models can be externally programmed).

### ENVIRONMENT/POWER REQUIREMENTS

- Temperature Range:
  - Operating: 0°C to +55°C.
  - Non-operating: -40°C to +85°C.
- Relative Humidity: 0 to 85% (non-condensing).
- Input Voltage: See Table 1-1.
- Power: 3.0w watts (typ).
CONVERSION CHARACTERISTICS

Conversion Time

- Time (Read) Rate

50 msee

Approx. 1/sec (factory set)

DIGITAL OUTPUTS

- NCD parallel (optional)

18 lines, 4 TTL loads (positive true)

- NCD bit serial (standard)

1 count/2.2 sec, count window signal provided, 1 TTL load

- Print

Logic "0", 1 TTL load

- Polarilty

Logic "0" indicates +, 4 TTL loads

- Overage

Logic "1", 4 TTL loads

- NCD Rating Connector

P/NFS 02902018, TRW/Cinch® 251-22-30-410 or equiv.

PHYSICAL CHARACTERISTICS

- Termination

15-pin dual row connector or 8-pin terminal strip

- Input Rating Connector

P/N FS 95400-1-3, Amphenol® 225-21521-101-117 or equiv.

- DIM Standard

- Slot (panel cutout)

1.772 in. (45mm) x 3.622 in. (92mm)

- Max. depth behind bezel

3.8" (96.5mm)

- Terminal block

3.7" (91mm)

- Weight

14.5 oz. (411 grams) max

- Case Construction

Molded A/E/S meets UL 94-V-0

- Calibration

Front access.

RELIABILITY PROTECTION

- Warranty

18 months

- Burn-in/Temp. Cycling

100 hrs. at 55° C w/pwr. and temp. on/off cycles.

MODEL NUMBERING

- Range

Input Voltage

Options

2=100/115Vac

2=10% 0=PC card

Terminal

1=BCD

2=BCD

(600V optoisolated)

5=2V/mA

47-63Hz

7=20V/mA

+10%

4/220/240Vac

7=20V/mA

47-63Hz

9=200V/ma

5=SVDC

2.25V

9=Terminal

Strip

* Poor Quality

[Signature]

1.2.77
March 4, 1977

TO: James D. Hankins
FROM: J.L. Imholte
SUBJECT: Monthly Status Report
CONTRACT: NAS8-32256
PERIOD: Month Ending 28 February 1977

I. TECHNICAL PERFORMANCE

During the month ending 28 February, 1977, Rho Sigma conducted the design review with its sub-contractor, Fairchild Instrumentation. Rho Sigma also completed the preliminary software package and began the flow charts necessary for programming the F-8 microprocessor.

The design review was held with Fairchild Instrumentation at their Chatsworth, CA plant. Fairchild committed themselves to the specifications of the preliminary review with no exceptions. Fairchild has started charging hours to the packaging of the card cage and individual printed circuit board.

The design review with Fairchild was attended by:
  Robert J Schlesinger - Rho Sigma, Inc.
  Joseph L Imholte  - Rho Sigma, Inc.
  Ken Rinaldo       - Fairchild Instrumentation

Rho Sigma, Inc. has written a preliminary program specification which was reviewed with Fairchild Instrumentation at the above meeting. The necessary program block diagram and a total program flow diagram are being worked on by Rho Sigma at this time.

II. SCHEDULE

The enclosed bar chart shows the status of key events. As of this time the actual progress to making the predicted. The prototype design date was rescheduled from the middle of May to the middle of March.

III. COST

This section has been deleted.
II SCHEDULE

The enclosed bar chart shows the status of key events. The prototype design date is now scheduled for 28 April, 1977.

III COST

This item has been deleted.
April 7, 1977

TO: James D. Hankins
FROM: J. L. Imholte
SUBJECT: Monthly Status Report
CONTRACT: NAS8-32256
PERIOD: Month Ending 31 March, 1977

I. TECHNICAL PERFORMANCE

During the month of March, 1977, Rho Sigma met with NASA and IBM and conducted a "preliminary" prototype design review. Rho Sigma's subcontractor, Fairchild, has built the breadboard and begun the P.C. board card design. Rho Sigma has completed the preliminary programming flow charts.

The review held in Huntsville, Alabama was attended by:

James D. Hankins MSFC
James R. Currie MSFC
Paul Hamby MSFC
T.N. Vann MSFC
Thevis Barton IBM
W.W. Scott IBM
B.J. Doran MSFC
R.J. Lewedag MSFC
J.L. Imholte Rho Sigma
R.J. Schlesinger Rho Sigma

Although the necessary data was not available for the prototype design review; the system was reviewed and questions answered. The official prototype review will be held at Rho Sigma's plant in Van Nuys, California on 28 April 1977.

Fairchild Systems has been contacted and they are on schedule and will provide the necessary documentation for the prototype review.

The preliminary flow charts for the RS 600 program are finished and will be reviewed with Fairchild.
RHO SIGMA, INC.

Contract NAS8-32256

Third Quarterly Report (DRL-10)

August 1, 1977
May 9, 1977

TO: James D. Hankins
FROM: J.L. Imholte
SUBJECT: Monthly Status Report
CONTRACT: NAS8-32256
PERIOD: Month Ending 30 April, 1977

I. TECHNICAL PERFORMANCE

During the month of April, 1977, Rho Sigma held the Prototype Design Review at its Van Nuys Plant, delivered the finalized programming specifications and flow charts and reviewed the system with Fairchild.

The Prototype Design Review was conducted at Rho Sigma's Van Nuys, CA. plant. The review was attended by:

<table>
<thead>
<tr>
<th>Name</th>
<th>Organization</th>
</tr>
</thead>
<tbody>
<tr>
<td>James D. Hankins</td>
<td>MSFC</td>
</tr>
<tr>
<td>E. S. Pletzman</td>
<td>Rho Sigma</td>
</tr>
<tr>
<td>Joseph L Imholte</td>
<td>Rho Sigma</td>
</tr>
<tr>
<td>David W. Corbin</td>
<td>Fairchild</td>
</tr>
</tbody>
</table>

The review covered the data package sent to NASA and all of the RID's resulting. Both Fairchild and Rho Sigma were aware of the RID's and appropriate action is being taken.

A new date was set for the first article review. It will now be held at Rho Sigma on 13 July, 1977.

Rho Sigma held a system design review with its subcontractor, Fairchild. The finalized program specifications and flow charts, minus the exact system equations were accepted by Fairchild. Details on the display and packaging were also discussed with the design to be fixed at a later date.
2. **SCHEDULE**

The enclosed Bar Chart shows the status of key events. The First Article Review is now rescheduled for 13 July, 1977.

3. **COST**

This item has been deleted.
<table>
<thead>
<tr>
<th>SUPPORT TASK</th>
<th>OCT.</th>
<th>NOV.</th>
<th>DEC.</th>
<th>JAN</th>
<th>FEB</th>
<th>MAR</th>
<th>APR.</th>
</tr>
</thead>
<tbody>
<tr>
<td>CONTROL REG. STUDY</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>N PROCESSOR STUDY</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>SYSTEM SPEC.</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CONTROLLER &amp; BREADBOARD</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>PRINTED CIRCUIT C</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CABLE</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>KEYBOARD DESIGN AND TEST</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>SOFTWARE DEVELOP</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>RMS SIGMA'S ACCEPT</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>FINAL PACKAGE</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>ACCEPTANCE TEST</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>MONTHLY REPORTS</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>QUARTLY REVIEW</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>PRELIM. DESIGN REVIEW</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>PROTOTYPE DESIGN REVIEW</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>FIRST ARTICLE REVIEW</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**Progress Schedule**

**MODEL: 600 Mk.**
TO:      James D. Hankins
FROM:    J.L. Imholte
SUBJECT: Monthly Status Report
CONTRACT: NAS8-32256
PERIOD:  Month Ending 31 May 1977

I.  TECHNICAL PERFORMANCE

During the month of May, 1977, Rho Sigma received the RID's from
the prototype design review and completed the necessary documents
to satisfy the RID's

Fairchild, the subcontractor has completed 80% of the hardware and
80% of the program for the Microprocessor necessary to operate the
RS 600.

II. SCHEDULE

The enclosed bar chart shows the status of key events.

II. CGR

This item has been deleted.
I. TECHNICAL PERFORMANCE

Rho Sigma reviewed the RS 600 with Fairchild during this reporting period. The first unit is undergoing program check out. The hardware is operational and the software is approximately 90% operational. Rho Sigma is scheduled to take acceptance of the first unit on 18 July, 1977 and should be ready for the first article review on 4 August, 1977.

No major problems were encountered during the review and the system operations via the keyboard was exercised and noted so that work can continue on the operations manual.

II. SCHEDULE

The enclosed bar chart shows the schedule and status of key events.

III. COST

This item has been deleted.
<table>
<thead>
<tr>
<th>SUPPORT TASK</th>
<th>OCT</th>
<th>NOV</th>
<th>DEC</th>
<th>JAN</th>
<th>FEB</th>
<th>MAR</th>
<th>APR</th>
<th>MAY</th>
<th>JUN</th>
</tr>
</thead>
<tbody>
<tr>
<td>CONTROL Req./STUDY</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>M PROCESSOR STUDY</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>SYSTEM SPEC.</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CONTROLLER A BREADBOARD</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>PRINTED CIRCUIT A CARD DESIGN</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>KEYBOARD DESIGN AND TEST</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>RAD SIGNS ACCEPT/TEST</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>FINAL PACKAGE</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>ACCEPTANCE TEST</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>MONTHLY REPORTS</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>QUARTERLY REVIEW</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>PRELIM. DESIGN REVIEW</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>PROTOTYPE DESIGN REVIEW</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>FIRST ARTICLE REVIEW</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**PROGRESS SCHEDULE**