Aging Behavior of Au-Based Ohmic Contacts to GaAs

Navid S. Fatemi

Sverdrup Technology, Inc.
(Lewis Research Center Group)
NASA Lewis Research Center
Cleveland, Ohio

July 1988

Prepared for
Lewis Research Center
Under Contract NAS3-24105
AGING BEHAVIOR OF Au-BASED OHMIC CONTACTS TO GaAs

Navid S. Fatemi
Sverdrup Technology, Inc.
(Lewis Research Center Group)
NASA Lewis Research Center
Cleveland, Ohio 44135

SUMMARY

Gold based alloys, commonly used as ohmic contacts for solar cells, are known to react readily with GaAs. It is shown that the contact interaction with the underlying GaAs can continue even at room temperature upon aging, altering both the electrical characteristics of the contacts and the nearby pn junction. Au-Ge-Ni as-deposited (no heat-treatment) contacts made to thin emitter (0.15 μm) GaAs diodes have shown severe shunting of the pn junction upon aging for several months at room temperature. The heat-treated contacts, despite showing degradation in contact resistance did not affect the underlying pn junction. Au-Zn-Au contacts to p-GaAs emitter (0.2 μm) diodes, however showed slight improvement in contact resistance upon 200 °C isothermal annealing for several months, without degrading the pn junction. The effect of aging on electrical characteristics of the as-deposited and heat-treated contacts and the nearby pn junction, as well as on the surface morphology of the contacts are presented.

INTRODUCTION

Gold-based alloys are the most commonly used metallization materials for both the front grid and the back ohmic contacts of GaAs solar cells. Au-Zn and Au-Ge-Ni have been the most popular systems for making ohmic contacts to p- and n-GaAs respectively, mainly due to the very low values of specific contact resistivity ($\rho_c$) achievable with these systems upon a post deposition heat-treatment. $\rho_c$ values in the low 10-6 Ω-cm² range have been reported by many workers for these contact systems to variously doped GaAs substrates (refs. 1 to 3). These low $\rho_c$ values are necessary to keep the contact resistance contribution to the series resistance of a solar cell negligible if these cells are to be operated under >100X sunlight concentrations (ref. 4).

It is highly desirable that the ohmic contacts remain stable during the life of the solar cell regardless of its operating temperature. Space concentrator solar cells e.g., are expected to operate in the 80 to 100 °C range. They may also be annealed periodically at 200 to 400 °C for a few hours at a time in order to reverse the radiation damage effects caused by electron and proton bombardment in the space environment (refs. 5 and 6). Several months of cumulative periodic high-temperature annealing may therefore be necessary during the life of a solar cell. High-temperature aging studies of Au-Ge-Ni contacts at 330 to 390 °C for several days, for example have revealed increased interactions between GaAs and the contacts and also a general increase in contact resistance (refs. 7 and 8), pointing to possible instability of these contacts with high-temperature aging.
In this work, the high-temperature (200 and 400 °C) aging stability of Au-Zn contacts to p-GaAs, as well as the room-temperature aging stability of Au-Ge-N1 contacts to n-GaAs for several months were investigated. The effects of aging on specific contact resistivity, metal-GaAs interaction, and surface morphology of the contacts are presented.

Au-Zn CONTACTS

Au-Zn contacts were made to highly doped (2x10^{18} \text{ cm}^{-3}) thin emitter (0.2 \mu \text{m quasi-neutral region thickness}) p-type epi-layer GaAs (obtained from Spire), whose structure is shown in figure 1(b). The 200 to 300 A thin Au layer interposed between GaAs and Zn helps the uniformity and adhesion of the contact at the interface. Six samples were heat-treated immediately after E-Beam deposition of the contacts to a maximum temperature of 434 °C for 90 sgc. The $\rho_c$ values for these contacts varied from 4.7x10^{-6} to 3.4x10^{-5} $\Omega\cdot\text{cm}$. The samples were then annealed at 200 °C in flowing N2 for a period of slightly more than 3 months. Subsequently, they were also subjected to a 400 °C anneal for a period of 64 hr in air. Contact resistance values, measured via the Transmission Line Method (ref. 9), and current-voltage characteristics of the p/n diodes underneath the contacts were monitored periodically for all samples. The effect of 200 and 400 °C isothermal annealing of these contacts, as well as 13 months of subsequent aging of the contacts at room temperature, on $\rho_c$ is given in table 1. Figure 2 compares a typical p/n diode I-V curve with a Au-Zn emitter contact at various stages of aging.

As shown in table 1, the 200 °C isothermal annealing actually slightly improved $\rho_c$ for all samples, indicating the stability of these contacts with high-temperature aging, although the Au-Zn/GaAs interaction must have continued to some degree as to bring about the change in $\rho_c$. However, this continued interaction between the contact and GaAs does not appear to have a significant effect on the nearby p/n junction as evident from figures 2(a) and 2(b). At 400 °C however Au-Zn/GaAs interactions were more severe, as shown in figure 2(c), resulting in much degradation in the I-V characteristics of the underlying junction. The contact resistance, on the other hand, showed little degradation for most samples and a slight improvement for one sample. Also, the subsequent room-temperature aging of the contacts for 13 months did not appear to affect $\rho_c$ or p/n diode I-V characteristics of these samples significantly (fig. 2(d)).

Despite the minimal changes occurring in $\rho_c$ for Au-Zn contacts after 13 months, the interaction in the Au-Zn/GaAs system continues at room temperature. This room-temperature aging is portrayed in figure 3, where the surface morphologies of the contacts for both as-deposited and heat-treated contacts are compared for new and aged contacts. As shown in figures 3(a) and 3(c), the 450 °C heat-treatment for 1 min did not appear to change the surface morphology of the contact, whereas the 13 months room-temperature aging of both as-deposited (fig. 3(b)) and heat-treated (fig. 3(d)) contacts altered the surface morphology dramatically.
Au-Ge-N1 CONTACTS

Au-Ge-N1 contacts were made to variously doped n-GaAs epilayers (MOCVD grown, in house). The n/p current-voltage characteristic measurements, however, were done on moderately doped \((6 \times 10^{17} \text{ cm}^{-3})\) thin emitter \((0.15 \mu \text{m}, \text{quasi-neutral region thickness})\) n-GaAs samples (obtained from Spire Co.), as shown in figure 1(a). For these samples, the as-deposited contacts showed rectifying characteristics. Upon heat-treatment in the 353 to 490 °C temperature range for short periods (15 to 60 sec), however most samples exhibited \(10^{-6} \Omega\text{-cm}^2\) \(\rho_c\) values. In table 2 the \(\rho_c\) values for many samples heat-treated at different temperatures and aged at room temperature for 9 to 31 months are given. As shown, the change in \(\rho_c\) upon aging for most samples was slight regardless of the heat-treatment temperature or the period of aging. In some cases, \(\rho_c\) values for identically treated samples took different directions upon aging for the same time period. This points to the complex mechanism involved in the low resistance contact formation at the metal-GaAs interface. The relative stability of the heat-treated contacts, however, is attributed to the formation of the low resistance ternary N-GaAs phase at the interface (ref. 10). It is apparent, therefore that the room-temperature aging of these contacts should not cause any dramatic increases in the contact resistance in solar cells.

The main concern for using Au-Ge-N1 as the front grid metallization in shallown junction solar cells is that penetration of the contact species into GaAs can damage the nearby n/p junction. As shown in figures 5(a) and 5(b), heat-treating the contacts at 360 and 395 °C for 20 sec can severely short out the junction. One solution to this problem is not to heat-treat the contacts. In case of low to moderately doped emitters, the contacts will be rectifying, but in case of highly doped \((>1 \times 10^{18} \text{ cm}^{-3})\) emitters, \(\rho_c\) will be in the high \(10^{-4} \Omega\text{-cm}^2\) range which is acceptable for one sun operation of solar cells. However, if the contacts are not heat-treated, the Au-Ge-N1/GaAs interactions can continue at room temperature to a greater degree than for the heat-treated contacts. This can be due to the absence of stable binary and ternary phases which are created at the metal-semiconductor interface upon heat-treatment. Figure 4 shows typical n/p diode I-V characteristics for several diodes with as-deposited Au-Ge-N1 contacts aged at room temperature for 14 months. As shown in figure 4(a), the newly deposited contact to \(6 \times 10^{17} \text{ cm}^{-3}\) doped emitter is rectifying. Upon aging, most contacts became nonrectifying (i.e., showing linear metal-semiconductor I-V behavior over a given current range), but most of them also severely degrade the nearby \((0.15 \mu \text{m})\) n/p junction (figs. 4(c) and 4(d)). In rare cases, the contacts can become nonrectifying and at the same time do not shunt the junction (fig. 4(b)).

Consequently, contacts must be heat-treated to remain stable with room-temperature aging. But as mentioned earlier, heat-treatment of the contacts can severely shunt the n/p junction under a thin emitter. One method to circumvent this problem is by encapsulating the contacts with SiO2 or Ta2O5, and/or by the use of a diffusion barrier such as TiN incorporated into the contact system prior to heat-treatment (ref. 12). The diffusion barrier in the case of Au-Ge-N1 contacts can be interposed between the top Au \((1550 \text{ A})\) layer and the underlying Ni-Ge-Au thin active layer, replacing the 100 A Ni layer. The \(\rho_c\) values for the contacts made with and without TiN barriers were measured to be comparable.
Figures 5(c) and 5(d) show the I-V characteristics of two n/p diodes with Au-Ge-Ni contacts heat-treated to 396 and 400 °C for 20 sec, respectively. The contacts of the diodes in figure 5(c) contained TiN (600 A) layers and those in figure 5(d) were encapsulated with Ta2O5 (600 A), and they were aged for 8 and 21 months at room temperature, respectively. As shown, no sign of shunting can be detected in these I-V curves even after long periods of room-temperature aging. Therefore, the use of diffusion barriers and/or dielectric encapsulants with Au-Ge-Ni contacts made to thin emitter diodes seems to be a necessity.

Additional evidence for the continued interactions between Au-Ge-Ni and GaAs is the change that occurs in the surface morphology of the contacts upon room-temperature aging. Figure 6 compares the surface morphology of the as-deposited new (fig. 6(a)), as-deposited and aged for 11 months (fig. 6(b)), heat-treated new (400 °C) (fig. 6(c)), and heat-treated and aged for 9 months (fig. 6(d)) contacts. Upon aging, both the as-deposited and heat-treated contacts seem to approach an end form with a more definite grain structure not evident prior to the aging process. High-temperature aging of these contacts indicates the continued out diffusion of Ga through the metallization and Ga2O3 oxide formation at the surface of the contact (ref. 7). Again, an effective diffusion barrier incorporated into the contact system should limit the dissolution of Ga into the metallization system greatly.

It is known that GaAs reacts readily with pure Au at room temperature (ref. 11). The effect of this room temperature interaction of Au with GaAs upon aging on $\rho_c$ was also studied. Au contacts were made to highly doped ($5 \times 10^{18}$ cm$^{-3}$) thick (1 to 1.2 μm) n-type GaAs epilayers. As shown in table 3, hardly any degradation in $\rho_c$ is observed for any of the contacts after room temperature aging of the contacts for 32 months. This indicates that the Au-GaAs interaction does not appear to affect the resistance at the metal-semiconductor interface.

**CONCLUSIONS**

Isothermal annealing of Au-Zn contacts on p-GaAs at 200 and 400 °C for 3 months and 64 hr, respectively, have shown that the specific contact resistivity of these contacts are relatively stable with high-temperature aging. No p/n junction degradation was observed in the 200 °C aging study, but for the case of annealing at 400 °C, the p/n diodes underneath the contacts degraded severely. The emitter thickness in both cases was 0.2 μm.

Room-temperature aging of Au-Ge-Ni contacts on n-GaAs for several months indicates that $\rho_c$ values for these contacts do not increase significantly compared to their as-fabricated values, and for many cases they remain very stable. It was also shown that the as-deposited contacts continue to interact with the underlying GaAs at room temperature, usually resulting in n/p junction degradation underneath the 0.15 μm emitter. The heat-treated contacts on the other hand, can severely short out the n/p junction underneath after >360 °C heat-treatment for a few seconds. The use of a TiN diffusion barrier and a SiO2 or Ta2O5 dielectric encapsulant can prevent degradation, even with these shallow emitters (0.15 μm), even after heat-treating the contacts to 400 °C. Their use will also inhibit the metal-GaAs interactions that would otherwise occur during room-temperature aging.
In addition, the contact resistances for pure Au contacts were found to be very stable after 32 months of room-temperature aging. It was also shown that the surface morphology of Au-Zn and Au-Ge-Ni contacts alter after several months of room-temperature aging for both as-deposited and heat-treated contacts, which is another indication that the interaction between GaAs and its metallization continues at room temperature, whether or not the contact is heat-treated. The I-V characteristics of the junction underneath the contacts, however indicate that this interaction is negligible for the heat-treated contacts compared to the as-deposited contacts.

ACKNOWLEDGMENTS

I would like to thank the following people for their contributions to this work: Manju Ghalla-Goradla and Victor Weizer for their help in formulating some of the concepts in this work, George Mazaris and David Wilt for the MOCVD growth of some of the epi-layers, Ralph Thomas for providing the Polaron measurements of most of the GaAs samples, and David Brinker for his help in initiating this effort.

REFERENCES


<table>
<thead>
<tr>
<th>Sample</th>
<th>( \rho_C (\Omega \text{-cm}^2) )</th>
<th>( \rho_C, 200 , ^\circ \text{C} )</th>
<th>( \rho_C, 400 , ^\circ \text{C} )</th>
<th>( \rho_C, \text{Room temperature} )</th>
</tr>
</thead>
<tbody>
<tr>
<td>SR004-1</td>
<td>4.7x10^-6</td>
<td>3.8x10^-6</td>
<td>5.3x10^-6</td>
<td>5.1x10^-5</td>
</tr>
<tr>
<td>SR005-2</td>
<td>3.4x10^-5</td>
<td>2.5x10^-5</td>
<td>1.7x10^-5</td>
<td>1.6x10^-5</td>
</tr>
<tr>
<td>SR008-1</td>
<td>9.6x10^-6</td>
<td>8.8x10^-6</td>
<td>2.9x10^-5</td>
<td>2.8x10^-5</td>
</tr>
<tr>
<td>SR008-2</td>
<td>5.9x10^-6</td>
<td>5.3x10^-6</td>
<td>9.0x10^-6</td>
<td>7.7x10^-6</td>
</tr>
<tr>
<td>SR010-1</td>
<td>8.8x10^-6</td>
<td>7.7x10^-6</td>
<td>(a)</td>
<td>(a)</td>
</tr>
<tr>
<td>SR010-2</td>
<td>1.3x10^-5</td>
<td>1.1x10^-5</td>
<td>3.6x10^-5</td>
<td>3.2x10^-5</td>
</tr>
</tbody>
</table>

(a) TLM data line fit not good enough for meaningful extraction of \( \rho_C \) due to unequal contact resistance of TLM contact electrodes.

<table>
<thead>
<tr>
<th>Sample</th>
<th>( \rho_C (\Omega \text{-cm}^2) )</th>
<th>( \rho_C, 200 , ^\circ \text{C} )</th>
<th>( \rho_C, 400 , ^\circ \text{C} )</th>
<th>( \rho_C, \text{Room temperature} )</th>
</tr>
</thead>
<tbody>
<tr>
<td>050-2A</td>
<td>7.8x10^-6</td>
<td>6.8x10^-6</td>
<td>31</td>
<td>30</td>
</tr>
<tr>
<td>050-2B</td>
<td>7.2x10^-6</td>
<td>1.6x10^-5</td>
<td>31</td>
<td>30</td>
</tr>
<tr>
<td>053-2</td>
<td>1.1x10^-4</td>
<td>1.9x10^-4</td>
<td>31</td>
<td>240</td>
</tr>
<tr>
<td>058-4</td>
<td>1.8x10^-6</td>
<td>8.5x10^-7</td>
<td>30</td>
<td>60</td>
</tr>
<tr>
<td>057-1</td>
<td>3.1x10^-5</td>
<td>4.7x10^-5</td>
<td>26</td>
<td>60</td>
</tr>
<tr>
<td>057-2</td>
<td>1.1x10^-5</td>
<td>1.1x10^-5</td>
<td>26</td>
<td>60</td>
</tr>
<tr>
<td>058-5</td>
<td>9.2x10^-6</td>
<td>6.2x10^-6</td>
<td>23</td>
<td>600</td>
</tr>
<tr>
<td>128-4A</td>
<td>4.0x10^-6</td>
<td>3.9x10^-5</td>
<td>12</td>
<td>20</td>
</tr>
<tr>
<td>SP014-F</td>
<td>1.5x10^-6</td>
<td>9.7x10^-6</td>
<td>12</td>
<td>35</td>
</tr>
<tr>
<td>25P025-1</td>
<td>7.2x10^-6</td>
<td>8.5x10^-6</td>
<td>10</td>
<td>20</td>
</tr>
<tr>
<td>25P026-1</td>
<td>1.0x10^-5</td>
<td>(a)</td>
<td>10</td>
<td>20</td>
</tr>
<tr>
<td>25P027-1</td>
<td>3.4x10^-6</td>
<td>(a)</td>
<td>9</td>
<td>15</td>
</tr>
<tr>
<td>25P031-A</td>
<td>1.0x10^-5</td>
<td>1.0x10^-5</td>
<td>9</td>
<td>15</td>
</tr>
<tr>
<td>25P033-A</td>
<td>5.0x10^-6</td>
<td>3.4x10^-5</td>
<td>9</td>
<td>15</td>
</tr>
</tbody>
</table>

(a) TLM data line fit not good enough for meaningful extraction of \( \rho_C \) due to unequal contact resistance of TLM contact electrodes.
TABLE 3. - EFFECT OF 32 MONTHS OF AGING ON SPECIFIC CONTACT RESISTIVITY OF Au OHMIC CONTACTS TO n-GaAs

<table>
<thead>
<tr>
<th>Sample</th>
<th>$\rho_C (\Omega \cdot \text{cm}^2)$</th>
<th>$\rho'_C (\Omega \cdot \text{cm}^2)$</th>
<th>Heat-treatment, min °C</th>
<th>Doping density, cm$^{-3}$</th>
</tr>
</thead>
<tbody>
<tr>
<td>058-1</td>
<td>1.6 x 10^{-5}</td>
<td>1.5 x 10^{-5}</td>
<td>2.0, 371</td>
<td>5 x 10^{18}</td>
</tr>
<tr>
<td>058-2</td>
<td>1.9 x 10^{-5}</td>
<td>2.1 x 10^{-5}</td>
<td>4.0, 409</td>
<td>5 x 10^{18}</td>
</tr>
<tr>
<td>059-1</td>
<td>7.7 x 10^{-5}</td>
<td>8.6 x 10^{-5}</td>
<td>1.0, 350</td>
<td>5 x 10^{18}</td>
</tr>
<tr>
<td>059-2L</td>
<td>7.9 x 10^{-5}</td>
<td>7.7 x 10^{-5}</td>
<td>1.0, 350</td>
<td>5 x 10^{18}</td>
</tr>
<tr>
<td>059-2R</td>
<td>5.3 x 10^{-5}</td>
<td>5.3 x 10^{-5}</td>
<td>1.0, 350</td>
<td>5 x 10^{18}</td>
</tr>
<tr>
<td>059-3L</td>
<td>6.0 x 10^{-5}</td>
<td>6.2 x 10^{-5}</td>
<td>1.0, 350</td>
<td>5 x 10^{18}</td>
</tr>
<tr>
<td>059-3R</td>
<td>7.1 x 10^{-5}</td>
<td>7.4 x 10^{-5}</td>
<td>1.0, 350</td>
<td>5 x 10^{18}</td>
</tr>
</tbody>
</table>

FIGURE 1. - CONTACT STRUCTURE.

(A) Au-Ge-Ni,
(B) Au-Zn
(A) 432 hr at 200 °C.

(B) 3 months at 200 °C.

(C) 64 hr at 400 °C.

(D) 13 months at room temperature.

FIGURE 2. - P/N DIODE I-V CURVES (0.2V, 1mA/div.) WITH Au-Zn CONTACTS AGED.
(A) AS-DEPOSITED NEW.

(B) AS-DEPOSITED AGED FOR 13 MONTHS.

(C) HEAT-TREATED AT 450 °C NEW.

(D) HEAT-TREATED AT 450 °C AGED FOR 13 MONTHS.

FIGURE 3. - SURFACE MORPHOLOGY OF Au-Zn CONTACTS.
FIGURE 4. - N/P DIODE I-V CURVES WITH AS-DEPOSITED Au-Ge-Ni CONTACTS AGED FOR 14 MONTHS.
(A) 360 °C FOR 20 SEC NEW.

(B) 395 °C FOR 20 SEC NEW.

(C) 396 °C FOR 20 SEC WITH TiN AGED FOR 8 MONTHS.

(D) 400 °C FOR 20 SEC WITH Ta₂O₅ ENCAPSULATION AGED FOR 21 MONTHS.

FIGURE 5. N/P DIODE I-V CURVES WITH HEAT-TREATED Au-Ge-Ni CONTACTS (0.5V, 1mA/DIV.).
(A) AS-DEPOSITED NEW.

(B) AS-DEPOSITED AGED FOR 11 MONTHS.

(C) HEAT-TREATED AT 400 °C NEW.

(D) HEAT-TREATED AT 400 °C AGED FOR 9 MONTHS.

FIGURE 6. - SURFACE MORPHOLOGY OF Au-Ge-Ni CONTACTS.
Gold based alloys, commonly used as ohmic contacts for solar cells, are known to react readily with GaAs. It is shown that the contact interaction with the underlying GaAs can continue even at room temperature upon aging, altering both the electrical characteristics of the contacts and the nearby pn junction. Au-Ge-Ni as-deposited (no heat-treatment) contacts made to thin emitter (0.15 μm) GaAs diodes have shown severe shunting of the pn junction upon aging for several months at room temperature. The heat-treated contacts, despite showing degradation in contact resistance did not affect the underlying pn junction. Au-Zn-Au contacts to p-GaAs emitter (0.2 μm) diodes, however showed slight improvement in contact resistance upon 200 °C isothermal annealing for several months, without degrading the pn junction. The effect of aging on electrical characteristics of the as-deposited and heat-treated contacts and the nearby pn junction, as well as on the surface morphology of the contacts are presented.