Storage and Computationally Efficient Permutations of Factorized Covariance and Square-Root Information Arrays

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A unified method to permute vector-stored Upper-triangular-Diagonal factorized covariance (UD) and vector-stored upper-triangular Square-Root Information (SRI) arrays is presented. The method involves cyclic permutation of the rows and columns of the arrays and retriangularization with fast (slow) Givens rotations (reflections). Minimal computation is performed, and a one-dimensional scratch array is required. To make the method efficient for large arrays on a virtual memory machine, computations are arranged so as to avoid expensive paging faults. This method is potentially important for processing large volumes of radio metric data in the DSN.

I. Introduction

In the reduction of observational data involving simultaneous least-squares estimation of many parameters, it is desirable to examine different modeling scenarios without reprocessing the data through the filter. Often the analyst will estimate a full contingent of system parameters in the initial filter pass. A reduced state estimate, reflecting a different modeling scenario, can then be obtained from this initial pass. Furthermore, the sensitivity of the reduced state to the excluded parameters is readily obtainable. Application of a priori information for the excluded parameters to the sensitivity and subsequent augmentation of the reduced state by these perturbations then yields a more conservative assessment of the reduced state errors. This type of analysis is particularly useful when the reduced state is sensitive to the excluded parameters that are insensitive to data [1].

Upper-triangular data structures, such as those encountered in an Upper-triangular-Diagonal factorized covariance filter (UD filter) or in an upper-triangular Square-Root Information filter (SRI filter), are conducive to computing these reduced state estimates. In a UD filter, it is necessary to permute the rows of the UD array in order to compute the reduced estimate. The rows corresponding to the parameters that are to be excluded must be moved below those corresponding to the parameters that are to be included in the reduced estimate. In an SRI filter, it is necessary to permute the columns of the SRI array in order to compute the reduced estimate. The
columns corresponding to the parameters that are to be excluded must be moved to the right of the columns corresponding to those that are to be included in the reduced estimate.\(^1\)

A brute force method to accomplish these permutations reorders the rows (columns) of the UD (SRI) array into a two-dimensional work array. To retriangularize this two-dimensional array, one can then post(pre)-multiply it by an implicitly defined orthogonal transformation composed of Householder reflections.\(^2\) This method is sound and numerically stable due to the orthogonality of the transformation. Furthermore, the commercially available Estimation Subroutine Library (ESL) has standard routines to conveniently perform these permutations and subsequent retriangularizations [2]. However, this method requires a two-dimensional work array to store the permuted UD (SRI) array. For large-scale systems, such as those encountered for Global Positioning System (GPS) studies, it is not at all uncommon to estimate on the order of 1000 parameters. This translates into four million extra bytes of storage. Additionally, over 3000 parameters are routinely solved when DSN VLBI data are processed with the order of \(10^6\) slow Givens reflection. Subsequently, this method required on the order of \(n^2\) slow Givens reflections, where \(n\) is the column distance between the two parameters of the exchange. Furthermore, this method required two explicit scratch arrays. One of the scratch arrays stored the right column of the exchange while the other was used to store elements that were created in the retriangularization process.\(^3\)

In 1987, Wolff (personal communication) suggested that the same pairwise permutations could be performed on an SRI array with retriangularization accomplished after each column exchange with slow Givens reflections.\(^4\) This method also eliminated the need for a two-dimensional work array. However, in the retriangularization process, elements not consistent with the upper-triangular data structure of the SRI array were created. Each inconsistent element required another slow Givens reflection. Subsequently, this method required on the order of \(n^2\) slow Givens reflections, where \(n\) is the column distance between the two parameters of the exchange. Furthermore, this method required two explicit scratch arrays. One of the scratch arrays stored the right column of the exchange while the other was used to store elements that were created in the retriangularization process.\(^5\)

Also in 1987, Pombra suggested a technique that eliminated the need to perform a multitude of these pairwise permutations in reordering a UD array.\(^6\) His technique was to insert into the covariance an artificial parameter with zero variance and zero correlation with the other parameters. This is easily accomplished by inserting into the UD array a row and column of zeros. A parameter could then be moved directly into its proper place by performing a pairwise permutation of this artificial parameter and the parameter that is desired to reside in its place. After the permutation is performed, this artificial parameter can then be deleted from the UD array. This is also easily accomplished by removing from the UD array the appropriate row and column of zeros. This technique drastically

\[ P_{\text{update}} = (I - GH)P(I - GH)^t + RGG^t \]

where \(P_{\text{update}}\) is then the permuted covariance of the covariance \(P\). The noise \(R\) is set equal to zero, and the gain \(G\) and design \(H\) are chosen so that \((I - GH)\) is a permutation operator. Such a permutation operator that permutes parameters \(i\) and \(j\) can be expressed as

\[ I - GH = I - (e_i - e_j)(e_i - e_j)^t \]

where \(e_i\) is a column vector whose elements are zero except element \(i\), which is 1. Trivially \(H = (e_i - e_j)^t\) and \(G = H^t\). In practice this computation is implemented with an optimal measurement update using the Bierman UD measurement update algorithm and a rank-1 update to include the effect of the suboptimality of the gain. This method eliminated the need for a two-dimensional scratch array. Software was eventually written that exploited the structure of the suboptimal gain \(G\) and design \(H\). This method still required several scratch arrays and a multitude of pairwise permutations.\(^3\)

In 1986, Bierman (personal communication) suggested that the necessary reordering of UD arrays be performed as a series of pairwise permutations in such a way that the upper-triangular structure of the UD array is always maintained. Each pairwise permutation could be performed as a suboptimal noise-free measurement update:

\[ P_{\text{update}} = (I - GH)P(I - GH)^t + RGG^t \]

where \(P_{\text{update}}\) is then the permuted covariance of the covariance \(P\). The noise \(R\) is set equal to zero, and the gain \(G\) and design \(H\) are chosen so that \((I - GH)\) is a permutation operator. Such a permutation operator that permutes parameters \(i\) and \(j\) can be expressed as

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1. S. C. Wu et al., *Oasis Mathematical Description*, JPL Publication D-3139 (internal document), Jet Propulsion Laboratory, Pasadena, California, April 1, 1986.
2. Before this retriangularization can be performed on the UD array, it is of course necessary to scale the columns by the square root of the corresponding diagonal. These square roots can be avoided by performing a modified weighted Gram-Schmidt UD triangularization algorithm.
4. Since the SRI array is not unique, either Givens reflections or rotations can be used.
reduced the number of permutations necessary to reorder the array. However, suboptimal noise-free measurement updates were still used to perform the pairwise permutations.

It was recognized that the insertion of a row and column of zeros into the UD array, together with the subsequent row exchange between the artificial parameter and the parameter of interest, created an array similar to a Morf–Kailath prearray with zero noise variance [4]. Retriangularization of this array into an upper-triangular Morf–Kailath postarray is easily accomplished with fast Givens rotations. This method requires a one-dimensional scratch vector which manifests itself as an augmentation of the UD array. An extra column of the UD array permits the insertion of a row and column of zeros. Furthermore, the method requires on the order of $\mathcal{N}_{\text{row}}$ fast Givens rotations, where $\mathcal{N}_{\text{row}}$ is the row distance between the parameter and the desired location of the parameter. Due to the nature of the fast Givens, square root computations are never required.

This technique can also be adopted to reordering SRI arrays. In a manner similar to the Wolff method, retriangularization is accomplished with slow Givens reflections. Unlike the Wolff method, however, no new elements are created. Hence, this method requires on the order of $\mathcal{N}_{\text{column}}$ slow Givens reflections, where $\mathcal{N}_{\text{column}}$ is the column distance between the parameter and the desired location of the parameter.

Since the artificial parameter is only a convenient place holder, an alternative method could be constructed that does not incorporate the insertion/deletion step. Similar results can be achieved by cyclically permuting the rows and columns of the arrays. Retriangularization is then achieved as before. Furthermore, since the size of the array does not change, it is not necessary to augment the array with an extra column. However, a one-dimensional scratch vector is required.

Cyclic column permutations and retriangularization of SRI arrays have already appeared in the literature [5]. In fact, the discussed method to cyclically permute columns of an SRI array is similar to the left circular shift method used in the LINPACK routine SCHEX [6]. Until now, however, these results have not been extended to cyclic row permutations and retriangularization of UD arrays. The permutations are performed in such a way that the only difference between permuting UD and SRI arrays lies in the retriangularization process.

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II. Row Permutations of UD Arrays

To demonstrate the technique on UD arrays, let the factorization of a covariance $P$ with parameters $U_1, U_2, U_3, U_4, U_5$ be

$$ P = UDU^t $$

where

$$ U = \begin{bmatrix} u_{11} & u_{12} & u_{13} & u_{14} & u_{15} \\ u_{21} & u_{22} & u_{23} & u_{24} & u_{25} \\ u_{31} & u_{32} & u_{33} & u_{34} & u_{35} \\ u_{41} & u_{42} & u_{43} & u_{44} & u_{45} \\ u_{51} & u_{52} & u_{53} & u_{54} & u_{55} \end{bmatrix} $$

and

$$ D = \begin{bmatrix} d_1 & 0 & 0 & 0 & 0 \\ 0 & d_2 & 0 & 0 & 0 \\ 0 & 0 & d_3 & 0 & 0 \\ 0 & 0 & 0 & d_4 & 0 \\ 0 & 0 & 0 & 0 & d_5 \end{bmatrix} $$

If we designate a weighted inner product as

$$ x \cdot y = \sum_i x_i d_i y_i $$

then the covariance can be written in terms of its factorized elements:

$$ P = \begin{bmatrix} u_{11} \cdot u_{11} & u_{11} \cdot u_{12} & u_{11} \cdot u_{13} & u_{11} \cdot u_{14} & u_{11} \cdot u_{15} \\ u_{12} \cdot u_{11} & u_{12} \cdot u_{12} & u_{12} \cdot u_{13} & u_{12} \cdot u_{14} & u_{12} \cdot u_{15} \\ u_{13} \cdot u_{11} & u_{13} \cdot u_{12} & u_{13} \cdot u_{13} & u_{13} \cdot u_{14} & u_{13} \cdot u_{15} \\ u_{14} \cdot u_{11} & u_{14} \cdot u_{12} & u_{14} \cdot u_{13} & u_{14} \cdot u_{14} & u_{14} \cdot u_{15} \\ u_{15} \cdot u_{11} & u_{15} \cdot u_{12} & u_{15} \cdot u_{13} & u_{15} \cdot u_{14} & u_{15} \cdot u_{15} \end{bmatrix} $$

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7Since the UD array is unique, fast Givens reflections cannot be used.

8SCHEX requires two one-dimensional scratch arrays to store the transformation pairs and does not store the upper-triangular array as a vector.
As usual, let $u_i = 1$ to obtain a unique factorization. With this convention, the diagonals of the D array can now be stored as the diagonals of the U array. This new array is referred to as the UD array:

$$UD = \begin{bmatrix}
    d_1 & u_1 & u_3 & u_4 & u_5 \\
    0 & d_2 & u_2 & u_4 & u_5 \\
    0 & 0 & d_3 & u_3 & u_5 \\
    0 & 0 & 0 & d_4 & u_5 \\
    0 & 0 & 0 & 0 & d_5
\end{bmatrix} \quad (6)$$

Say it is desired to reorder the parameters as $U_1, U_3, U_4, U_2, U_5$. The first step is to insert an artificial parameter, with zero variance and zero correlation with the other parameters, before parameter $U_5$. This is accomplished by inserting into the UD array a row of zeros above row 5 and a column of zeros left of column 5:

$$\begin{bmatrix}
    d_1 & u_1 & u_3 & u_4 & 0 & 0 & u_5 \\
    0 & d_2 & u_2 & u_4 & 0 & 0 & u_5 \\
    0 & 0 & d_3 & u_3 & 0 & 0 & u_5 \\
    0 & 0 & 0 & d_4 & 0 & 0 & u_5 \\
    0 & 0 & 0 & 0 & d_5 & 0 & 0
\end{bmatrix} \quad (7)$$

The corresponding covariance is

$$\begin{bmatrix}
    u_1 \cdot u_1 & u_1 \cdot u_2 & u_1 \cdot u_3 & u_1 \cdot u_4 & 0 & u_1 \cdot u_5 \\
    u_2 \cdot u_1 & u_2 \cdot u_2 & u_2 \cdot u_3 & u_2 \cdot u_4 & 0 & u_2 \cdot u_5 \\
    u_3 \cdot u_1 & u_3 \cdot u_2 & u_3 \cdot u_3 & u_3 \cdot u_4 & 0 & u_3 \cdot u_5 \\
    u_4 \cdot u_1 & u_4 \cdot u_2 & u_4 \cdot u_3 & u_4 \cdot u_4 & 0 & u_4 \cdot u_5 \\
    u_5 \cdot u_1 & u_5 \cdot u_2 & u_5 \cdot u_3 & u_5 \cdot u_4 & 0 & u_5 \cdot u_5 \\
\end{bmatrix} \quad (8)$$

Rows 2 and 5 of Eq. (7) can then be exchanged:

$$\begin{bmatrix}
    d_1 & u_1 & u_3 & u_4 & 0 & u_5 \\
    0 & 0 & d_3 & u_3 & 0 & u_3 \\
    0 & 0 & 0 & d_4 & 0 & u_4 \\
    0 & d_2 & u_2 & u_4 & u_5 & 0 \\
    0 & 0 & 0 & 0 & 0 & d_5
\end{bmatrix} \quad (9)$$

In practice, however, elements $d_2, u_2, u_4$ would not be moved, since a scratch array would be needed to store these elements. As will soon be evident, the second row can itself be used as a scratch array. The array in Eq. (9) implicitly corresponds to a nontriangular $\overline{U}$ array

$$\overline{U} = \begin{bmatrix}
    u_1 & u_1 & u_3 & u_4 & 0 & u_5 \\
    0 & 0 & 0 & u_4 & 0 & u_5 \\
    0 & 0 & u_3 & u_3 & 0 & u_5 \\
    0 & u_2 & u_2 & u_2 & u_2 & 0 \\
    0 & 0 & 0 & 0 & 0 & u_5
\end{bmatrix} \quad (10)$$

The upper-triangular elements of either a UD or an SRI array are stored contiguously in computer memory as a vector; the zero elements below the diagonal are obviously not needed to represent the array. 

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9 The upper-triangular elements of either a UD or an SRI array are stored contiguously in computer memory as a vector; the zero elements below the diagonal are obviously not needed to represent the array.
and a covariance $\tilde{\mathbf{P}}$

$$\tilde{\mathbf{P}} = \mathbf{D}^{\dagger} \mathbf{D}$$

\[
\begin{bmatrix}
 u1 \cdot u1 & u1 \cdot u3 & u1 \cdot u4 & u1 \cdot u2 & u1 \cdot u5 \\
 u3 \cdot u1 & u3 \cdot u3 & u3 \cdot u4 & u3 \cdot u2 & u3 \cdot u5 \\
 u4 \cdot u1 & u4 \cdot u3 & u4 \cdot u4 & u4 \cdot u2 & u4 \cdot u5 \\
 u2 \cdot u1 & u2 \cdot u3 & u2 \cdot u4 & u2 \cdot u2 & u2 \cdot u5 \\
 u5 \cdot u1 & u5 \cdot u3 & u5 \cdot u4 & u5 \cdot u2 & u5 \cdot u5 \\
\end{bmatrix}
\] (12)

Except for the artificial parameter, this is the desired permuted covariance. The covariance factorization in Eq. (12) can be thought of as the product of an upper-triangular array and its transpose:

$$\mathbf{P} = \mathbf{U} \sqrt{\mathbf{D}} (\mathbf{U} \sqrt{\mathbf{D}})^\dagger$$

where

$$\sqrt{\mathbf{D}} =
\begin{bmatrix}
 \sqrt{d}_1 & 0 & 0 & 0 & 0 \\
 0 & \sqrt{d}_2 & 0 & 0 & 0 \\
 0 & 0 & \sqrt{d}_3 & 0 & 0 \\
 0 & 0 & 0 & \sqrt{d}_4 & 0 \\
 0 & 0 & 0 & 0 & \sqrt{d}_5 \\
\end{bmatrix}
$$

(13)

It is desirable to construct an orthogonal transformation $\mathbf{T}$ such that $\mathbf{U} \sqrt{\mathbf{D}} \mathbf{T}$ is an upper-triangular array with zeros in the second row and column:

$$\mathbf{U} \sqrt{\mathbf{D}} \mathbf{T} =
\begin{bmatrix}
 u1 \sqrt{d}_1 & 0 & x & x & u1 \sqrt{d}_5 \\
 0 & 0 & 0 & 0 & 0 \\
 0 & 0 & u3 \sqrt{d}_3 & u3 \sqrt{d}_4 & u3 \sqrt{d}_5 \\
 0 & 0 & 0 & u4 \sqrt{d}_4 & u4 \sqrt{d}_5 \\
 0 & 0 & 0 & 0 & u5 \sqrt{d}_5 \\
\end{bmatrix}
$$

(14)

(16)

A portion of the array in Eq. (15) is similar to a Morf-Kailath prearray with zero noise variance. Reduction of this prearray to the postarray in Eq. (16) is easily accomplished with three fast Givens rotations. As a result of the zero noise variance, the first fast Givens $T_0$ is simply a permutation of columns 2 and 5:

$$\mathbf{U} \sqrt{\mathbf{D}} \mathbf{T}_0 =
\begin{bmatrix}
 u1 \sqrt{d}_1 & u1 \sqrt{d}_2 & u1 \sqrt{d}_3 & u1 \sqrt{d}_4 & u1 \sqrt{d}_5 \\
 0 & 0 & 0 & 0 & 0 \\
 0 & 0 & u3 \sqrt{d}_3 & u3 \sqrt{d}_4 & u3 \sqrt{d}_5 \\
 0 & 0 & 0 & u4 \sqrt{d}_4 & u4 \sqrt{d}_5 \\
 0 & 0 & 0 & 0 & u5 \sqrt{d}_5 \\
\end{bmatrix}
\times
\begin{bmatrix}
 1 & 0 & 0 & 0 & 0 \\
 0 & 0 & 0 & 1 & 0 \\
 0 & 0 & 1 & 0 & 0 \\
 0 & 0 & 0 & 1 & 0 \\
 0 & -1 & 0 & 0 & 0 \\
\end{bmatrix}
$$

(17)
This is the required step to produce the zeros in the second row and column. The next fast Givens $T_1$ operates on columns 3 and 5 in such a way that the array element containing $u_{23}\sqrt{d_3}$ becomes zero. The final fast Givens $T_2$ operates on columns 4 and 5 such that the array element containing $u_{24}\sqrt{d_4}$ becomes zero. Each of these Givens transformations maps the elements in each row of the columns on which they operate into a linear combination of the same. Hence, elements not consistent with an upper-triangular structure are never created; a linear combination of zeros is still zero. For example, $T_1$ operates on Eq. (17) as follows:

\[
U\sqrt{B}\sqrt{T_0}T_1 =
\begin{bmatrix}
1 & 0 & 0 & 0 & 0 & 0 \\
0 & u_1\sqrt{d_1} & 0 & u_3\sqrt{d_3} & u_4\sqrt{d_4} & u_2\sqrt{d_2} & u_5\sqrt{d_5} \\
0 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & u_3\sqrt{d_3} & u_3\sqrt{d_4} & 0 & u_5\sqrt{d_5} \\
0 & 0 & 0 & u_4\sqrt{d_4} & 0 & u_5\sqrt{d_5} \\
0 & 0 & u_2\sqrt{d_3} & u_2\sqrt{d_4} & u_2\sqrt{d_5} & 0 & u_5\sqrt{d_5} \\
0 & 0 & 0 & 0 & u_5\sqrt{d_5} & 0 & 0
\end{bmatrix}
\]

\[
= \begin{bmatrix}
1 & 0 & 0 & 0 & 0 & 0 \\
0 & u_1\sqrt{d_1} & 0 & y & y & u_5\sqrt{d_5} \\
0 & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & x & y & y & u_5\sqrt{d_5} \\
0 & 0 & 0 & y & y & u_5\sqrt{d_5} \\
0 & 0 & 0 & 0 & y & u_5\sqrt{d_5} \\
0 & 0 & 0 & 0 & 0 & u_5\sqrt{d_5}
\end{bmatrix}
\]

(19)

Likewise, $T_2$ operates on Eq. (18) as follows:

\[
U\sqrt{B}\sqrt{T_0}T_1T_2 =
\begin{bmatrix}
1 & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & x & u_3\sqrt{d_4} & x & u_3\sqrt{d_5} \\
0 & 0 & 0 & u_4\sqrt{d_4} & 0 & u_4\sqrt{d_5} \\
0 & 0 & u_2\sqrt{d_4} & x & u_2\sqrt{d_5} & 0 \\
0 & 0 & 0 & 0 & u_5\sqrt{d_5} & 0
\end{bmatrix}
\]

Trivially, since the transformations $T_i$ are orthogonal, the covariance $\bar{F}$ in Eqs. (12) and (13) remains unchanged:

\[
\bar{F} = U\sqrt{B}\sqrt{T_0}T_1T_2(U\sqrt{B}\sqrt{T_0}T_1T_2)^T
\]

(20)

The importance of the zeros in the second row and column of Eq. (16) is now readily apparent. Since the insertion of a row and column of zeros into the UD array in Eq. (7) did not affect the individual elements of the covariance in Eq. (8), deletion of the second row and column of zeros from Eq. (16) will have a similar null effect on the individual elements of the covariance $\bar{F}$.  

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III. Column Permutations of SRI Arrays

To demonstrate the technique on SRI arrays, represent an augmented SRI array \( R \) with parameter order \( R_1, R_2, R_3, R_4, R_5 \) as

\[
(R | Z) = \begin{bmatrix}
  r_1 & r_2 & r_3 & r_4 & r_5 & z_1 \\
  0 & r_2 & r_3 & r_4 & r_5 & z_2 \\
  0 & 0 & r_3 & r_4 & r_5 & z_3 \\
  0 & 0 & 0 & r_4 & r_5 & z_4 \\
  0 & 0 & 0 & 0 & r_5 & z_5 \\
\end{bmatrix}
\]

where \( Z \) is the residual vector. The following identity suggests the justification of incorporating an artificial parameter into an SRI array:

\[
\begin{bmatrix}
  R_x & 0 & R_{xy} \\
  0 & e & 0 \\
  0 & 0 & R_y \\
\end{bmatrix}^{-1} = \begin{bmatrix}
  R^{-1}_x & 0 & -R^{-1}_x R_{xy} R^{-1}_y \\
  0 & e^{-1} & 0 \\
  0 & 0 & R^{-1}_y \\
\end{bmatrix}
\]

(21)

If there are no intentions of computing the inverse of the SRI array, it is permissible to allow \( e = 0 \). Hence, the insertion and subsequent deletion of an artificial row and column of zeros into an SRI array does not affect the information content of the array.

Say it is desired to change the parameter order to \( R_1, R_3, R_4, R_2, R_5 \). The first step is to insert a row and column of zeros into the \( (R | Z) \) array before the parameter \( R_5 \):

\[
\begin{bmatrix}
  r_1 & r_2 & r_3 & r_4 & 0 & r_5 & z_1 \\
  0 & r_2 & r_3 & r_4 & 0 & r_5 & z_2 \\
  0 & 0 & r_3 & r_4 & 0 & r_5 & z_3 \\
  0 & 0 & 0 & r_4 & 0 & r_5 & z_4 \\
  0 & 0 & 0 & 0 & 0 & r_5 & z_5 \\
\end{bmatrix}
\]

(23)

Columns 2 and 5 of Eq. (23) can then be exchanged. This is just a reordering of the design equation

\[
(R | Z) = \begin{bmatrix}
  r_1 & 0 & r_3 & r_4 & r_2 & r_5 & z_1 \\
  0 & 0 & r_2 & r_4 & r_2 & r_5 & z_2 \\
  0 & 0 & r_3 & r_4 & 0 & r_5 & z_3 \\
  0 & 0 & 0 & r_4 & 0 & r_5 & z_4 \\
  0 & 0 & 0 & 0 & 0 & r_5 & z_5 \\
\end{bmatrix}
\]

(24)

It is desirable to construct an orthogonal transformation \( T_0 \) such that \( T_0(R | Z) \) has only zeros in the second row and column. Such a transformation is just a permutation of rows 2 and 5:

\[
T_0(R | Z) = \begin{bmatrix}
  1 & 0 & 0 & 0 & 0 & 0 \\
  0 & 0 & 0 & 1 & 0 & 0 \\
  0 & 0 & 1 & 0 & 0 & 0 \\
  0 & 1 & 0 & 0 & 0 & 0 \\
  0 & 0 & 0 & 0 & 0 & 1 \\
\end{bmatrix}
\]

(22)

Say it is desired to change the parameter order to \( R_1, R_3, R_4, R_2, R_5 \). The first step is to insert a row and column of zeros into the \( (R | Z) \) array before the parameter \( R_5 \):

\[
\begin{bmatrix}
  r_1 & 0 & r_3 & r_4 & 0 & r_5 & z_1 \\
  0 & 0 & r_3 & r_4 & 0 & r_5 & z_2 \\
  0 & 0 & r_3 & r_4 & 0 & r_5 & z_3 \\
  0 & 0 & 0 & r_4 & 0 & r_5 & z_4 \\
  0 & 0 & 0 & 0 & 0 & r_5 & z_5 \\
\end{bmatrix}
\]

(25)
In practice, however, elements $r_{32}$ and $r_{42}$ would not be moved, since a scratch array would be needed to store these elements. As is evident, the second row can itself be used as a scratch array. It is now a simple matter of retriangularizing Eq. (25) with two slow Givens. The first Givens $T_1$ operates on rows 3 and 5 in such a way that the array element containing $r_{32}$ becomes zero. The second Givens $T_2$ operates on rows 4 and 5 in such a way that the array element containing $r_{42}$ becomes zero. Each of these Givens transformations maps the elements in each column of the rows on which they operate into a linear combination of the same. Elements not consistent with an upper-triangular structure are never created.

For example, $T_1$ operates on Eq. (25) as follows:

$$T_1 T_0 (R | Z) = $$

Likewise, $T_2$ operates on Eq. (26) as follows:

$$T_2 T_1 T_0 (R | Z) = $$

From Eq. (22) it is a trivial matter that the second row and column of zeros of Eq. (27) can then be deleted.

**IV. Cyclic Permutations**

Since the artificial parameter is only a convenient place holder, an alternative method can be constructed that does not incorporate the insertion/deletion step. By cyclically permuting the rows and columns of either the UD or SRI arrays, a data structure similar to Eq. (17) or Eq. (25), respectively, can be achieved. For example, cyclically left permuting columns 2 through 4 of Eq. (21) yields the following:
Cyclically upward permuting rows 2 through 4 of Eq. (28) then yields

\[
\begin{bmatrix}
  r_1 & r_3 & r_4 & r_2 & r_5 \\
  0 & r_3 & r_4 & 0 & r_5 \\
  0 & 0 & r_4 & 0 & r_5 \\
  0 & 0 & 0 & 0 & r_5
\end{bmatrix} \quad z_1
\]

(28)

This is the desired data structure of Eq. (25) without the artificial parameter. Slow Givens sweeps across rows 2, 4 and rows 3, 4 of Eq. (29) can then be applied to zero out the array elements containing \(r_{32}\) and \(r_{42}\), respectively. Since the size of the array never changes, this method would not require that the array be augmented with an extra column. However, one explicit scratch array is required to hold that part of Eq. (29) not consistent with an upper-triangular data structure.

A similar approach can also be taken toward permuting UD arrays. Cyclic permutations of the rows and columns would yield the data structure in Eq. (17) without the artificial parameter:

\[
\begin{bmatrix}
  u_1 \sqrt{a_1} & u_3 \sqrt{a_3} & u_4 \sqrt{a_4} & u_2 \sqrt{a_2} & u_5 \sqrt{a_5} \\
  0 & u_3 \sqrt{a_3} & u_3 \sqrt{a_4} & 0 & u_5 \sqrt{a_5} \\
  0 & 0 & u_4 \sqrt{a_4} & 0 & u_5 \sqrt{a_5} \\
  0 & u_2 \sqrt{a_2} & u_2 \sqrt{a_4} & u_2 \sqrt{a_5} \\
  0 & 0 & 0 & 0 & u_5 \sqrt{a_5}
\end{bmatrix}
\]

(30)

Fast Givens sweeps down columns 2, 4 and columns 3, 4 of Eq. (30) can then be applied to zero out the array elements containing \(u_2 \sqrt{a_2}\) and \(u_2 \sqrt{a_4}\), respectively.

V. Implementation

When dealing with large arrays on a virtual memory machine, computations should be arranged to minimize expensive page faulting. The Givens sweeps should access contiguous storage locations. The usual column ordering of a vector-stored upper-triangular data structure with \(n\) rows and \(n\) columns is as follows:

\[
S_{\text{column}} = \begin{bmatrix}
  s(1) & s(2) & s(4) & \ldots & s\left(\frac{n^2-n}{2} + 1\right) \\
  0 & s(3) & s(5) & \ldots & s\left(\frac{n^2-n}{2} + 2\right) \\
  0 & 0 & s(6) & \ldots & s\left(\frac{n^2-n}{2} + 3\right) \\
  \vdots & \vdots & \vdots & \ddots & \vdots \\
  0 & 0 & 0 & \ldots & s\left(\frac{n^2+n}{2}\right)
\end{bmatrix}
\]

(29)

Element \(S_{\text{column}}(i, j)\) is easily referenced as \(s[(i^2 - i/2) + j]\). For UD arrays, the Givens sweeps down the columns using the data storage in Eq. (31) would require minimal paging. For SRI arrays, the Givens sweeps across the rows with the data storage in Eq. (31) would be paging intensive. Hence, for SRI arrays, it proves to be more efficient to store the upper-triangular array with unnatural row ordering as follows:

\[
S_{\text{row}} = \begin{bmatrix}
  s(1) & s(2) & s(3) & \ldots & s(n) \\
  0 & s(n + 1) & s(n + 2) & \ldots & s(2n - 1) \\
  0 & 0 & s(2n) & \ldots & s(3n - 3) \\
  \vdots & \vdots & \vdots & \ddots & \vdots \\
  0 & 0 & 0 & \ldots & s\left(\frac{n^2+n}{2}\right)
\end{bmatrix}
\]

(32)

Element \(S_{\text{row}}(i, j)\) can be referenced as \(s[j + (i^2 - i/2) + (i - 1)(n - i)]\). Since filtering software generally stores

\[S_{\text{row}}(i, j) = S_{\text{column}}(i, j);\] this is just a relabeling of the elements.
upper-triangular arrays according to Eq. (31), it would be necessary to relabel from $S_{\text{column}}$ to $S_{\text{row}}$ before the permutations and then back to $S_{\text{column}}$ afterward.

It is equally easy to perform cyclic permutations with either Eq. (31) or Eq. (32). In the former, movement is along the columns; in the latter, movement is across the rows. The only paging-intensive step in cyclically permuting the rows and columns with data storage $S_{\text{column}}$ ($S_{\text{row}}$) is in storing the upper row (left column) into a scratch array.

### VI. Performance

To assess the permutation methods on UD arrays, an upper-triangular array was constructed with 499 parameters and an estimate column. The following permutations were then performed on a VAX 11/780. First, the parameters were permuted randomly. In practice, of course, this would never happen. It is more realistic to move blocks of parameters to the bottom of the array. Therefore, the following systematic permutations were performed. A block of 10 parameters starting in rows 1, 167, and 334 were moved to the bottom of the array. These permutations were then repeated with a block of 100 parameters. CPU time and page faults were accumulated for three different methods. First, the ESL routine HHPOST was used to apply a post-Householder to a row-reordered column-scaled UD array. Second, pairwise permutations were performed with suboptimal noise-free measurement updates. This is equivalent to the ESL routine U2U. Third, the permutations were performed with the discussed method of cyclic permutations with fast Givens sweeps. The results are presented in Table 1.

The ESL method with HHPOST performs the same regardless of the number and type of permutations performed. The performance of the cyclic permutations with slow Givens sweeps depends on the number and type of permutations. As the starting column is moved to the right, not only are there fewer elements to retriangularize, but the lengths of Givens sweeps also become smaller, and fewer computations are required. Since the VAX 11/780 is a virtual memory machine, the method with data storage $S_{\text{row}}$ is generally superior to the method with data storage $S_{\text{column}}$. For fixed memory machines or small SRI arrays, the method with data storage $S_{\text{column}}$ should be superior, since no relabeling or unlabeling is required.

### VII. Conclusion

The most efficient method to permute UD (SRI) arrays has been shown to be cyclic permutation with fast (slow) Givens sweeps for retriangularization. Since the only difference is in the retriangularization process, it has been possible to combine the methods into one subroutine. This method has been incorporated into the OASIS software [7] and should prove to be advantageous for GPS, TOPEX, and DSN high-earth-orbiter data processing and analysis.
References


### Table 1. CPU and paging faults in permuting UD arrays

<table>
<thead>
<tr>
<th>Permutations</th>
<th>CPU time; thousands of page faults</th>
<th>ESL U2U</th>
<th>Cyclic permutations and fast Givens</th>
</tr>
</thead>
<tbody>
<tr>
<td>Random</td>
<td>0:33:06</td>
<td>1:06:31</td>
<td>0:17:04</td>
</tr>
<tr>
<td></td>
<td>1025</td>
<td>2051</td>
<td>612</td>
</tr>
<tr>
<td>Rows</td>
<td>0:32:43</td>
<td>0:48:55</td>
<td>0:01:24</td>
</tr>
<tr>
<td>1–10</td>
<td>948</td>
<td>1823</td>
<td>40</td>
</tr>
<tr>
<td>Rows</td>
<td>0:31:49</td>
<td>0:40:00</td>
<td>0:01:14</td>
</tr>
<tr>
<td>167–176</td>
<td>970</td>
<td>1338</td>
<td>35</td>
</tr>
<tr>
<td>Rows</td>
<td>0:31:12</td>
<td>0:24:26</td>
<td>0:00:45</td>
</tr>
<tr>
<td>334–343</td>
<td>970</td>
<td>728</td>
<td>21</td>
</tr>
<tr>
<td>Rows</td>
<td>0:32:44</td>
<td>0:59:25</td>
<td>0:11:23</td>
</tr>
<tr>
<td>1–100</td>
<td>968</td>
<td>1985</td>
<td>363</td>
</tr>
<tr>
<td>Rows</td>
<td>0:31:32</td>
<td>0:48:47</td>
<td>0:08:46</td>
</tr>
<tr>
<td>167–266</td>
<td>946</td>
<td>1433</td>
<td>275</td>
</tr>
<tr>
<td>Rows</td>
<td>0:30:59</td>
<td>0:27:29</td>
<td>0:02:20</td>
</tr>
<tr>
<td>334–433</td>
<td>973</td>
<td>747</td>
<td>52</td>
</tr>
</tbody>
</table>

### Table 2. CPU and paging faults in permuting SRI arrays

<table>
<thead>
<tr>
<th>Permutations</th>
<th>CPU time; thousands of page faults</th>
<th>Cyclic permutations and slow Givens using data storage</th>
<th>Cyclic permutations and slow Givens using data storage</th>
</tr>
</thead>
<tbody>
<tr>
<td>Random</td>
<td>0:22:28</td>
<td>0:41:09</td>
<td>0:20:07</td>
</tr>
<tr>
<td></td>
<td>729</td>
<td>3253</td>
<td>575</td>
</tr>
<tr>
<td>Columns</td>
<td>0:20:29</td>
<td>0:01:36</td>
<td>0:01:38</td>
</tr>
<tr>
<td>1–10</td>
<td>717</td>
<td>48</td>
<td>55</td>
</tr>
<tr>
<td>Columns</td>
<td>0:21:13</td>
<td>0:00:55</td>
<td>0:01:03</td>
</tr>
<tr>
<td>167–176</td>
<td>723</td>
<td>31</td>
<td>41</td>
</tr>
<tr>
<td>Columns</td>
<td>0:21:53</td>
<td>0:00:20</td>
<td>0:00:39</td>
</tr>
<tr>
<td>334–343</td>
<td>719</td>
<td>11</td>
<td>27</td>
</tr>
<tr>
<td>Columns</td>
<td>0:21:25</td>
<td>0:13:11</td>
<td>0:11:10</td>
</tr>
<tr>
<td>1–100</td>
<td>727</td>
<td>424</td>
<td>411</td>
</tr>
<tr>
<td>Columns</td>
<td>0:21:15</td>
<td>0:06:14</td>
<td>0:05:14</td>
</tr>
<tr>
<td>167–266</td>
<td>714</td>
<td>229</td>
<td>205</td>
</tr>
<tr>
<td>Columns</td>
<td>0:21:03</td>
<td>0:00:51</td>
<td>0:01:43</td>
</tr>
<tr>
<td>334–433</td>
<td>723</td>
<td>52</td>
<td>104</td>
</tr>
</tbody>
</table>