DOPPLER-CORRECTED DIFFERENTIAL DETECTION SYSTEM

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Application No.: 203,374
Filed: Jun. 7, 1988

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ABSTRACT
Doppler in a communication system operating with a multiple differential phase-shift-keyed format (“MDPSK”) creates an adverse phase shift in an incoming signal. An open loop frequency estimation is derived from a Doppler-contaminated incoming signal. Based upon the recognition that, whereas the change in phase of the received signal over a full symbol contains both the differentially encoded data and the Doppler effect, the same change in phase over half a symbol (within a given symbol interval) contains only the doppler induced phase shift, the same change in phase over half a symbol (within a given symbol interval) contains only the doppler induced phase shift, and the Doppler effect can be estimated and removed from the incoming signal. Doppler correction occurs prior to the receiver’s final output of decoded data. A multiphase system can operate with two samplings per symbol interval at no penalty in signal-to-noise ratio provided that an ideal low pass pre-detection filter is employed, and two samples, at $\frac{1}{4}$ and $\frac{3}{4}$ of the symbol interval $T_s$ are taken and summed together prior to incoming signal data detection.

31 Claims, 13 Drawing Sheets
**FIG. 1**

PRIORITY ART

![Diagram of Prior Art](image)

**FIG. 2**

![Diagram of New Design](image)
FIG. 5
FIG. 6
FIG. 10
FIG. 13

- ANALYSIS
- X X X SIMULATION (AWGN)
- ■ ■ ■ SIMULATION (FADING)

![Graph showing comparison between analysis and simulation for different values of Ka (10, 20, 40, 160) across Eb/No dB.](image)
FIG. 14
DOPPLER-CORRECTED DIFFERENTIAL DETECTION SYSTEM

ORIGIN OF THE INVENTION

The invention described herein was made in the performance of work under a NASA contract, and is subject to the provisions of Public Law 96-517 (35 USC 202) in which the Contractor has elected not to retain title.

TECHNICAL FIELD

This invention relates to an improved communication system which is provided with a Doppler-corrected differential detector located in the communication system's receiver.

BACKGROUND ART

Digital data communication systems employing a carrier that is modulated by data at a transmitter and demodulated at a receiver to decode the transmitted data are well known. Several different types of modulation and demodulation are known and used. This invention pertains to a type of modulation-demodulation which employs data transmission through multiple differential phase-shift-keying ("MDPSK") at the modulator and a comparable differential detection at the system's receiver or demodulator.

Modems, a contraction for modulators and demodulators, are commonly used at both ends of a data communication transmission system. When one modem physically moves relative to another modem in a transmission link, the transmitted signals experience a Doppler effect. Typically Doppler effects are experienced in an earth to space satellite link and cause an effective change of frequency of a received signal due to the relative velocity of the link's transmitter with respect to the link's receiver. The Doppler effect must be rectified in order to achieve efficient data transmission and reception.

A prior art search related to the invention discloses some typical approaches to Doppler correction. Not one of the references located in the search, however, discloses or suggests an open-loop Doppler frequency estimation derived from an incoming signal. Moreover, open-loop correction for a Doppler effect in a MDPSK system having an improved filtering and differential detection configuration is not disclosed in the prior art.

The following search-located U.S. Pat. Nos. include: 4,472,817, Poklemba; 4,419,759, Poklemba et al 4,472,817 and Mehrgardt et al 4,663,595.

Heard '493 discloses a complex closed-loop Doppler correction scheme that decodes and then attempts to correct for the next expected Doppler effect. Such closed loop tracking is not feasible in certain Doppler-related-applications e.g., a multipath fading environment. Poklemba et al '817 discloses open loops but does not disclose Doppler correction in a differential detection system. Poklemba '759 is a typical phase-locked loop system. Mehrgardt '959 is of marginal interest only in that it discloses delays and summers in a form typically known in the modulator/demodulator art.

A multipath fading environment creates uncertainties in the carrier phase and/or frequency. These uncertainties essentially eliminate the known techniques which use a phase and/or frequency tracking synchronization loop. Moreover, the above-noted uncertainties may be further masked by phase jitter on the carrier reference. This additional transmission impairment adds further complexity to the system operation. Differential detection employs, as a demodulation reference, the phase and frequency of the carrier corresponding to the previous transmitted data symbol. The deleterious effects in a multipath fading environment are largely avoided by that differential system approach because there is no requirement that the carrier must be synchronized during each present symbol interval.

Another known transmission impairment is intersymbol interference (ISI). It is known to use pulse shaping so that the ISI at a receiver is at a minimum at the sampling point for data detection by the receiver. Splitting signal filtering between the transmitter and receiver locations is a known way of achieving pulse shaping and minimum ISI.

For MDPSK systems it is common to strive for what is known as a 100% raised-cosine spectrum at the decoder's input. In an MDPSK system such shaping will achieve a satisfactory signal to noise ratio for center sampling at the decoder or demodulator. Various types of multilevel coding and decoding schemes have been introduced in efforts to achieve an optimum signal to noise ratio for such center sampling. Included within these multilevel schemes is the so-called duobinary signaling, a partial response signaling scheme as taught in A. Lender, "The Duobinary Technique for High Speed Data Transmission," IEEE Transactions on Communication Electronics, Vol. 82, May 1963, pp. 214–218.

STATEMENT OF THE INVENTION

The invention features an open loop frequency estimation derived from a Doppler-contaminated data-modulated input signal. A Doppler estimate is obtained in a doppler-estimating circuit and that estimate is followed by Doppler correction prior to the receiver's decoding of the modulated data. An overall Doppler-corrected differential detection system of simple and efficient design is thus provided.

One of the key features of the invention is based upon the inventor's recognition that whereas the change in phase of the received signal over a full symbol contains the sum of the differentially encoded data and the Doppler induced phase shift, the same change in phase over half a symbol (within a given symbol interval) contains only the Doppler induced phase shift. Thus, by proper open-loop processing, including post-detection integration (whose duration is related to the rate of change of the Doppler), the Doppler effect can be estimated and removed from the incoming signal.

It has been further discovered that a multiphase system can operate with two samplings per symbol interval at no penalty in signal-to-noise ratio. This discovery is possible provided that an ideal low pass pre-detection filter is employed, and two samples, at $\frac{1}{4}$ and $\frac{3}{4}$ of the symbol interval $T_s$, are taken and summed together prior to decoding.

BRIEF DESCRIPTION OF THE DRAWING

FIG. 1 depicts a prior art differentially coherent receiver employing a full symbol delay;

FIG. 2 is a basic block diagram of an open-loop Doppler-corrected differentially coherent receiver system in accordance with the invention;

FIG. 3 depicts an I-F implementation of the invention of FIG. 2;
FIG. 4 depicts a baseband implementation of the invention of FIG. 2; FIG. 5 is a set of curves useful in demonstrating the performance of the invention; FIG. 6 is another set of curves also useful for demonstrating the invention's performance; FIG. 7 is a simple building-block diagram depicting the Doppler-corrected receiver in complex form, in a tracking mode; FIG. 7A supplies a more detailed form of the Doppler estimator of FIG. 7; FIG. 7B depicts a fast acquisition circuit for Doppler correction; FIG. 7C depicts a fast acquisition circuit which is equivalent to that depicted in FIG. 7B; FIG. 8 is a block diagram, employing the building blocks of FIG. 7, 7A and 7C, to form a dual mode Doppler-corrected differential detector in complex form; FIG. 9 is the dual mode Doppler-corrected MDPSK receiver of the invention using quadrature components of a received baseband signal; FIG. 10 is an eye diagram that shows sample points for zero intersymbol interference for the system of FIG. 9; FIG. 11 is a mathematical model which is useful in describing a more complex feature of the invention; FIG. 12 is a mathematical equivalent of FIG. 11; FIG. 13 is a graph depicting the improvement in results achieved by the invention in the absence of time jitter; and FIG. 14 is a graph similar to that of FIG. 13 depicting results in the presence of ten and twenty percent time jitter.

DETAILED DESCRIPTION OF THE INVENTION

FIG. 1 is a typical block diagram of a known differential detector in a MDPSK receiver. At input terminal 5 an input signal s(t) which may include modulated data and Gaussian noise is split into an in-phase (I) and a quadrature phase (Q) channel 10 and 20 respectively. The quadrature phase channel 20 includes a 90 degree phase shifter 15.

Each channel includes a full symbol interval delay provided by two half-symbol delay networks 31, 32 and 41, 42. Each network thus provides a one-half symbol delay time. Multiplying junctions 35, 45 multiply the undelayed and delayed signals together to form a basis for comparing two different time-related phase angles of the input signals applied to those multiplying junctions 35, 45. The output of each multiplying junction is applied to a detector 50, which detector determines the angular difference between s(t) and s(t) delayed by one symbol and applies a decoded output signal an output lead 6. Both in-phase (I) and quadrature phase (Q) channels 10, 20 are required to represent the well-known phasor diagrams of the prior art.

FIG. 2 includes the broad concept of the Doppler-corrected differential phase detection of the invention. A key feature of the invention is provided by a Doppler estimator circuit 100 which requires input signals from both the in-phase channel 10 and the quadrature channel 20. The in-phase channel 10 supplies a pair of input signals to circuit 100 in the form of an incoming signal s(t) and a delayed version thereof. Doppler estimator circuit 100 receives the incoming signal s(t) over lead 90 and also receives a one-half (T/2) delayed version of that incoming signal over lead 101, which lead connects the output of the T/2 delay circuit 31 to Doppler estimator 100. Simultaneously the quadrature channel 20 applies an input signal to circuit 100 in the form of a phase shifted s(t) and a delayed version thereof. Being delayed by less than a full symbol interval, the input signals from channels 10 and 20, into Doppler estimator 100 have the same data-representing phase; but, with proper processing, the Doppler-induced change in phase in the incoming signal s(t) can be estimated in circuit 100.

Multipliers 35 and 45, operating as before, are connected as in FIG. 1. Doppler removal is done in both the channels and it will now be described.

Between the multipliers 35, 45 and decoder 50 are located Doppler removal circuits 125 and 150. Each Doppler removal circuit receives the estimated Doppler signal from estimator 100 and that estimated signal, prior to signal decoding in detector 50, is removed by Doppler removal circuits 125, 150 in the (I) and (Q) channels 10, 20, respectively. With the Doppler effect nullified, detector 50, in known fashion, delivers a data-representing output signal on lead 6 in accordance with standard MDPSK decoding techniques.

The detector 50 may be of any suitable and known type. Such detectors include the type which employ a maximum-likelihood decoding algorithm. Further processing of the output signal on lead 6 depends upon whether the transmitted data is coded or uncoded. In the case of uncoded data, a decision on the output is standard MDPSK decoding techniques.

A more detailed and rigorous description of the invention is now presented with reference to FIG. 3. In FIG. 3, the Doppler-contaminated input signal s(t) is shown at input terminal 5. That signal is defined as:

\[ s(t) = \sqrt{2P} \sin[(\omega_0 + \Delta\omega)t + \theta(t)] \]  

where the carrier is the Doppler shift and \( \theta(t) \) is the data modulated phase information in the form of a predetermined angle shift and some arbitrary phase shift that may be neglected. The input signal will also contain Gaussian noise \( n(t) \) at lead 7, which noise is defined as:

\[ n(t) = P \sum (n(t) \cos(\omega_0 + \Delta\omega)t + n(t) \sin(\omega_0 + \Delta\omega)t) \]  

where \( P \) denotes the average signal power in watts, \( \Delta\omega = 2\pi\beta f \) is the radial Doppler shift, \( s(t) \) is MPSK modulation with symbol rate \( 1/T_s \), and \( n(t), n(t) \) is low-pass Gaussian noise processes with one-sided power spectral density \( N_0 \) W/Hz.

Input leads 5 and 7 are connected to summing junction 14, which junction, in turn, is connected to a band pass IF filter 33 which functions to limit the noise in the summed signal. After passing the sum of \( s(t) \) and \( n(t) \) through the IF filter 33 (which for simplicity of analysis is assumed not to distort \( s(t) \)), the resulting signal, \( y(t) \), is then processed in parallel by the in-phase (I) and quadrature phase (Q) channels 10 and 20. These two channels produce signals suitable for data detection and Doppler estimation in accordance with the present invention.
In the Doppler estimation circuit 100 of the receiver, the in-phase and quadrature input components (delayed \(T_s/2\) by delay circuits 31, 41) are multiplied at multipliers 21, 22 with the undelayed input itself. After zonal filtering by IF filters 34, 36 the second harmonic of the carrier has been removed, leaving the following signal:

\[
x_k(t) = x(t) \cos(T_s/2)
\]

\[
x_r(t) = x(t) \sin(T_s/2)
\]

where

\[
N_k(t) = n(t - T_s/2) + n(t - T_s/2) + n(t)
\]

\[
N_r(t) = n(t) + n(t - T_s/2) + n(t)
\]

In the above equations (3) and (4) \(N_k(t)\) and \(N_r(t)\) are noise terms, and the subscript 90 refers to a 90° phase shift in the signal having that subscript. Making the assumption, as is typical in differential detection analyses, that \(\omega_0 T_s/2\) is an integer multiple of \(2\pi\), then Equation (3) may be simplified to

\[
x_k(t) = P \cos(\Delta \omega T_s/2 + \theta(t)) + N_k(t)
\]

\[
x_r(t) = P \sin(\Delta \omega T_s/2 + \theta(t)) + N_r(t)
\]

Since the MPSK modulation \(\theta(t)\) is constant over a symbol time, then \(\theta(t) - \theta(t - T_s/2) = 0\) in the latter half of every symbol interval.

In FIG. 3, switches 81, 82 denote time sampling by any conventionally well-known sampling circuits. If we sample \(x_k(t)\) and \(x_r(t)\) at \(t_k=\frac{(4k+3)}{4} T_s\), when \(k\) is equal to 1, 2, 3 etc., then these sample values denoted by \(x_k\) and \(x_{rk}\), respectively, become

\[
x_k = P \cos(\Delta \omega T_s/2 + N_k)
\]

\[
x_r = P \sin(\Delta \omega T_s/2 + N_r)
\]

The signals \(x_k\) and \(x_r\) are sampled by switches 81, 82 respectively and these samples are amplified by gain circuits 91, 92 each of which provide a gain term of \(1 - \alpha\). Equation 6 shows that the data term has disappeared and we are left with the Doppler estimation signal. Finally, filtering each sample sequence to reduce undesired phase noise, an estimate \(\Delta \omega T_s\) of the normalized Doppler \(\Delta \omega T_s\) can be obtained. The required filtering is performed using digital filters 93, 94 with gain coefficient \(0 < \alpha < 1\). In such a case, the Doppler estimate, \(\Delta \omega T_s\), is given by

\[
\Delta \omega T_s = 2 \tan^{-1} \left( \frac{(1 - \alpha) \sum_{k=0}^{\infty} a^{x_{rk,k-1}}} {(1 - \alpha) \sum_{k=0}^{\infty} a^{x_{rk,k-1}}} \right)
\]

Taking the sine and cosine of this estimate, namely,

\[
y_k = \cos \Delta \omega T_s
\]

\[
y_r = \sin \Delta \omega T_s
\]

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-continued

\[
y_k(t) = \cos(\Delta \omega T_s + \Delta \theta_k) + N_{rk}
\]

\[
y_r(t) = \sin(\Delta \omega T_s + \Delta \theta_k) + N_{rk}
\]

where \(\Delta \theta_k\) is the \(k\)th data symbol phase before differential encoding at the transmitter, \(\Delta \omega\) is the undesired Doppler term, and \(N'_{rk}\) and \(N'_{rk}\) are input noise terms. The noise free sample values at the output of switches 53 and 54 are respectively applied as one input signal each to multiplier junctions 111 and 116. These junctions 111, 116 also receive additional input signals as processed by Doppler estimator circuit 100.

Proper detection of \(\Delta \theta_k\) requires removal of the Doppler from the signals in equation (11). The term \(T_s/2\) is a tangent ratio, as is seen by an examination of equation (6), and thus we can use an arc tangent circuit 95 of any well-known type connected to the outputs of digital filters 93, 94. Multiplier circuit 96 multiplies the input thereto by a factor of 2 to produce the estimate \(\Delta \omega T_s\) rather than \(\Delta \omega T_s/2\). The quadrature Doppler estimate signals of equation (8) are cross-correlated by circuits 110, 117 and 115, 112. These cross-correlated signals are processed in the form:
ever, is that here, in the baseband configuration, we are dealing with baseband signals rather than IF signals and is therefore described as follows. In the IF operation signals are multiplied by the in-phase (I) and quadrature (Q) demodulators and are removed by the zonal filters. The primary differences between the IF and baseband implementations (FIGS. 3 and 4) are described as follows. In the IF operation signals are delayed and multiplied by the input IF filter bandwidth, and the sum terms represent second harmonics of the carrier which contain either the Doppler alone (Eqs. 25 and 26) or the sum of the data and Doppler (Eq. 27). The non-Gaussian orthogonal components \( A_{ck} \) and \( A_{ek} \) are uncorrelated. Furthermore, \( B_{ck} \) and \( A_{ck} \) both have zero mean and variance

\[
\sigma^2 = \frac{1 + \frac{\sigma_d}{2\beta}}{B}
\]

which is common to all baseband and IF equivalent circuitry. FIG. 4 requires no further discussion because it uses the features and principles described in connection with FIG. 3 hereinafore. PERFORMANCE OF THE DOPPLER ESTIMATOR

To assess the performance of the Doppler estimator operating in accordance with this invention as described hereinbefore, we must characterize the statistical properties of the noise samples \( N_{ck} \) and \( N_{ek} \). Using terms (1) and (2) in equation (4), it is straightforward to show that

\[
\begin{align*}
N_{ck} &= A_{ck} \cos \Delta \omega T_{s/2} - A_{ek} \sin \Delta \omega T_{s/2} \\
N_{ek} &= A_{ek} \sin \Delta \omega T_{s/2} + A_{ek} \cos \Delta \omega T_{s/2}
\end{align*}
\]

where \( A_{ck} \) and \( A_{ek} \) are, respectively, samples (at \( T_k = (4k + 3)/4 T_s \)) of \( A_c(t) \) and \( A_e(t) \) defined by

\[
A_c(t) = \sum_{n \in \mathbb{Z}} a_c(t - n T_s) \cos \omega_c t + b_c(t - n T_s) \sin \omega_c t
\]

and

\[
A_e(t) = \sum_{n \in \mathbb{Z}} a_e(t - n T_s) \cos \omega_c t - b_e(t - n T_s) \sin \omega_c t
\]

The difference terms represent the desired signals that are available in the coding format. These reference phasors for the decision making process are applied to the two outputs of multipliers and are used to select the phase angle that is closest to the various reference angles.

The I and Q demodulators receive the sine and cosine terms of a carrier in the form

\[
\begin{align*}
I(t) &= A_c(t) \cos \omega_c t + e(t) \\
Q(t) &= A_e(t) + e(t)
\end{align*}
\]

where \( e(t) \) is the noise process. It is the output of the carrier frequency producing sum and difference terms. The sum terms represent second harmonics of the carrier and are removed by the zonal filters.

In the baseband operation, the carrier is first removed from the input by the in-phase (I) and quadrature (Q) demodulators and in FIG. 4 which demodulators receive the sine and cosine terms of a carrier in conventional operation. The demodulation once again produces sum and difference terms. The sum terms (appearing at leads 114 and 115 at the output of filters 412, 413) are processed using delay and multiply operations to produce the desired signals needed for data detection and Doppler estimation. One difference, however, is that here, in the baseband configuration, we must sum (at 416, 417, 418 and 419) the I x I and Q x Q signals with the Q x Q and Q x I signals, respectively, to yield the proper signals. (The subscript "T" denotes a T-sec or T/2-sec, delayed version of the respective signal as appropriate). In the IF configuration, the required signals are formed from the I x I and Q x Q signals alone. This difference is a consequence of multiplying baseband signals rather than IF signals and is
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(no Doppler) MDPSK, the average symbol error probability is given by

\[
P_s = \sin \frac{\pi}{M} \int_0^{\pi/2} \exp \left[ \frac{-E_b}{N_0} \left( 1 - \cos \left( \frac{\pi}{M} - \Delta \Phi \right) \cos \alpha \right) \right] \, d\alpha
\]

where \( E_b = (\log_2 M) E_b \) is the energy per symbol with \( E_b \) the bit energy.

When Doppler is present, which results in a noisy phase \( \Delta \Phi = (\Delta \omega - \Delta \phi) T_s \) being added to the decision variable [see (13)], then using Eqs. (11) and (43) of the above-noted Pawula et al. article, equation (21) is modified to

\[
P_s = \int_{-\pi}^{\pi} p_s(\Delta \Phi) \, d\Delta \Phi = 2 \int_0^{\pi/2} p_s(\Delta \Phi) \, d\Delta \Phi = \frac{1}{2} \left( 1 + \frac{N_0 B}{2P} \right)
\]

which for \( \Delta \Phi = 0 \) reduces to (21). Averaging (22) over the probability density function of \( \Delta \Phi \) gives the average symbol error probability performance of the Doppler-corrected differential detector, namely,

\[
P_s = \int_{-\pi}^{\pi} P_s(\Delta \Phi) \, p_s(\Delta \Phi) \, d\Delta \Phi = 2 \int_0^{\pi/2} P_s(\Delta \Phi) \, p_s(\Delta \Phi) \, d\Delta \Phi
\]

where \( P_s(\Delta \Phi) \) is given by (19).

EVALUATION OF THE AVERAGE BIT ERROR PROBABILITY

To evaluate the average bit error probability, we follow the procedure derived by Lee in "Computation of the Bit Error Rate of Coherent Mary PSK with Gray Code Bit Mapping," IEEE Transactions on Communications, Vol. COM-34, No. 5, May 1986, pp. 488-490 for ideal MPSK and MDPSK and extend it to the case where a noisy phase is added to the decision variable. In particular, we define

\[
A_s(\Delta \Phi) = \frac{\sin (\Delta \Phi - (2i + 1) \frac{\pi}{M})}{4\pi} \times \int_{-\pi/2}^{\pi/2} \exp \left[ \frac{-E_b}{N_0} \left( 1 - \cos \left( \Delta \Phi - (2i + 1) \frac{\pi}{M} \right) \cos \alpha \right) \right] \, d\alpha
\]

which represents the probability that the detector output phase is in the region \([2i-1]/M, (2i+1)/M\] under the hypothesis that the input phase \( \Delta \phi = 0 \). Then, the
conditional symbol error probability of (22) can be expressed as

\[ P_d(\Delta \Phi) = \sum_{i=1}^{M-1} A_i(\Delta \Phi) \]  

(25)

with

\[ F(\omega, \beta) = \frac{\sin(\beta - \omega)}{4\pi} \int_{-\pi}^{\pi} \exp \left[ -\frac{E_t}{N_0} \frac{(1 - \cos(\beta - \omega) \cos \alpha)}{1 - \cos(\beta - \omega) \cos \alpha} \right] \, d\alpha \]  

(32)

and assuming a Gray code bit assignment with Hamming weights \( w_m \), the conditional bit error probability is given by

\[ P_d(\Delta \Phi) = \frac{1}{\log_2 M} \sum_{m=1}^{M-1} w_m A_m(\Delta \Phi) \]  

(26)

Here \( w_m \) represents the number of bit errors that occur if the detector output phase lies in the region corresponding to \( A_m(\Delta \Phi) \).

Recognizing from (24) that \( A_m(\Delta \Phi) = A_{M - m}(\Delta \Phi) \), and since \( P(\Delta \Phi) \) is a symmetric probability density function then \( A_{M-1} = A_0 \) where the overbar denotes averaging over \( P(\Delta \Phi) \). Hence, when the same average is performed on (25) and (26), we get

\[ P_t = \frac{1}{M} \sum_{m=1}^{M/2} A_m - A_{M/2} \]  

(27)

and

\[ P_b = \frac{1}{\log_2 M} \left[ \sum_{m=1}^{M/2} (w_m + w_{M-m})A_m - w_{M/2} A_{M/2} \right] \]  

(28)

Lee, in the above-cited article, showed that for \( M = 4 \) and \( M = 8 \), simple closed form expressions could be obtained for the bit error probability of ideal MDPSK. The analogous expressions for the case of corrected Doppler present are

\[ P_b = F\left( \frac{3\pi}{4} \right) - F\left( \frac{\pi}{4} \right) \quad (M = 4) \]  

(29)

and

\[ P_b = \frac{1}{2} \left[ F\left( \frac{13\pi}{8} \right) - F\left( \frac{\pi}{8} \right) \right] \quad (M = 8) \]  

(30)

where

\[ F_{\pi}(\phi, \omega) = \frac{\pm(22 \sqrt{1 - z^2}) \cos \phi - (2z^2 - 1) \sin \phi}{4\pi} \times \]  

(38)
Illustrated in FIGS. 5 and 6 are the average bit error probability performances of 4DPSK and 8DPSK as a function of bit energy-to-noise ratio, $E_b/N_0$, in dB with $K_a$ as a parameter. These numerical results were obtained from equations (29) and (30), respectively. For the purpose of comparison, the bit error probability performance for perfect Doppler compensation (i.e., $K_a = \infty$) is also illustrated in these figures. It should be observed that to achieve about 0.5 dB requires a value of $K$ on the order of eighty (80). This seemingly high value of $K$ comes from the fact that the Doppler estimator must be multiplied by 2 (see the multiplier 96 in FIGS. 3 and 4) before using that estimate to remove the Doppler from the data detection signals. This, unfortunately, increases the variance of the estimator by a factor of 4 (see Eq. (20)). The requirement for multiplying the Doppler estimate by a factor of 2 is tied to the fact that within a given symbol interval, the change in phase over half a symbol interval is independent of the data symbol itself and contains only the Doppler induced phase shift, thus allowing its estimation.

PRACTICAL CONSIDERATIONS

In bandlimited applications, the rectangular baseband pulses assumed thus far should be shaped to limit adjacent channel interference (ACI) and control intersymbol interference (ISI). Since $\theta(t)$ is no longer constant over a symbol time, then in order to still achieve $x_{ck}$ and $x_{dk}$ of the form in (6), i.e., I and Q Doppler estimation samples which are independent of the data modulation, we must impose a requirement on the pulse shaping such that

$$\hat{\theta}(k+1)T_s - \hat{\theta}(k+1/2)T_s = 0$$

(39)

where $\theta(t)$ is the filtered version of $\theta(t)$. Equivalently, we require a pulse shaping such that

$$\sin \hat{\theta}(k+1)T_s = \sin \hat{\theta}(k+1/2)T_s$$

$$\cos \hat{\theta}(k+1)T_s = \cos \hat{\theta}(k+1/2)T_s$$

(40)

Assuming that one chooses a pulse shaping to satisfy (40), then, in general, the samples taken at $t=(k+1/2)T_s$ (i.e., the midpoints of the symbol interval for data detection purposes) will be subject to ISI. Thus, the question may arise: Can we choose a pulse shaping to satisfy (39) yet still allow data detection without any penalty due to ISI? This question has a fortuitous answer which depends on applying ingenuity which relates to additional features of the invention.

We shall show by reference to equations depicted and described relative to FIG. 11 (described in additional detail in Appendix A hereinafter) that we can obtain the characteristics of a 100% raised cosine system by splitting this overall transmission characteristic into a 100% root raised cosine filter 1110 followed by two samples at $T_s/4$ (delayed by 1135) and $3T_s/4$ supplied to a summer 1130 yields an equivalent operation to a 100% root raised cosine filter.

Suppose that we consider an overall (transmitter plus receiver) pulse shaping of the 100% excess bandwidth root raised cosine type. Such pulse shaping is known and has a transfer characteristic of the form

$$P(\omega) = P_r(\omega)P_T(\omega) = \left\{ \begin{array}{ll} \sin \omega T_s/4; & |\omega| \leq 2\pi/T_s \\ 0; & \text{otherwise} \end{array} \right.$$ 

(41)

with a corresponding impulse response of

$$p(t) = \frac{\cos 2\pi T_s}{1 - \left(\frac{2\pi}{T_s}\right)^2}$$

(42)

Since the zero crossings of $p(t)$ occur at $t = \pm(2k+1)T_s/4; k=1, 2, 3, \ldots$, then if $p(t)$ were used as the underlying pulse shape in a digital pulse train of the form $m(t)=\sum a_n(t-nT)$ with $\{a_n\}$ representing a multilevel data sequence, then $m(t)$ taken at $t=(n+1/4)T_s$ and $(n+3/4)T_s$ would be ISI free and equal in amplitude. Thus, using such an $m(t)$ for the I and Q data signals $\sin \theta(t)$ and $\cos \theta(t)$ allows (40) to be satisfied.

The question remains as to where to sample and how to process the I and Q data signals for data detection purposes. Since, as pointed above, samples of $\sin \theta(t)$ and $\cos \theta(t)$ taken at $t=(n+1/4)T_s$ and $(n+3/4)T_s$ are ISI free, then it seems reasonable to sum these two samples prior to differential detection. Since $p(t)$ has value unity at these sample points, i.e., the same value as a unit rectangular pulse, then compared to a single sample of this rectangular (ISI free) pulse, the above sum yields a 6 dB increase in power of the pre-detection sample in the absence of noise.

Ordinarily from the standpoint of maximizing the SNR of a single sample of I or Q, one would split the total pulse shaping $P(\omega)$ equally between the transmit and receive filters with the filters at each location taking the form of a square root raised cosine characteristic. Note that from an overall system standpoint the filters multiply and thus yield an equivalent raised cosine system response. Such a split would result in the noise samples at $t=(n+1/4)T_s$ and $(n+3/4)T_s$ being correlated. Suppose instead that half of the filtering (square root raised cosine) is placed at the transmitter and a root raised cosine filter.

The samples are taken one-half symbol times. The ideal low pass filter 1110 followed by two samples at $T_s/4$ (delayed by 1135) and $3T_s/4$ supplied to a summer 1130 yields an equivalent operation to a 100% root raised cosine filter. This results in a 3 dB penalty in the noise power of a single sample. However, it can be shown that noise samples separated by $T_s/2$ are now uncorrelated and hence the increase in noise power produced by summing the above two samples to produce a single pre-detection sample is 3 dB. Finally then, for the root
the pre-detection sample formed from the sum of samples taken at \( t = (n + 1/4)T_s \) and \( (n + 3/4)T_s \) is identical to that of a single sample of a rectangular pulse and thus there is no ISI penalty.

In fact, the above-noted result, which appears rather surprising at first, can be supported quite easily by showing that, in the absence of Doppler, the split filtering with an ideal low pass filter at the receiver is indeed the optimum filter split for Nyquist-type signaling with two nonzero uniform sample points (as is proved by Appendix A). In this context, the above scheme bears resemblance to the duobinary approach discussed earlier but is entirely binary and does not depend upon the three level complexity of the duobinary approach.

When Doppler is present, \( G(\omega) \) of FIG. 12 is replaced by

\[
G(\omega - \omega_0) = 1 + \exp \left\{ -j\omega_0 - \Delta \omega T_s/2 \right\}
\]

\[
= 1 + a \exp \left\{ -j\omega_0 T_s/2 \right\}
\]

thus requiring knowledge of the Doppler frequency shift. What is used is the Doppler estimate \( \Delta \omega \) for \( \Delta \omega \) in the implementation of equation (42) analogous to the use of \( \Delta \omega \) instead of \( \Delta \omega \) for the final Doppler removal. If the matched filter follows the differential detector, the circuit operation is once again \( G(\omega) \) of FIG. 12. It should be noted that one could indeed also use a differential detector followed by the \( G(\omega) \) combination for the data detection portion of the receiver in an effort to produce some commonality in structure. However, this later approach is suboptimum and leads to a degradation in output SNR. Also, in this case, the probability density function of the differential detector output (which is not Gaussian) is changed by the matched filter and hence, the bit error probability performance expressions of equations (24) through (28) are no longer valid.

One additional point, alluded to above, needs to be discussed. Since in the ACQ mode, the differential detector operates over a \( T_s \) sec. interval, the output of the digital filter will proportional to \( \exp \left\{ -j\Delta \omega T_s/2 \right\} \) rather than \( \exp \left\{ -j\Delta \omega T_s/2 \right\} \) as in the tracking (TRK) mode where the differential detector operates over \( T_s/2 \) sec. Since implementation of the matched filter for data detection requires the complex quantity \( a \) of equation (42), then in the ACQ mode, one must take the square root of the digital filter output in order to make this quantity available. On the other hand, in the TRK mode, \( a \) is directly available, then to achieve the quantity \( \exp \left\{ -j\Delta \omega T_s/2 \right\} \) which is needed for Doppler removal, we square the digital filter output.

FIG. 7, described below, relates to differential detection with Doppler estimation. This FIG. 7 includes, in a practical implementation, the theory discussed above concerning data detection and Doppler correction. A review of equation 43 shows that if the Doppler amount is zero then the matched filter of FIG. 7 is in accordance with FIG. 12.

In FIG. 7, the box legend "C" represents a conjugate operation. Pseudo-matched filter 710 includes brickwall filter 740, multiplier 750, a half symbol delay 760 and a summer 770. Multiplier 750 requires two signals, (1) a filtered output signal from brickwall filter 740 and (2) an input signal in the form of the complex quantity \( a = \exp(j \Delta \omega T_s/2) \). The complex quantity \( \exp(j \Delta \omega T_s/2) \) is obtained by Doppler estimator 790, is required for Doppler removal in the pseudo-matched filter prior to differential detection in detector 720. In the absence of Doppler, \( a = 1 \) and the pseudo-matched filter becomes a true matched filter, i.e., a root raised cosine filter. Furthermore, the bandwidth of the brickwall filter is narrowed to the symbol rate. Differential detector 720 of FIG. 7 operates in the manner described previously and requires no further discussion.

A practical implementation of the receiver of FIG. 7 requires an understanding and use of acquisition ("ACQ") and tracking ("TRK") modes which will be discussed in connection with additional figures such as FIGS. 7A through 7C. If \( \Delta \omega = \Delta \omega = 0 \) and the bandwidth of the brickwall filter 740, FIG. 7, is equal to \( 1/T_s \) then filter 740 is exactly a matched filter for a root raised cosine filter with 100% excess bandwidth at the transmitter. If \( \Delta \omega \) is not equal to zero then filter 740 represents a pseudo-matched filter (e.g., an approximately matched filter). The bandwidth of brickwall filter 740, in the presence of Doppler frequency shift, should be equal to \( (1/T_s + \Delta \omega_{MAX}) \).

FIG. 7A presents additional details of the circuitry and operation of the Doppler estimator 790 shown simply as a block diagram in FIG. 7. A tracking mode for the Doppler estimator circuit 790 of FIG. 7A will be described. An output from the brickwall filter 740 is applied as an input signal to another differential detector 781 which operates in the manner previously described, i.e., that differential detector 781 of FIG. 7A differentially detects two samples within each full symbol interval. The samples are separated by \( T_s/2 \) in order to "wipe off" the data phase, and thus result in a complex signal of Doppler induced phase \( T_s/2 \). The sampled output from detector 781 is applied to a digital low pass filter 782. This digital low pass filter is comparable to those described earlier in connection with FIG. 3 and is employed to improve the estimate of the Doppler by removing the effect of noise on \( \Delta \omega T_s/2 \). An amplitude normalizer 783 normalizes the envelope of digital filter output 782 to unity. This normalized output from 783 is provided both to the pseudo-matched filter 710, FIG. 7, and to a squaring circuit 784. A squared output from 784 is applied to the residual Doppler corrector 730, FIG. 7. As previously explained, the residual Doppler is removed at the output of 730 and the output signal on lead 731 contains only the differential detected phase data which is recovered in standard operation by differential detector 720, FIG. 7.

In many applications, Doppler estimation, such as that achieved by estimator 790, first takes place via a coarse Doppler estimate which is obtained in the presence of a known data sequence, e.g., all zeros. FIG. 7B depicts a fast acquisition circuit that is used when the data sequence is known to the receiver. Such preordained knowledge at the receiver is accomplished by having the transmitter send a constant phase for a short time in the order of about twenty symbols as a preamble to every data packet. Such a preamble results in a fast acquisition of the Doppler estimate. After that fast acquisition the receiver can switch to the tracking mode for open loop Doppler estimation where the data phase is not known by the receiver.

In the above-noted acquisition (ACQ) mode, the change in \( \theta(t) \) is known (for the all zeros sequence, this change is indeed zero) and thus one would expect that the differential detector output samples could be used as inputs to a digital filter in the Doppler estimator. Unfortunately, as we have just pointed out above, the differential detector output samples are themselves functions of the Doppler estimate and thus implementing the
system in the fashion would result in a feedback or closed loop system. Since one of the goals of our invention was to maintain a feedforward (open loop) configuration, we compromise a bit by reversing the order of matched filter and differential detector operations for the Doppler estimation portion of the receiver in the ACQ mode.

Again in FIG. 7B, the brickwall filter 740 is used to noise limit and has a bandwidth that is equal to the symbol rate plus the maximum frequency shift that is to be expected. A pair of differential detectors 721 and 722, operating as previously described, include switches 727 and 728 to obtain two samples during each symbol interval. The noise in these samples at 1/4T₂ and 3/4T₂ is independent of each other but both include the same Doppler-induced phase. Accordingly, adding these samples in summer 729 enhances the signal-to-noise ratio by approximately 3 dB as explained earlier. The complex signal at the output of summer 729 includes a noisy version of the Doppler-induced phase and the version is passed through a digital filter 782 and a normalizer 783. Two output signals are developed. One output signal is the exp(∫Δf(t)dt) term and the other output signal is fed through a square root circuit 734, producing exp(∫Δf(t)dtT₂/2).

FIG. 7C is an equivalent fast acquisition circuit which employs only one detector 742, a delay and add circuit 743, (including sampler 729), digital filter 782, normalizer 783 and square root circuit 734. The operation of the circuitry of FIG. 7C is functionally equivalent, in fast acquisition, to that of FIG. 7B and thus requires no further explanation.

Only a brief correlation of the circuit components of FIG. 8 relative to the circuits of FIGS. 7, 7A, 7B or 7C need be described. FIG. 8, a simple block diagram of a dual mode receiver in complex form, is essentially the integration of the circuits shown in FIGS. 7A and 7C. In FIG. 8 some circuit elements are shared between the circuits of FIGS. 7A and 7C to reduce the complexity of the dual mode receiver of FIG. 8. Switches 860, 873 and 875 are switches to select the acquisition mode or the tracking mode. Only one mode at a time is employed. FIG. 8 components 805, 810, 820, 815 represent the pseudo-matched filter 710 of FIG. 7. Components 880, 881, 890 and 891 represent the data differential detector 720 of FIG. 7. Components 892, 893 represent the residual Doppler correction circuit 730 of FIG. 7. Components 810, 826, 850, 855, 782, 783, 784 represent the Doppler estimator in tracking mode of FIG. 7A. Components 805, 810, 840, 845, 830, 835, 836, 838, 782, 783, 734 represent the fast acquisition circuit of FIG. 7C. The operation of FIG. 8 requires no further description.

FIG. 9 is an actual baseband I-Q implementation of the Doppler-corrected differential detector analogous to FIG. 2 but including all of the above practical considerations for the bandlimited application as incorporated in the simple illustration of FIG. 8. As described earlier, the elements 31, 32, and 51 are each delays of one-half symbol interval as are elements 41, 42 and 52. The eye diagram (FIG. 10) of the brickwall filter output corresponding to sin θ(t) and cos θ(t) is illustrated for 8DPSK. Some other practical simplifications have been made in regard to the manner in which the tan⁻¹ operation is performed. In particular, the quadrature signals necessary to remove the Doppler (see FIG. 9) can be obtained by noting the following trigonometric identities.

\[ \cos \left(2 \tan^{-1} \frac{E}{A}\right) = \frac{A^2 - E^2}{A^2 + E^2} \]

\[ \sin \left(2 \tan^{-1} \frac{E}{A}\right) = \frac{2AE}{A^2 + E^2} \]  

Also, allowance has been made for the possibility of acquisition and tracking modes of operation as discussed above.

**SIMULATIONS RESULTS**

A software simulation of the Doppler frequency estimator was conducted and proved: 1) verification of the analytical results, and 2) inclusion of the effects of inter-symbol interference (ISI) due to timing jitter on the performance of the system under consideration. The simulation and analytical results were in excellent agreement as shown by FIGS. 13 and 14 included herein.

**APPENDIX A**

On the Optimal Filter Split for Nyquist-Type Signaling with Two Nonzero Sample points

In the foregoing, it was shown that 100% excess bandwidth root raised cosine signaling could be used to satisfy the Doppler estimation requirement of equal, zero ISI sampling points spaced T₂/2 apart and symmetrically placed with respect to the midpoint of the symbol interval. From the standpoint of optimum data detection, the conventional Nyquist approach is to use an equivalent flat channel (i.e., the sum of all translates of the overall frequency response should be a constant over the Nyquist bandwidth) and comparison of a single sample to a threshold. Raised cosine signaling achieves such an equivalent Nyquist channel with a single ISI-free sampling point. Thus, to use this technique in combination with the above Doppler estimation circuit, one would require an additional root raised cosine filter at the receiver. (Actually, for the baseband implementation, two such additional filters would be required; one for the I channel and one for the Q channel).

To avoid the necessity of this additional root raised cosine filter, the overall channel response remains root raised cosine, as required for the Doppler estimate, and for data detection the pair of samples taken per symbol are algebraically summed and used as a single sample for comparison with a threshold. Using the above filter split (i.e., all of the filtering at the transmitter and none at the receiver), it was shown that the error probability performance achieved was indeed equal to that using the conventional Nyquist approach (i.e., raised cosine signaling with single sample per symbol detection) as described above.

In this appendix, by interpreting the summation of two T₂/2-spaced samples as a simple digital filter, we show that this choice of filter split is indeed optimum in accordance with the traditional notion of putting equal amounts of the overall channel response in the transmitter and receiver.

**SYSTEM MODEL**

Consider the mathematical model of the system illustrated in FIG. 11. Actually, this system represents only the in-phase (I) or quadrature channel (Q) of the base-
For our purpose here, it is sufficient to consider only either the I or Q channel. The data source represents either the I or Q component of the MPSK modulation and as such produces M/2-level symbols at a rate 1/Ts. These symbols are transmitted (we consider only the baseband portion of the overall system) as a pulse sequence m(T) with pulse shape pT(t) equal to the inverse Fourier transform of the transmit filter P(f). At the receiver the noise corrupted sequence x(t) = m(t) + n(t) is passed through the receiver filter P(f) to produce y(t) = m(t) + n(t). As discussed above, it is desired that m(t) be a pulse sequence with a pulse shape p(t) such that samples of m(t) taken at t = (n+1/4)Ts and t = (n+3/4)Ts result in zero ISI. To achieve this, we have selected P(f) and p(t) as in equations (41) and (42), respectively.

The receive filter output y(t) is sampled twice per symbol as shown in FIG. 11 and the sum of these two samples is inputted to the differential detector. From the standpoint of analysis, the two samplers (A, B, 1110 and B, 1125) and the T1/2 delay 1135 can be replaced by a simple digital filter G(f) 1200 and a single sampler 1215 as shown in FIG. 12. Thus, by analogy with a true Nyquist channel, wherein a single sample of the output is used for purposes of data detection, we can regard samples of x(t) (see FIG. 12) as the pre-detection variables and the cascade of P(f) and the digital filter G(f) as the total receive filter.

It is well known that for additive white Gaussian noise (AWGN) channels with no ISI, i.e., Nyquist pulse shaping, the average symbol error probability is minimized (with respect to the noise) by selecting identical transmit and receive filters each of whose transfer function has a magnitude response equal to the square root of the overall transmission characteristic. Thus, in terms of the equivalent representation of FIG. 11, optimum performance would be achieved by selecting

$$|P(f)|^2 = |P(G(f))|^2G(f)|^2 = |P(f)|^2|P(G(f))|^2 \quad (A-1)$$

However, the magnitude squared transfer function of the digital filter G(f) is given by

$$|G(f)|^2 = (2 \cos \omega Ts/2)^2 \text{ for all } \omega \quad (A-2)$$

Then using (41) and (A-2) in (A-1) gives the desired result, namely,

$$|P(G(f))| = \begin{cases} \sqrt{\frac{T_s}{2}} & \text{if } |\omega| \leq 2\pi/Ts \\ 0 \text{ otherwise} \end{cases} \quad (A-3)$$

$$|P(f)| = \begin{cases} \sqrt{2} Ts \cos \omega Ts/4 \text{ if } |\omega| \leq 2\pi/Ts \\ 0 \text{ otherwise} \end{cases} \quad (A-4)$$

The above description presents the best mode contemplated in carrying out our invention. Our invention is, however, susceptible to modifications and alternate constructions from the embodiments shown in the drawings and described above. Consequently, it is not the intention to limit the invention to the particular embodiments disclosed. On the contrary, the invention is intended and shall cover all modifications, sizes and alternate constructions falling within the spirit and scope of the invention, as expressed in the appended claims when read in light of the description and drawings.

What is claimed is:

1. A receiver in a communication system which produces a Doppler effect in an incoming multiple differential phase-shift-keyed signal having a given symbol interval, with the receiver for the incoming signal having a differential detector for restoring the data by multiplying the incoming signal with a full symbol interval delayed version of itself (or an equivalent sampled version thereof) and applying the restored signal to the detector's output, said receiver comprising:

   means for delaying the incoming data signal (which has the Doppler effect therein) by a half symbol interval;

   means in an open-loop forward acting circuit for estimating the amount of the Doppler effect from the incoming data signal and the half-symbol delayed signal; and

   means for subtracting the estimated Doppler amount from the detector's output.

2. A communication system having a transmitter and the receiver of claim 1 separated by a bandlimited medium over which the transmitter sends to the receiver, a data modulated multiple differential phase-shift-keyed signal having a predetermined symbol interval, Ts contaminated by said Doppler effect, said system comprising:

   a detector in the detector's output to yield an impulse response at the receiver in the form of

   $$p(t) = \frac{4}{\pi} \left[ \frac{\cos \frac{2\pi}{T_s} t}{1 - \left(\frac{4\pi}{T_s}\right)^2 t^2} \right]$$

   where t is time and Ts is the symbol duration.

3. A communication system in accordance with claim 2 wherein said differential detector for decoding the data represented in the received signal has the Doppler effect in the detector's output, said system further comprising:

   means in the detector including said receiver's signal shaping for sampling the received signal at times t = (n+1/4)Ts and t = (n+3/4)Ts to obtain intersymbol interference-free ("ISI") data signal samples, wherein "n" corresponds to the nth symbol interval of duration Ts and n is any arbitrarily selected integer.

4. A communication system in accordance with claim 3 wherein said means for removing the Doppler effect from the differential detector output, further comprises:

   means for multiplying said ISI samples with the Doppler estimating amount from said subtracting means as a Doppler-correcting error to cancel the Doppler effect from the differential detector's output.

5. A communication system in accordance with claim 1 wherein the Doppler effect introduces a Doppler-induced phase change that tends to mask differential phase changes encoded in said incoming data signal, and said system includes a receiver which is further characterized in that:

   said Doppler effect estimating means is responsive to the change in phase of said incoming signal over a full symbol interval, which incoming signal con-
tains both the differentially encoded data and the Doppler-induced phase change, whereas the same phase change over half a symbol interval contains only the Doppler-induced phase change.

6. A communication system in accordance with claim 2 wherein said signal shaping is provided by transmit and receive filters with the transmitter filter taking the form of a square root raised cosine characteristic, the improvement in said system comprising:

a filter at the receiver having a characteristic matched to the transmitter's filter and also exhibiting a square root raised cosine characteristic that is formed from an ideal low pass filter with its output connected in tandem to a summer, which summer receives the low pass filter's output, and a half symbol delay circuit for delaying the incoming data signal; and

b means for sampling the received signal at times 

t=(n+1/4) T,s and (n+3/4) T,s

where t is time and T,s is the symbol duration,

means for multiplying the sampled signals from said sampling means with said detector's output.

7. A communication system in accordance with claim 6 wherein the symbol interval is expressed as T,s and the transmitter's filter has 100% excess bandwidth and further wherein said receiver's ideal low pass filter has a bandwidth equal to 1/T,s expressed in Hertz.

8. A communication system in accordance with claim 6 wherein the symbol interval is expressed as T,s and wherein the transmitted signal experiences a maximum Doppler frequency shift ("f_d,MAX") and further wherein said receiver's ideal low pass filter has a bandwidth of 1/T,s (in Hz) plus f_d,MAX.

9. A communication system in accordance with claim 8 wherein the low pass filter's output is connected to a Doppler correcting circuit, and wherein said system is further characterized:

a means in an open loop forward acting circuit which receives the low pass filter's output, for estimating said Doppler-correcting error signal, which error signal is indicative of the amount of Doppler correction that is required at the detector's output.

10. A communication system in accordance with claim 9 wherein the data at the transmitter is encoded as multiple differential phase-shift-keyed changes on a transmitted carrier as an output signal shaping means split between the transmitter and the receiver to yield an impulse response at the receiver in the form of

\[ r(t) = \frac{4}{\pi} \frac{\cos 2\pi \frac{t}{T_s}}{1 - \left(\frac{2t}{T_s}\right)^2} \]

where t is time and T,s is the symbol duration, an ideal low pass filter in said signal shaping means for filtering the incoming signal, means in said receiver for sampling the received and filtered incoming signal at times t=(k+1/4) T,s where k is equal to 1, 2, 3 etc. and (k+3/4) T,s to obtain Doppler error samples, and an open loop feedforward circuit in said receiver connected to said sampling means for removing the Doppler effect from the incoming signal in accordance with said Doppler error samples from said sampling means.

14. A system in accordance with claim 13 wherein the open loop forward circuit further comprises:

a modulator at the system's transmitter for impressing information as differential phase-shift-keyed changes on a transmitted carrier as an output signal;

a Doppler-inducing communication medium that causes a Doppler phase error to become embedded in the transmitted signal;

a system receiver operative on the incoming signal for both removing the Doppler phase error and restoring the information from the differential phases in said incoming signal, said receiver including:

a demodulator connected to receive the incoming signal and restore therefrom the transmitted information which still has the Doppler phase error in the restored signal;

a sensing circuit receiving the incoming signal and responsive to a particular portion of each symbol interval of the incoming signal having only the Doppler-phase therein for deriving a Doppler error correction signal; and

a forward acting open loop correction circuit connected to said sensing circuit for removing the Doppler phase error from said restored signal so
that information only is available at the receiver's output terminal.

16. An improved communication system for communicating information, encoded by differential phase changes in symbol intervals, over a communication link which introduces a Doppler-induced phase change that tends to mask the encoded differential phase changes, said system improvement being based upon the recognition that, at a receiver, the change in phase of a received incoming signal over a full symbol interval contains both the differentially encoded data and the Doppler-induced phase change, whereas the same phase change over half a symbol interval contains only the Doppler-induced phase change, and said system improvement comprising at the system's receiver:

an input signal receiving terminal;

means connected to said terminal and operative over a half symbol interval in response to said input signal for deriving therefrom a Doppler estimate signal that is indicative of the Doppler-induced phase change which is present in the incoming signal;

means connected to said input terminal and operative over a full symbol interval in response to said input signal for deriving therefrom a detected output signal which is indicative of the data encoded phase change and including the Doppler-induced phase change as an undesired part of the detected output signal; and

means applying the Doppler estimate to the detected output signal for canceling the undesired Doppler-induced phase change from the detected output signal.

17. A communication system for symbol interval data in an environment with Doppler frequency shift and having a signal transmitter and a signal receiver connected together via a communication medium, and having transmit and receive filters with the transmitter filter taking the form of a square root raised cosine characteristic, the improvement in said system comprising:

a filter at the receiver having a characteristic pseudo matched to the transmitter's filter and also exhibiting a square root raised cosine characteristic only in absence of Doppler shift, said filter being formed from an ideal low pass filter with its output connected in tandem to a summer, which summer receives the low pass filter's output, and a half symbol delayed version of that filter's output, which half symbol delayed version is corrected by said Doppler estimate.

18. An improved communication system of claim 17 and further comprising:

means at the transmitter for transmitting, as part of a data signal, a known sequence of phase shifts; and means at the receiver responsive to that known sequence for acquiring a Doppler error signal during the time of receipt of that known sequence.

19. An improved communication system of claim 18 and further comprising:

means at the receiver responsive to said Doppler error acquiring means for condition achieving Doppler correction during the receipt of unknown data phase shifts by said receiver.

20. A communication system in accordance with claim 17 wherein the symbol interval is expressed as \( T_s \) and the transmitter's filter has 100% excess bandwidth and further wherein said receiver's:

ideal low pass filter has a bandwidth equal to \( 1/T_s \) (expressed in \( \text{Hz} \)).

21. A communication system in accordance with claim 17 wherein the symbol interval is expressed as \( T_s \) and wherein the transmitted signal experiences a maximum Doppler frequency shift ("\( f_{\text{DMAX}} \")) and further wherein said receiver's:

ideal low pass filter has a bandwidth of \( 1T_s \) (in \( \text{Hz} \) per second) plus \( f_{\text{DMAX}} \).

22. A communication system in accordance with claim 21 wherein the low pass filter's output is connected to a Doppler correcting circuit, and further wherein said Doppler correcting circuit is characterized by:

means in an open loop forward acting circuit which receives the low pass filter's output for estimating therefrom, a Doppler error signal, which error signal is indicative of the amount of Doppler correction that is required;

data detector for restoring data at the receiver and making the restored data available at the detector's output; and

means for removing the Doppler from the restored data of said detector by applying said Doppler error signal from said estimating means to the detector's output.

23. A communication system in accordance with claim 22 wherein the data is encoded as multiple differential phase changes in said symbol intervals, and further wherein:

said data detector is a multiple differential phase decoder for restoring the data in a data output signal that is formed by multiplying, at a multiplier circuit, the incoming data signal with a full symbol interval delayed version of itself.

24. A communication system in accordance with claim 23 wherein the Doppler correcting circuit further comprises:

means for delaying the incoming data signal by a delay time of one-half of the symbol interval's means for multiplying that half delayed signal with the incoming data signal; and

means for further processing the multiplied signal from said multiplying means before said signal is applied to said detector's output as a Doppler error correction signal.

25. The communication system of claim 23 wherein the processing means further comprises:

means for sampling the multiplied signal at times \( t=(n+1/4)T_s \) and \( (n+3/4)T_s \), where "\( n \)" corresponds to the \( n \)th symbol interval of duration \( T_s \) and \( n \) is any arbitrarily selected integer; and

means for multiplying the sampled signals from said sampling means with said detector's output.

26. A receiver in a communication system which produces a Doppler effect in an incoming multiple differential phase-shift-keyed signal having a given symbol interval, with the receiver for the incoming signal having a differential detector for restoring the data by multiplying the incoming signal with a full symbol interval delayed version of itself (or an equivalent sampled version thereof), said receiver comprising:

means for delaying the incoming data signal (which has an unwanted Doppler effect therein) by a half symbol interval; and
an open-loop forward acting circuit means connected to said delaying means for deriving an estimated amount of the Doppler effect from the signal to be restored, the estimated amount then being used to cancel the unwanted Doppler.

27. A communication system having a transmitter and the receiver separated by a bandlimited medium over which the transmitter sends to the receiver, a data modulated multiple differential phase-shift-keyed signal or a multiple phase-shifted-keyed signal, with each signal having a predetermined symbol interval, Tₚ, contaminated by an unwanted Doppler effect that may be removed by samples, a half symbol-interval apart, from the incoming signal, said system comprising:

signal shaping split between the transmitter and the receiver to yield an impulse response at the receiver, where said signal shaping is connected, with said impulse being in the form of

$$p(t) = \frac{4}{\pi} \frac{\cos 2\pi \frac{t}{T_s}}{1 - \left(\frac{4t}{T_s}\right)^2}$$

where t is time and Tₛ is the symbol duration, and said signal shaping yields samples for Doppler removal that are a half symbol duration apart; and

means connected to said signal shaping means and responsive to said samples yielded thereby for correcting for said unwanted Doppler effect.

28. The communication system of claim 27 wherein the Doppler error is removed by samples taken a half symbol duration apart for Doppler correction, said system comprising:

means for sampling the multiplied signal at times t = (n + 1/4)Tₛ and (n + 3/4)Tₛ, where n corresponds to the nth symbol interval of duration Tₛ and n is any arbitrarily selected integer, in order to obtain two independent samples; and

Doppler correcting means responsive to said independent samples for correcting for said unwanted Doppler.

29. A receiver in a communication system which produces a Doppler effect in an incoming multiple phase-shift-keyed signal having a given symbol interval, with the receiver for the incoming signal having a matched filter detector for restoring the data after Doppler removal, said receiver comprising:

means for delaying the incoming data signal (which has the Doppler effect therein) by a half symbol interval; and

an open-loop forward acting circuit means connected to said delaying means for estimating the amount of the Doppler effect from the signal to be restored, the estimate of which is then used to cancel the Doppler.

30. A communication system for symbol interval data having a signal transmitter and a signal receiver connected together via a communication medium, and having transmit and receive filters with the transmitter filter taking the form of a square root raised cosine characteristic, the improvement in said system comprising:

a filter at the receiver having a characteristic matched to the transmitter's filter and also exhibiting a square root raised cosine characteristic that is formed from an ideal low pass filter with its output connected in tandem to a summer, which summer receives the low pass filter's output, and a half symbol delayed version of that filter's output, means at the transmitter for transmitting, as part of a data signal, a known sequence of phase shifts; and

means at the receiver responsive to that known sequence for acquiring ("ACQ") a Doppler error signal during the time of receipt of that known sequence.

31. An improved communication system of claim 30 and further comprising:

means at the receiver responsive to the acquisition of the Doppler correction by said Doppler error acquiring means for assuming a tracking condition wherein the Doppler correction takes place during the receipt of unknown data phase shifts by said receiver.