The attached documents are being provided to Switching Power Magazine for information purposes. This magazine is writing a feature article on the International Space Station Electrical Power System, focusing on the switching power processors. These units include the DC-DC Converter Unit (DDCU), the Bi-directional Charge/Discharge Unit (BCDU) and the Sequential Shunt Unit (SSU).

These diagrams are high-level schematics/block diagrams depicting the overall functionality of each unit.

6920/Gregory V. Schmitz, Acting EPS SSM Team Lead

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Simplified Block Diagram (Strawman)
d November 20, 2000

8-String Module (1 of 10)

- Ramp Generator
- Ramp Generator (1 of 2)

RBC 650 uFd
Fail Safe Bus

FailSafe +16.0 VDC +15.0 VDC

(3 FETs)