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Walter Santiago
Glenn Research Center, Cleveland, Ohio
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Walter Santiago
National Aeronautics and Space Administration
Glenn Research Center
Cleveland, Ohio 44135

NASA Glenn Research Center (GRC) has been involved in the research and development of high speed flywheel systems for small satellite energy storage and attitude control applications. One research and development area has been the minimization of the switching noise produced by the pulsed width modulated (PWM) inverter that drives the flywheel permanent magnet motor/generator (PM M/G). This noise can interfere with the flywheel M/G hardware and the system avionics hampering the full speed performance of the flywheel system. One way to attenuate the inverter switching noise is by placing an AC filter at the three phase output terminals of the inverter with the filter neutral point connected to the DC link (DC bus) mid-point capacitors. The main benefit of using an AC filter in this fashion is the significant reduction of the inverter’s high dv/dt switching and its harmonics components. Additionally, common mode (CM) and differential mode (DM) voltages caused by the inverter’s high dv/dt switching are also reduced. Several topologies of AC filters have been implemented and compared. One AC filter topology consists of a two-stage R-L-C low pass filter. The other topology consists of the same two-stage R-L-C low pass filter with a series connected trap filter (an inductor and capacitor connected in parallel). This paper presents the analysis, design and experimental results of these AC filter topologies and the comparison between the no filter case and conventional AC filter.

Nomenclature

\[ V_{cm} = \text{Common Mode Voltage, rms} \]
\[ V_{cm} \text{ (inst)} = \text{Instantaneous Common Mode Voltage} \]
\[ V_{og} = \text{Voltage across DC Link Mid-Point “o” and System Ground, rms} \]
\[ \frac{dV}{dt} = \text{change of voltage with respect of a change of time} \]
\[ C_p = \text{lumped motor/generator parasitic capacitance across the motor stator windings and flywheel main housing, } \mu\text{-F} \]
\[ C_d = \text{DC bus main capacitor, } \mu\text{-F} \]
\[ C_3, C_4 = \text{DC bus series connected capacitors, } \mu\text{-F} \]
\[ C_1, C_2 = \text{two-stage R-L-C filter capacitors, } \mu\text{-F} \]
\[ R_2 = \text{two-stage R-L-C filter damping resistor, Ohms} \]
\[ L_1, L_2 = \text{two-stage R-L-C filter inductors, } \mu\text{-H} \]
\[ f = \text{frequency, Hz} \]
\[ f_c = \text{cut-off frequency, Hz} \]
\[ N = \text{R-L-C Filter Neutral} \]
\[ P = \text{number of poles of the motor/generator rotor} \]
\[ R_m = \text{motor/generator winding resistance per phase, Ohms} \]
\[ L_m = \text{motor/generator winding inductance per phase, } \mu\text{-H} \]
\[ i_{p1}, i_{p2}, i_{p3} = \text{phase current at the motor/generator terminals, rms} \]
\[ I_p = \text{motor/generator parasitic current, rms} \]
\[ i_{121}, i_{232}, i_{313} = \text{phase current between the first and second stage of the two-stage R-L-C filter, rms} \]
\[ i_{1N}, i_{2N}, i_{3N} = \text{phase to neutral, } N, \text{ of the first stage of the R-L-C filter, rms} \]
\[ i_{1N}, i_{2N}, i_{3N} = \text{phase to neutral, } N, \text{ of the second stage of the R-L-C filter, rms} \]
\[ V_{dc} = \text{DC Bus voltage, rms} \]
\[ V_{1g}, V_{2g}, V_{3g} = \text{motor/generator phase terminal voltages with respect to the system ground, rms} \]
\[ V_{1o}, V_{2o}, V_{3o} = \text{motor/generator phase terminal voltages with respect to the DC Link Mid-Point “o”, rms} \]
\[ V_{ao}, V_{bo}, V_{co} = \text{inverter AC terminals voltages with respect to the system ground, rms} \]
\[ V_{ao}, V_{bo}, V_{co} = \text{inverter AC terminals voltages with respect to the DC Link Mid-Point “o”, rms} \]
$V'_{1g}, V'_{2g}, V'_{3g}$ = R-L-C filter output phase voltages with respect to the system ground, rms
$V'_{1o}, V'_{2o}, V'_{3o}$ = R-L-C filter output phase voltages with respect to the DC Link Mid-Point “o”, rms
$V'_{ag}, V'_{bg}, V'_{cg}$ = R-L-C filter first stage output voltage with respect to the system ground, rms
$V'_{ao}, V'_{bo}, V'_{co}$ = R-L-C filter first stage output voltage with respect to the DC Link Mid-Point “o”, rms
$i_{o1}, i_{o2}$ = neutral current of the first and second stage of the R-L-C filter, rms
$i_o$ = total neutral current, rms

I. Introduction

NASA Glenn Research Center (GRC) has been involved in the research and development of flywheel systems as an alternative for batteries (energy storage) and control momentum gyros (attitude control) in small satellites systems [1]. A flywheel system contains several components that are essential for its success. These components are the motor/generator (M/G), the magnetic bearings (MB), the flywheel rotor and the touchdown bearings (TB). The M/G system, with the help of a DC to AC inverter, manages the satellite charge, discharge and attitude control modes. The MB System keeps the flywheel rotor levitated to ensure high speed (approximately 60,000 rpm) rotor operation. The touchdown bearing system restrains the flywheel rotor when the MB is un-powered or fails. Figure 1 shows how the components are integrated in a flywheel system with feedback control loops for the M/G and MB system.

The main function of the M/G and the DC to AC inverter is to transfer energy between the satellite DC bus and the flywheel mass. Any excess electrical energy that is collected by the satellite solar array will flow from the inverter into the M/G and is converted and stored as kinetic energy by spinning up the flywheel. When the solar array is unable to supply the energy demanded by the satellite loads the flywheel will transfer back its stored energy from the flywheel into the DC power bus through the M/G and the inverter.

Figure 1. Representation of a Flywheel System

With the increase of operational speed and power ratings of solid state power switching devices, DC to AC inverters can employ high switching frequency Pulsed Width Modulation (PWM) schemes to provide better M/G drive performance, enhanced motor adjustable-speed control bandwidth and reduced ripple on the motor phase currents. Although the PWM inverters can provide the above benefits to the M/G system they also have some adverse effects on the M/G and MB system. If the inverter is coupled directly to the M/G, high values of Common Mode (CM) and Differential Mode (DM) voltage noise are injected to the system due to the high dv/dt switching of the inverter (fig. 2). These high values of CM and DM voltages can have the following effects on the on the flywheel M/G and MB system:

- Deterioration of the motor winding isolation with the potential of line to flywheel chassis failure.
- Reduced motor efficiency.
- Increased M/G eddy current and hysteresis losses.
- High leakage current from the motor windings through the flywheel main chassis due to parasitic capacitances.
- EMI on the magnetic bearing flywheel position sensors.
The first four effects are well known for any conventional motor-inverter configurations using a PWM switching scheme. However, for a flywheel system the high dv/dt switching of the inverter introduces an additional impact which could be even more harmful. This major impact is noticed in the magnetic bearing system of the flywheel. A high speed flywheel system relies on the use of active magnetic bearings (MB) with feedback control (see fig. 1). The active MB system allows high speed (60,000 RPM) flywheel operation and long life performance by replacing the use of conventional ball bearings. The use of an inverter that applies Pulsed Width Modulation (PWM) voltage to the M/G can seriously affect the performance of the magnetic bearing system through the introduction of noise on the MB eddy current position sensors. The eddy current position sensors employ a high frequency magnetic field to measure the distance or displacement of a target (the flywheel shaft). When the eddy current sensors are in close proximity to the M/G, high dv/dt signals from the inverter can provide electromagnetic interference affecting the accuracy of the measurement provided by the position sensors. False distance readings by the eddy current position sensors can lead to unnecessary control action by the MB actuators which can lead to a critical failure if it causes MB system instability. In less severe cases it will lead to increased losses in the MB system.

To reduce the impact of the CM and DM noise due to high dv/dt’s, a three phase AC filter coupled between the DC to AC inverter and the M/G can be implemented (fig. 1). There are several AC filter topologies that have been used but some of them only address CM or DM noise but not both at the same time [2], [3], and [5]. One paper address the reduction of CM and DM noise but the use of an extra half bridge switch increases the sophistication and requires an additional control scheme [4]. Rendasara et al. [6] proposed the use of an R-L-C filter in a star configuration with the filter neutral connected to the mid-point of the two series DC capacitors in a 480-V 20-hp Motor PWM drive system (fig. 3). This topology shows a great reduction of CM and DM voltages and leakage current by carefully selecting the values of R, L and C of the filter. One advantage of this filter topology is that it can be connected at the inverter AC terminals without consideration of the length of the AC cables and transmission issues that it can introduce to the M/G [7]. The simplicity of this AC filter topology and the potential performance improvement that it brings to the inverter and motor system make this a good candidate for the NASA GRC flywheel system.

Taking the figure 3 AC filter as the baseline, different approaches were tested in order to determine the best topology for the NASA GRC flywheel system. The following sections will explain the theoretical concept and
impact on the common mode (CM) and differential mode (DM) in a PWM inverter and motor system when an AC filter is not employed and when different AC filter topologies are employed. Also test data shows a comparison between the different AC filter and no AC filter cases in terms of reduction of motor stresses, losses and leakage currents without affecting the phase shift in the motor control loop. Finally, the AC filter topologies are compared on the basis of noise reduction on the position sensors of the MB system.

II. ANALYSIS

Figure 4 shows the inverter and M/G configuration used on the NASA flywheel system without an AC filter. An isolated DC power supply is connected to the inverter to simulate the satellite solar array system and 15 feet of 10 AWG twisted cable is connected between the motor/generator and the inverter. At the DC terminals of the inverter two capacitors \( C_3 \) and \( C_4 \) are connected in series, each one rated for 1,200 Vdc and with their mid-point denoted by “o”. The motor/generator is a two pole permanent magnet (PM) synchronous motor rated at 5.85 kW with an operational line to line voltage of 79.26 Vrms and a speed range of 0 to 60,000 rpm. The relationship between mechanical speed and AC electrical speed, or fundamental frequency, is given by

\[
f = \frac{P \times \text{RPM}}{120}
\]

where \( f \) is the electrical frequency in Hertz (Hz), \( P \) is the motor/generator number of poles and \( \text{RPM} \) is the rated mechanical speed in revolutions per minute. The NASA GRC flywheel M/G is a two pole machine so the range of AC electrical frequency is between 0 and 1 kHz.

\[V_{1g} - V_{cm} = R_m i_a + L_m \frac{di_a}{dt}\]

\[V_{2g} - V_{cm} = R_m i_b + L_m \frac{di_b}{dt}\]

\[V_{3g} - V_{cm} = R_m i_c + L_m \frac{di_c}{dt}\]

where \( V_{1g}, V_{2g}, \) and \( V_{3g} \) are the voltages at the motor terminals with respect to ground “g” and \( R_m \) and \( L_m \) are the per phase motor winding resistance and inductance respectively. Adding equations (2), (3) and (4) we obtain

\[V_{1g} + V_{2g} + V_{3g} - 3V_{cm} = \left( R_m + L_m \frac{d}{dt} \right) (i_a + i_b + i_c)\]
Since the M/G is a balanced system $i_a + i_b + i_c = 0$ and $I_p = 0$ therefore the common mode voltage can be expressed as

$$V_{cm} = \frac{V_{1g} + V_{2g} + V_{3g}}{3} \quad (6)$$

The motor terminal voltages with respect to ground “g” can also be expressed in terms of the DC link middle point “o” capacitors as follows

$$V_{1g} = V_{1o} + V_{og} \quad (7)$$
$$V_{2g} = V_{2o} + V_{og} \quad (8)$$
$$V_{3g} = V_{3o} + V_{og} \quad (9)$$

Substituting equations (7), (8) and (9) into Eq. (6) results in the following expression for $V_{cm}$.

$$V_{cm} = \frac{V_{1o} + V_{2o} + V_{3o} + V_{og}}{3} \quad (10)$$

In this inverter three out of its six MOSFET switches are conducting at any given time which represents eight possible switching states. Two switching states provide zero voltage to the M/G and correspond either to all three top inverter switches on or all three bottom switches on. The remaining six switching states provide voltage to the M/G and alternate between one top switch with two bottom switches on and two top switches with one bottom switch on. With this in mind the summation of voltages at the inverter AC terminals with respect to the DC link mid-point “o” can be express as follows

$$V_{ao} + V_{bo} + V_{co} = \begin{cases} 
\pm \frac{3V_{dc}}{2} & \text{all top or bottom switches are on} \\
\pm \frac{V_{dc}}{2} & \text{one top and two bottom switches on or vice versa} 
\end{cases} \quad (11)$$

As mentioned before and illustrated on figure 4 the M/G and inverter use 15 feet of 10 AWG cable between the inverter and the motor. It has been observed that a PWM inverter and motor configuration without an AC filter can experience overvoltages due to reflections at the motor terminals which can be attributed to the high voltage dv/dt’s [7]. For the NASA GRC M/G inverter configuration with a 125 V DC bus and no AC filter, the dv/dt can be as high as 1,126 V/µs (voltage rise of 107 Volts and rise time of .095 µs). Therefore the pulsed voltage at the motor terminal ($V_{1o}$, $V_{2o}$ and $V_{3o}$) can be approximately twice the amount of voltage seen at the inverter terminals. Consequently by using equation (11) the summation of the M/G terminals voltage with respect to the DC link mid-point “o” can be express as follows

$$V_{1o} + V_{2o} + V_{3o} = \begin{cases} 
\pm 3V_{dc} & \text{all top or bottom switches are on} \\
\pm V_{dc} & \text{one top and two bottom switches on or vice versa} 
\end{cases} \quad (12)$$

Combining equation (12) into equation (10) the instantaneous common mode voltage can be express as
\[ V_{cm\ \text{inst}} = \begin{cases} \pm V_{dc} + V_{og} & \text{all top or bottom switches are on} \\ \pm \frac{V_{dc}}{3} + V_{og} & \text{one top and two bottom switches on or vice versa} \end{cases} \] (13)

Equation (13) expresses the common mode voltage applied to the motor in the absence of an AC filter. Notice that this common mode voltage is expressed as a function of the DC bus voltage \( V_{dc} \), and the voltage across DC link mid-point “o” and ground \( V_{og} \). \( V_{cm} \), the voltage across the parasitic capacitance located between the M/G windings and flywheel structure, creates noise on the MB position sensors and causes M/G winding stress due to the high dv/dt. Therefore, the lower \( V_{cm} \) becomes the less the noise and stress there is.

B. Voltage between DC Link Mid-Point “o” to Ground (g) - \( V_{og} \)

\( V_{og} \) plays a very important role in the reduction of noise due to the common mode voltage effect. Based on equation (13), \( V_{cm} \) is a function of the DC bus voltage \( V_{dc} \) and the voltage between the DC Link Mod-Point “o” and the system ground \( V_{og} \). The larger the value of \( V_{og} \), the more noise the flywheel system will have. In figure 5(a) when the inverter is off, \( V_{og} \) shows a near sinusoidal waveform. The presence of this 60 Hz sinusoidal waveform at this system set up is because of the isolation of the DC power supply with respect to the building three phase AC input. Otherwise \( V_{og} \) will be a triangular waveform as seen in [6] and figure 3. If \( V_{og} \) were always a sinusoidal waveform CM noise will be much less because of its low harmonic content. Therefore, to guarantee a low common mode system noise \( V_{og} \) should be as sinusoidal as possible in order to reduce the dv/dt’s effect on the system. Figure 5(b) shows \( V_{og} \) with the inverter turned on with a star configured three phase AC filter. The \( V_{og} \) for this filter configuration shows a high dv/dt superimposed on the 60Hz sinusoidal waveform. In contrast, the same filter with the neutral coupled to the DC link mid-point “o” is shown in figure, 5(c). Figure 5(d) has the same filter but with an L-C trap connected in series. These two last configurations show the benefits of the AC filter with \( V_{og} \) nearly a 60 Hz sinusoidal waveform which results in low dv/dt and low common mode noise \( (V_{cm}) \).

Figure 5. Voltage between DC Link Mid-Point “o” to Ground (g) versus Time (Vog).
(a) Inverter Off. (b) Inverter ON with10 kHz R-L-C Filter with Neutral NOT tied to “o”.
(c) Inverter ON with10 kHz R-L-C Filter with Neutral tied to “o”.
(d) Inverter ON with 10 kHz R-L-C Filter and 65 kHz Trap with Neutral tied to “o”.

C. Common Mode Voltage with an AC Filter and NO Neutral coupled to the DC Link Mid-Point “o”

Figure 6 shows an L-R-C filter connected between the flywheel M/G and the inverter with no connection between the filter neutral “N” and the DC link mid-point “o”. Most industries use this conventional configuration
which suppresses the high line to line dv/dt voltage (differential mode). The reduction of differential mode dv/dt helps to alleviate the stresses on the motor windings and the effect of voltage reflection along the three phase line. With the proper design approach and knowledge of the system configuration the filter parameters, $L_1$, $C_1$, and $R_1$ can be selected such that the filter attenuates all the high frequency switching and provides a differential mode output of nearly sinusoidal line to line voltages at the desired fundamental frequency (between 0 to 1 kHz for this case).

![Inverter and Motor/Generator Configuration with an R-L-C Filter](image)

Figure 6. Inverter and Motor/Generator Configuration with an R-L-C Filter

Since the $L-R-C$ filter supplies a near sinusoidal voltage to the M/G, the filter output stage voltages ($V_{1g}$, $V_{2g}$, $V_{3g}$) can be equated to the M/G terminal voltages, $V_{1g}$, $V_{2g}$, and $V_{3g}$. Therefore the common mode voltage at the motor terminals can be expressed as follows:

$$V_{cm} = \frac{V_{1g} + V_{2g} + V_{3g}}{3} = \frac{V_{1g} + V_{2g} + V_{3g}}{3}$$

The voltage relationship between the inverter terminal voltages and the output of the AC filter can be also expressed as:

$$V_{ag} = V_{1g} = L_1 \frac{di_1}{dt}$$

$$V_{bg} = V_{2g} = L_1 \frac{di_2}{dt}$$

$$V_{cg} = V_{3g} = L_1 \frac{di_3}{dt}$$

Then adding equations (15), (16) and (17) results in:

$$(V_{ag} + V_{bg} + V_{cg}) - (V'_{1g} + V'_{2g} + V'_{3g}) = L_1 \frac{d}{dt}(i_a + i_b + i_c)$$

For a balanced three phase system $(i_a + i_b + i_c) \approx 0$ and Eq. (18) becomes:

$$(V_{ag} + V_{bg} + V_{cg}) = (V'_{1g} + V'_{2g} + V'_{3g})$$

Substituting equation (19) into equation (14) $V_{cm}$ yields:

$$V_{cm} \approx \frac{V_{ag} + V_{bg} + V_{cg}}{3}$$
Each inverter terminal voltages from equation (20) \( V_{ag}, V_{bg}, \) and \( V_{cg} \) can also be expressed as the sum of its terminal voltage w.r.t. the DC link mid-point “o” plus the voltage from the mid-point “o” w.r.t. the system ground:

\[
V_{ag} = V_{ao} + V_{og} \tag{21}
\]

\[
V_{bg} = V_{bo} + V_{og} \tag{22}
\]

\[
V_{cg} = V_{co} + V_{og} \tag{23}
\]

Substituting equations (21), (22) and (23) into equations (20) yields:

\[
V_{cm} \approx \frac{V_{ao} + V_{bo} + V_{co}}{3} + V_{og} \tag{24}
\]

As explained in section II-A the inverter has three switches conducting at any given time corresponding to eight switching states. Therefore, by combining equation (11) into (24), the instantaneous common mode voltage \( V_{cm} \) for an M/G and inverter system with floating R-L-C results in:

\[
V_{cm} \text{(inst)} = \begin{cases} 
\pm \frac{V_{dc}}{2} + V_{og} & \text{all top or bottom switches are on} \\
\pm \frac{V_{dc}}{6} + V_{og} & \text{one top and two bottom switches on or vice versa}
\end{cases}
\tag{25}
\]

This equation demonstrates that even though a floating R-L-C filter can be effective in reducing differential mode voltages at the motor terminals, the common mode voltages will remain high. This is the result of the high dv/dt voltage switching and the instantaneous values of \( V_{og} \) as explained on section II-B and figure 5(b). Consequently, something further improvements are required.

**D. Common Mode Voltage with an AC Filter with Neutral coupled to the DC Link Mid-Point “o”**

Figure 7 depicts the proposed R-L-C filter topology for the flywheel M/G and inverter system. This filter configuration has two distinctive features. One is the connection of the filter neutral “N” to the DC Link Mid-Point “o”. The other feature is the application of a two stage R-L-C filter acting as a four pole low pass AC filter (fig. 8). The details of the two stage R-L-C filter will be explained in the following sections.

![Figure 7. Proposed Three Phase R-L-C Filter for the Flywheel M/G and Inverter System](image)

To express the common mode voltage of this flywheel configuration it is necessary to reference the filter voltages to the mid-point “o”. Figure 8 is a detailed schematic of the filter. The R-L-C filter first stage output
voltage w.r.t. the system ground ($V'_{ag}$, $V'_{bg}$, $V'_{cg}$) and R-L-C filter the second stage output voltage w.r.t. the system ground ($V'_{1g}$, $V'_{2g}$, $V'_{3g}$) can be described as:

\[
V'_{ag} = V'_{ao} + V_{og} \tag{26}
\]

\[
V'_{bg} = V'_{bo} + V_{og} \tag{27}
\]

\[
V'_{cg} = V'_{co} + V_{og} \tag{28}
\]

\[
V'_{1g} = V'_{1o} + V_{og} \tag{29}
\]

\[
V'_{2g} = V'_{2o} + V_{og} \tag{30}
\]

\[
V'_{3g} = V'_{3o} + V_{og} \tag{31}
\]

---

**Figure 8. Detailed Schematic of the Proposed Three Phase Two Stage R-L-C Filter (Fourth Order Low Pass Filter)**

---

Given $X = a$, $b$, and $c$, the first stage voltages with respect to the DC mid-point “o” ($V'_{ao}$, $V'_{bo}$, $V'_{co}$) can be express as follows:

\[
V'_{Xo} = \frac{1}{C_1} \int i_{XN} \, dt \tag{32}
\]

Summing all the three terminals voltages of the first stage derived from equation (32) and $i_{aN} + i_{bN} + i_{cN} = i_{a1}$ results in:

\[
V'_{ao} + V'_{bo} + V'_{co} = \frac{1}{C_1} \int (i_{aN} + i_{bN} + i_{cN}) \, dt = \frac{1}{C_1} \int i_{a1} \, dt \tag{33}
\]

Given $Y = 1$, 2, and 3, the second stage voltages with respect to the DC mid-point “o” ($V'_{1o}$, $V'_{2o}$, $V'_{3o}$) can be express as follows:
\[ V'_{1o} = i_{1N} R_2 + \frac{1}{C_1} \int i_{1N} \, dt \]  

(34)

Summing all the three terminals voltages of the second stage derived from equation (34) and \( i_{1N} + i_{2N} + i_{3N} = i_{o2} \) results in:

\[ V'_{1o} + V'_{2o} + V'_{3o} = R_2 (i_{1N} + i_{2N} + i_{3N}) + \frac{1}{C_2} \int (i_{1N} + i_{2N} + i_{3N}) \, dt = i_{o2} R_2 + \frac{1}{C_2} \int i_{o2} \, dt \]  

(35)

The voltage relationship between the stage one and stage two filter voltages can be express as:

\[
\begin{align*}
V'_{ao} - V'_{1o} &= L_2 \frac{di_{a1}}{dt} \\
V'_{bo} - V'_{2o} &= L_2 \frac{di_{b2}}{dt} \\
V'_{co} - V'_{3o} &= L_2 \frac{di_{c3}}{dt}
\end{align*}
\]  

(36, 37, 38)

Adding equations (36), (37) and (38) results in:

\[
(V'_{ao} + V'_{bo} + V'_{co}) - (V'_{1o} + V'_{2o} + V'_{3o}) = L_2 \frac{d}{dt}(i_{a1} + i_{b2} + i_{c3})
\]  

(39)

For a balanced three phase system \((i_{a1} + i_{b2} + i_{c3}) \approx 0\) and equation (39) becomes

\[
(V'_{ao} + V'_{bo} + V'_{co}) = (V'_{1o} + V'_{2o} + V'_{3o})
\]  

(40)

Now by adding equations (33) and (35), and using the relationship of equation (40) results in the following expression

\[
V'_{1o} + V'_{2o} + V'_{3o} = \frac{1}{2} \left[ R_2 i_{o2} + \frac{1}{C_1} \int i_{o1} \, dt + \frac{1}{C_2} \int i_{o2} \, dt \right]
\]  

(41)

As in the previous case the L-R-C filter supplies a nearly sinusoidal voltage to the M/G and the filter output voltages \((V'_{1g}, V'_{2g} \text{ and } V'_{3g})\) can be equated to the M/G terminal voltages \((V_{1g}, V_{2g} \text{ and } V_{3g})\). Thus equation (14), common mode voltage at the motor terminals, still applies.

Combining equations (29), (30), (31) and (41) results in the following expression for the motor terminal common mode voltage when a 2-stage filter with its neutral connected at the DC link Mid-point “o” is used.

\[
V_{cm} = \frac{1}{6} \left( R_2 i_{o2} + \frac{1}{C_1} \int i_{o1} \, dt + \frac{1}{C_2} \int i_{o2} \, dt \right) + \text{V}_{eq}
\]  

(42)

By comparing equation (42) with the instantaneous common mode voltage equations of the no R-L-C filter case, equation (13), and the floating neutral R-L-C filter case, equation (25), it can be concluded that by using the proposed non-floating two-stage R-L-C filter topology and carefully selecting the values of the \(C_1, L_1, C_2, L_2, \text{ and } R_2\) two major improvements can be made. They are:

- Decreased common mode voltage magnitude
- Reduced the high voltage \(dv/dt\)

An additional advantage of this non-floating R-L-C filter is that the common mode voltage (eq. 42) is dependent on controllable parameters. Therefore, by making the damping resistor \(R_2\) small enough and the capacitance \(C_1\) and \(C_2\) large enough the common mode voltage can be considerably reduced in addition to the \(1/6\) factor. This is in contrast to the previous cases given in equations (13) and (25) where the common mode voltage is not dependent on filter...
parameters. Furthermore, by connecting the filter neutral to the DC mid-point “o”, $V_{og}$ will become more sinusoidal (fig. 5(c) and (d)).

The $dv/dt$ reduction and the decreased common mode voltage effect given by the two-stage R-L-C filter tied to the DC mid-point “o” provides the following additional benefits for the Flywheel M/G and MB systems:

- No deterioration of the motor winding isolation.
- Increased motor efficiency.
- Reduced M/G eddy current and hysteresis losses.
- Low leakage current from the motor windings through the flywheel main chassis due to parasitic capacitances.
- Low EMI on the magnetic bearing flywheel position sensors.

E. Differential Mode Voltage Effect

The combination of the inverter’s very high voltage and short rise times enabled by fast switching MOSFET’s or IGBT’s result in high $dv/dt$ line to line voltage (differential mode noise). In industry applications these $dv/dt$ voltages at the motor terminals can reach between 7,000 V/$\mu$s and 11,000 V/$\mu$s producing motor winding stresses and voltage reflection effects [6]. The typical alternative to suppress the high line to line $dv/dt$ voltage is the application of an $L-R-C$ filter with a floating neutral (no connection between its neutral “N” and the DC link mid-point “o”, fig. 6). With proper design and the knowledge of the system configuration the filter parameters $L_1$, $C_1$, $L_2$, $C_2$ and $R_2$ (fig. 7) can be selected so that the filter attenuates all the high frequency switching and provides a differential mode output of a nearly sinusoidal line to line voltages at the desired fundamental frequency (between 0 to 1 kHz for this flywheel case).

III R-L-C Filter Design

The NASA Glenn Research Center flywheel M/G and MB set up is shown in figure 1. As described previously, a proper R-L-C filter design is crucial for the success of this flywheel system. The DC to AC inverter DM and CM noise must be minimized so that the M/G can operate with low stresses and losses and the MB position sensors can function with the minimum amount of interference which can result in false displacement readings of the flywheel shaft.

The DC to AC inverter will have a switching frequency of 65 kHz and the DC bus voltage will be 125 Vdc. The flywheel M/G system uses a two-pole PM synchronous motor capable of operating at a fundamental frequency between 0 to 1 kHz equal to spinning the flywheel from 0 to 60,000 RPM. The major design criteria for the R-L-C filter was the selection of the cut-off frequency. This cut-off frequency has to fall between the maximum fundamental frequency of the motor and the inverter’s switching frequency with good attenuation at the high frequency range and minimum attenuation between 0 to 1 kHz. Another filter design aspect to consider is the phase lag effect. Major phase lag in the fundamental operating range (0 to 1 kHz) can affect the proper operation of the flywheel M/G controls.

The values of the filter R, L’s and C’s were selected to produce a near sinusoidal line to line voltage and a small common mode voltage, equation (42).

A. Two-Stage R-L-C Filter Topology (Four-pole Low Pass Filter)

The two-stage R-L-C filter topology is shown in figures. 7 and 8. Figure 9 also shows the single phase representation of the filter with its transfer function given in equation (43).

\[
H(s) = \frac{R_2 C_2 + 1}{L_1 C_1 L_2 C_2 s^4 + L_1 C_1 R_2 C_2 s^3 + (L_1 C_1 + L_1 C_2 + L_2 C_2) s^2 + R_2 C_2 s + 1}
\]  

Figure 9. Single Phase Circuit for the Two-Stage R-L-C Filter

The cut-off frequency at each stage can be expressed with the following equation...
were $L$ and $C$ are the inductance and capacitance of the L-C-R filter for the first stage or the second stage. The cut-off frequency for this flywheel application was selected between the maximum fundamental frequency of the M/G (1 kHz) and the inverter switching frequency (65 kHz) assuring minimum phase lag impact in the operating region of 0 and 1 kHz and large high attenuation at high frequencies. Bode plots are made to confirm that the filter satisfies the requirements.

A cut-off frequency ($f_c$) of 10 kHz was selected for our flywheel M/G application. Figure 10 depicts the Bode plot of a 10 kHz two-stage R-L-C filter. This L-C-R filter Bode plot shows no attenuation or amplification with minimum phase lag impact at frequencies below or equal to 1 kHz. At frequencies above 10 kHz filter the signal attenuates at an approximate rate of 60 dB per decade. At the inverter 65 kHz switching frequency the attenuation is 47 dB. The phase plot shows a lagging effect at high frequency but no impact in the M/G operating frequency range.

The filter inductance values were deliberately kept low in the filter design to minimize the filter output impedance within the operating range of frequencies (0 to 1 kHz). A large output impedance is undesirable for two reasons. One is the phase shift would introduce a lag in the motor control loop. Second the voltage drop across the output impedance would reduce the maximum voltage available to the motor which limits the motor acceleration at high speeds [9]. Thus the filter series inductors, $L_1$ and $L_2$, were designed so their sum is below the motor inductance value. The R, L’s and C’s values for the proposed two-stage R-L-C filter can be seen on figure 10 and these are:

$$L_1 = 15 \mu\text{H}, \quad C_1 = 16.99 \mu\text{F}$$
$$L_2 = 7 \mu\text{H}, \quad C_2 = 36.19 \mu\text{F} \quad \text{and} \quad R_2 = 0.5 \Omega.$$ 

The sum of both inductors, $L_1 + L_2$, is $23 \mu\text{H}$ which is smaller than the phase M/G winding inductance ($33.5 \mu\text{H}$). The capacitance values were obtained by using equation (44) based on the $L_1$, $L_2$ values and the 10 kHz cut-off frequency. Since the values of the inductors are low, the capacitance value should be large in order to satisfy the 10 kHz cut-off frequency. The large value capacitors also provide the benefit of reducing the CM voltage as shown in equation (42) when connecting the filter neutral ($N$) to the DC-Link mid-point “o”. See also figure 7 for the system topology.

**B. Design of a Two-Stage R-L-C Filter Topology with an L-C Trap Filter**

The AC filter topology can be augmented with an L-C trap filter between the inverter and the two-stage R-L-C filter, see figure 11. The purpose of an L-C trap filter is to further reduce the switching frequency harmonic of the inverter by introducing a high impedance path [8] between the inverter and the M/G. The values of the inductor ($L_T$) and the capacitor ($C_T$) are tuned using equation (44) with the cut-off frequency, $f_c$, set to the switching frequency of the inverter MOSFETs. The bode plot (fig. 12) of the R-L-C filter low pass and L-C trap filter shows more than
twice the attenuation (more than 100 dB) at the 65kHz switching frequency compared to the two-stage R-L-C filter gain plot at the same frequency (47 dB in fig. 10).

This topology produces some amplification (maximum of 2dB) between 200 Hz and 1 kHz and some phase lag between 300 and 1 kHz (maximum lag of –1.18 degrees). Even though the trap filter introduced a gain and phase effect on the system this does not introduce a major impact to the M/G control loop system due to their small values [9]. On the other hand, the very high impedance value of the L-C trap filter at the switching frequency greatly reduces the leakage current ($I_p$) in the parasitic capacitance ($C_p$) of the flywheel M/G. In addition, the amount of switching frequency voltage between the DC link mid-point capacitor “o” and the system ground ($V_{og}$) is reduced. This provides lower common mode voltage, thus reducing the negative effects of the high dv/dt listed in the introduction section of this paper (see also fig. 5(d)).

![Figure 11. Proposed Three Phase R-L-C Filter with an L-C Trap for the Flywheel M/G and Inverter System](image)

![Figure 12. Bode Plot of a 10 kHz Two-stage R-L-C Filter and a 65 kHz L-C Trap](image)

### III Test Results

Three filter topologies and the no-filter configurations were selected to test and compare the impact of the different filter topologies on the CM and DM voltages. The four filter topologies were

- No filter
- Two-Stage 10 kHz R-L-C filter with NO connection between its neutral (N) and the DC Link Mid-Point “o” (figs. 6 and 7).
- Two-Stage 10 kHz R-L-C filter with a connection between its neutral (N) and the DC Link Mid-Point “o” (fig. 7).
- Two-Stage 10 kHz R-L-C filter plus a 65 kHz L-C trap with a connection between its neutral (N) and the DC Link Mid-Point “o” (fig. 11).
A. Differential Mode (DM) Test Results

Figure 13 shows the line to line voltage when the flywheel system is spinning at 55,000 rpm or at a fundamental frequency of 917 Hz. Clearly the effect of the two-stage R-L-C filter at the differential mode can be seen. A near sinusoidal waveform has been obtained thus no high dv/dt voltage will reach at the motor windings and the voltage stresses have been eliminated. This improvement of the differential mode holds whether a connection between the filter neutral and DC point “o” has been made or not.

It is also important to point out that if a filter is absent high dv/dt will travel through the line creating the M/G winding stresses due to the voltage doubling effect and also increasing leakage current.

B. Leakage Current Tests Results ($I_p$)

Although $V_{cm}$ cannot be measured directly to confirm equations (13), (25) and (42), it is still possible to evaluate its effect by measuring the leakage current ($I_p$) and the parasitic capacitance, $C_p$, between the motor stator windings and the flywheel main housing. $I_p$ can be easily measured under different topology conditions by using a current probe at the flywheel ground cable (see fig. 11) and $C_p$ by using an Impedance Measuring System (IMS).

![Image](image1)

Figure 14. M/G Leakage current ($I_p$) Frequency Spectrum
(a) No Filter, (b) 10 kHz R-L-C Filter with NO Neutral Tied to “o”,
(c) 10 kHz R-L-C Filter with Neutral Tied to “o” and
(d) 10 kHz R-L-C Filter plus 65 kHz L-C Trap Filter with Neutral Tied to “o”
Figure 14 shows the leakage current measurements with different filter topologies. With the no filter topology (fig. 14(a)) and in the floating R-L-C filter topology (fig. 14(b)), high frequency leakage current is present. This will produce noise propagation that the MB position sensors are vulnerable to. This noise on the position sensors is caused by high dv/dt at the common mode voltage across the parasitic capacitor resulting in leakage current to freely flowing (eq. (13) and (24)). In contrast, the case in which the two-stage 10-kHz R-L-C filter is employed with its neutral point “N” is connected to the DC link mid-point “o” (fig. 14(c)), all the high frequency leakage currents have been significantly attenuated. However, the inverter 65 kHz switching component is still present although at a lesser amplitude. In figure 14(d) when a 65 kHz L-C Trap filter is applied in addition to the 10 kHz R-L-C filter, the 65 kHz inverter switching effect is almost eliminated as a result of the high impedance path of the trap filter. This further reduces the noise interference on the flywheel system.

Figures 15 (a), (b), (c) and (d) also shows the leakage current of the different filter topologies in the frequency domain. Again, figures 15 (c) and (d) show a major leakage current reduction at the high frequency harmonics of the 65 kHz inverter but again, only when the trap filter is applied the 65 kHz frequency component is significantly reduced. In conclusion, when the filter neutral point is connected to the DC link mid-point “o” $V_{og}$ becomes more sinusoidal (less noisy). In accordance with CM voltage equation (42) and figure 5 the values of $L_1$, $C_1$, $L_2$, $C_2$ and $R_2$ can be also selected to help reduced the CM voltage.

To measure the leakage capacitance, $C_p$, between the M/G and the flywheel main housing an Impedance Measuring System (IMS) was used (fig. 16). The IMS consists of a frequency response analyzer and a computer to fully automate the frequency sweep through a given band. By connecting all the M/G Terminals in a single node and using the flywheel main chassis as the return, the IMS will perform the frequency sweep while measuring the voltage and current. The computer control software divides the voltage and current to display the impedance of the test article.

Figure 17 shows the results of the IMS measurements. The leakage capacitance and the inductance of the M/G windings start to interact with a minimum impedance point at 1.731 MHz. At this resonant point the leakage capacitance ($C_p$) is around 13.27 nano-Farads. This measured value then can be used for future system simulation.
C. Magnetic/Bearings Position sensors test results

As mentioned in the introduction, the importance of having a clean feedback signal of every eddy-current position sensors is crucial for the success of spinning the flywheel rotor up to 60,000 rpm. By applying an R-L-C filter with its neutral attached to the DC link mid-point “o” it gives a desirable solution of containing the noise right at the source rather than applying extra filtering at the receiving end of the position measurement. In the magnetic bearing section of figure 1 it can be seen that the measurements of the position signal feedback were made before a gain and offset board at the labeled “X1 Test Point”. This gives a raw measurement with no previous processing and filtering.

Figures 18 and 19 shows the effect of the different AC filter topologies on the MB position sensors, in the time and frequency domain respectively. Clearly when no filters are being used, a large amount of high frequency noise is present (figs. 18(a) and 19(a)). With this much noise on the position sensors, the MB system cannot work reliably.

When a floating R-L-C filter is applied some reduction of high frequency noise on the position sensors was noticeable (figs. 18(b) and 19(b)). Even though an improvement has been made, high frequency harmonics are still present. In this configuration, the voltage components due to the eight switching states and $V_{op}$ are still high (eq. (25) and fig. 5(b)). Again, applying an R-L-C filter with no connection to the DC mid-point “o” does not reduce noise enough to provide a very clean position signal.

The impact of connecting the R-L-C filter neutral (N) to the DC Link Mid-Point “o” can be seen clearly in figures 18(c), (d) and figures 19 (c) and (d). A great amount of high frequency noise at the position sensors has been
eliminated making it easier for the MB control system to operate. This is the result of reducing the high frequency content of $V_{og}$ through the proper selection of the R-L-C components (eq. (42) and fig. 5(c) and (d)).

Figure 18. Magnetic Bearing Position Sensor versus Time  
(a) Inverter On with No Filter, (b) Inverter On with 10 kHz R-L-C Filter with NO Neutral Tied to “o”,  
(c) Inverter On with 10 kHz R-L-C Filter with Neutral Tied to “o”  
(d) Inverter On with 10 kHz R-L-C Filter plus 65 kHz L-C Filter with Neutral Tied to “o”

Figure 19. Magnetic Bearing Position Sensor (X axis) Frequency Spectrum  
(a) Inverter On with No Filter, (b) Inverter On with 10 kHz R-L-C Filter with NO Neutral Tied to “o”,  
(c) Inverter On with 10 kHz R-L-C Filter with Neutral Tied to “o”  
(d) Inverter On with 10 kHz R-L-C Filter plus 65 kHz L-C Filter with Neutral Tied to “o”
When monitoring the position sensors of the flywheel system, several oscilloscopes set in X-Y mode are used (fig. 1). Figure 20(a) and (b) show the position sensor readings when the R-L-C filter has the neutral attached to the mid point “o” and with the neutral floating respectively. The scales of the position sensors are approximately 0.28 Volts per thousand of an inch (0.288V/mils), and the total displacement before the flywheel shaft hits the touchdown bearings is 8 mils. By looking at figure 20(b) it is clear that common mode noise can provide false displacement measurements of about 2.14 mils on the X and Y axis or almost 30 % of the total displacement. This measurement was also taken after an 8 kHz low pass filter has been. This is a clear indication that containing the high dv/dt noise at its source, the inverter in this case, is preferable than trying to filter it at the receiving end of the MB feedback signals.

![Figure 20. Magnetic Bearing Position Sensor in an X-Y scope. X and Y Scale of 0.5V/div. (a) Two-stage R-L-C Filter with Neutral Attached to the DC Link Mid-point “o” (b) Floating Two-stage R-L-C Filter](image)

**D. Motor/Generator Phase Current Test Result**

Phase current ripple that flows into the M/G windings also has an effect. The larger the ripple current the more heating at the M/G stator is being generated. The amount of ripple that is generated is influenced by the total inductive reactance of the motor windings plus the line and the R-L-C filter inductance ($X_L = j\omega L$). The filter without the trap reduces some of the high frequency motor current ripple as can be seen in figure 21(a). This is expected given the highly sinusoidal voltage that is applied to the motor after the filter (see fig. 13). But if a 65 kHz L-C trap filter is used in addition to the R-L-C filter, the total impedance at the switching frequency will increase significantly thus reducing the 65 kHz current ripple amplitude even more. The effect of the L-C trap can be easily seen in figure 21, where the frequency spectra of the motor phase current using the two filter topologies were measured. Figure 21(b) shows that the use of the L-C trap filter considerably reduces the 65 kHz switching effect on the M/G line current.

![Figure 21. M/G Line Current Frequency Spectrum (a) 10 kHz Two-Stage R-L-C Filter, (b) 65 kHz L-C Trap plus 10 kHz Two-Stage R-L-C Filter](image)
IV. Conclusions

The use of a two-stage R-L-C filter with its neutral connected to the DC link mid-point “o” proved to be advantageous for the flywheel motor/generator (M/G) system and the magnetic bearing (MB) system in reducing the common mode (CM) and differential mode (DM) without affecting the M/G control loop. Noise in the M/G system caused by the DC to AC inverter high dv/dt switching is reduced by designing the values of the R-L-C filter elements to acquire a near sinusoidal line to line waveform. Voltage stresses and the voltage doubling effect at the motor terminals have also been considerably reduced. By connecting the R-L-C filter neutral to the DC link mid-point “o” the high frequency components of the leakage current are minimized thus reducing the risk of EMI in the flywheel internal avionics.

Most importantly, noise in the magnetic bearing eddy current position sensors has been significantly reduced, consequently allowing reliable operation of the flywheel up to 60,000 rpm. With the addition of the L-C trap filter, a high impedance path has been provided at the inverter’s 65 kHz switching frequency thereby reducing the 65 kHz components of the leakage current (I_p) and the motor phase current resulting in more efficient and less noisy M/G operation.

References

Inverter Output Filter Effect on PWM Motor Drives of a Flywheel Energy Storage System

Walter Santiago

NASA Glenn Research Center (GRC) has been involved in the research and development of high speed flywheel systems for small satellite energy storage and attitude control applications. One research and development area has been the minimization of the switching noise produced by the pulsed width modulated (PWM) inverter that drives the flywheel permanent magnet motor/generator (PM M/G). This noise can interfere with the flywheel M/G hardware and the system avionics hampering the full speed performance of the flywheel system. One way to attenuate the inverter switching noise is by placing an AC filter at the three phase output terminals of the inverter with the filter neutral point connected to the DC link (DC bus) midpoint capacitors. The main benefit of using an AC filter in this fashion is the significant reduction of the inverter's high dv/dt switching and its harmonics components. Additionally, common mode (CM) and differential mode (DM) voltages caused by the inverter’s high dv/dt switching are also reduced. Several topologies of AC filters have been implemented and compared. One AC filter topology consists of a two-stage R-L-C low pass filter. The other topology consists of the same two-stage R-L-C low pass filter with a series connected trap filter (an inductor and capacitor connected in parallel). This paper presents the analysis, design and experimental results of these AC filter topologies and the comparison between the no filter case and conventional AC filter.