Waveguide Power Combiner Demonstration for Multiple High Power
Millimeter Wave TWTAs

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NASA is presently developing nuclear reactor technologies, under Project Prometheus, which will provide spacecraft with greatly increased levels of sustained onboard power and thereby dramatically enhance the capability for future deep space exploration. The first mission planned for use of this high power technology is the Jupiter Icy Moons Orbiter (JIMO). In addition to electric propulsion and science, there will also be unprecedented onboard power available for deep space communications. A 32 GHz transmitter with 1 kW of RF output power is being considered to enable the required very high data transmission rates. One approach to achieving the 1kW RF power, now being investigated at NASA GRC, is the possible power combining of a number of 100-150 W TWTs now under development.

The work presented here is the results of a proof-of-concept demonstration of the power combining Ka-band waveguide circuit design and test procedure using two Ka-band TWTAs (Varian model VZA6902V3 and Logimetrics model A440/KA-1066), both of which were previously employed in data uplink evaluation terminals at 29.36 GHz for the NASA Advanced Communications Technology Satellite (ACTS) program. The characterization of the individual TWTAs and power combining demonstration were done over a 500 MHz bandwidth from 29.1 to 29.6 GHz to simulate the Deep Space Network (DSN) bandwidth of 31.8 to 32.3 GHz. Figures 1-3 show some of the power transfer and gain measurements of the TWTAs using a swept signal generator (Agilent 83640b) for the RF input. The input and output powers were corrected for circuit insertion losses due to the waveguide components. The RF saturated powers of both ACTS TWTAs were on the order of 120 W, which is comparable to the expected output powers of the 32 GHz TWTs. Additional results for the individual TWTAs will be presented (AM/AM, AM/PM conversion and gain compression), some of which were obtained from swept frequency and power measurements using a vector network analyzer. The results for the power combining demonstration as well as a more detailed description of the power combining test circuit and test procedure will also be presented.
Figure 1. Power transfer curves at 29.35 GHz

Figure 2. Comparison of gain at 29.35 GHz

Figure 3. Saturated powers at 29.1, 29.35 and 29.6 GHz