Abstract — Oscillators that operate at 720 and 940 MHz and characterized over a temperature range of 25 °C to 200 °C and 250 °C, respectively, are presented. The oscillators are designed on alumina substrates with typical integrated circuit fabrication techniques. Cree SiC MESFETs, thin film metal-insulator-metal capacitors and spiral inductors, and Johanson miniature chip antennas make-up the circuits. The output power and phase noise are presented as a function of temperature and frequency.

Index Terms — MESFETS, chip antennas, oscillators SiC alumina.

I. INTRODUCTION

The need for electronics that operate at high temperatures is growing in both commercial and government applications. The automobile industry uses a variety of electronics that operate at high temperatures including power brake (140 °C), on-engine (150 °C) and exhaust sensors (850 °C) [1]. The oil and natural gas industry require high temperature wireless sensors for drilling and monitoring wells up to 360°C [2]. NASA’s planned missions to Venus will require electronic systems for probes and robotic landers on the surface that could reach as high as 500 °C [3]. Mechanisms for exploring the atmosphere of Venus may consist of small airplanes, but sophisticated electronics will be required to navigate the vessel in temperatures up to 320 °C [4].

NASA Glenn Research Center is developing high temperature electronics for wireless sensing applications [5, 6]. Oscillators that incorporate a capacitive pressure sensor have been developed [7]. The pressure was varied and the oscillation frequency shifted according to the change in capacitance at 300 °C. The oscillator/pressure sensor was coupled to a square ring antenna, which radiated its power over a distance of one meter.

In this paper, two oscillators that operate over a temperature range of 25 to over 200 °C are presented. Both oscillators utilize a miniature chip antenna that radiates to a receive antenna 1.7 m away. The miniature chip antenna greatly reduces the overall size of the circuit, making it much more applicable for miniaturized wireless sensor applications that operate at high temperatures.

II. DESIGN AND FABRICATION

The oscillators are based on a conventional Clapp oscillator design, which is shown in Fig. 1. The values of C_T, C_1, C_2 and L_T are given in Table 1 for both the 720 and 940 MHz designs. The method of design is illustrated in [6].

![Schematic of Clapp oscillator](https://ntrs.nasa.gov/search.jsp?R=20110011901)

Table 1. Passive component values.

<table>
<thead>
<tr>
<th>Oscillator</th>
<th>C_T (pF)</th>
<th>C_1 (pF)</th>
<th>C_2 (pF)</th>
<th>L_T (nH)</th>
</tr>
</thead>
<tbody>
<tr>
<td>720 MHz</td>
<td>10</td>
<td>4</td>
<td>4</td>
<td>8.5</td>
</tr>
<tr>
<td>940 MHz</td>
<td>5</td>
<td>10</td>
<td>10</td>
<td>10.5</td>
</tr>
</tbody>
</table>

Figures 2 and 3 are photographs of the fabricated oscillators. The oscillators are fabricated on 99.9 % alumina substrates that are 500 µm thick and have a dielectric constant of 9.9. A Cree SiC MESFET, model number CRF24010D, is used in both oscillators with thin film metal-insulator-metal (MIM) capacitors and spiral inductors. The metallization of the underlying first metal of the MIMs is Cr/Au (25/500 nm). A 1.6 µm thick silicon dioxide layer is used as the insulator; the dielectric
constant of the silicon dioxide is approximately 4.3. The top electrode metallization is Ti/Au (25 nm/1.7 µm) and finally a third layer of 1.7 µm Au, for a total metal thickness of 3.9 µm, is used to decrease the resistive losses. The thin film spiral inductor has 1.5 turns for the 720 MHz design and 1.9 turns for the 940 MHz design, which results in an inductance of 8.5 and 10.5 nH, respectively, according to Sonnet simulations. The miniature chip antennas, model number 0920AT50A080, were provided by Johanson Technology. The antennas resonate at approximately 920 MHz, which matched well the 940 MHz oscillator design, thus no matching circuit required. To better match the chip antenna to the 720 MHz design, a 2 pF chip capacitor was placed in series with the input of the antenna to resonate with the series inductance of the antenna. Wire bonds were used to connect the drain, gate and source of the transistor to DC bias pads.

The oscillators were characterized in an anechoic chamber. All of the test equipment is outside the room to eliminate interference from the equipment. The oscillators are clamped to a ceramic heater that is placed on a rotational stage so that a 180° radiation pattern can be measured. A wideband horn is used as the receive antenna and has a gain of 2.3 and 4.0 dBi at 720 and 940 MHz, respectively. The oscillators with chip antennas are separated from the receive antenna by 1.7 m. Between the oscillator and ceramic heater are four, 500 µm thick alumina wafers to minimize the effects the heating element in the ceramic heater has on the circuits. A thermocouple is placed on the circuit substrate near the oscillator to accurately record the temperature of the circuit. The temperature of the ceramic heater is varied with a Labview controlled power source. An Agilent E4440A spectrum analyzer is used to record the spectrum and phase noise at temperature increments of 25 °C starting at room temperature and ending when the oscillator fails. Before each measurement, the temperature is held constant with the oscillator operating for 10 minutes to assure thermal equilibrium is reached. The gate voltage was varied as the temperature is varied to keep the drain current constant at 100 mA for both designs. For the 720 MHz circuit, the drain voltage is 6 V, and for the 940 MHz design, the drain voltage is 8.8 V.

The measured spectrum for the 720 MHz design at 25 and 200 °C is shown in Fig. 4. It is seen that the frequency of oscillation and transmitted power decrease with temperature. The oscillation frequency decreases from 726.2 MHz at 25 °C to 720 MHz at 200 °C, which is less than a 1% change. The transmitted power decreases from -22.1 dBm at 25 °C to -27.9 dBm at 175 °C, which is only a decrease of 3 dBm, but it decreases to -34.4 dBm at 200 °C. Just beyond 200 °C, the 720 MHz oscillator seize to operate. Figure 5 shows the frequency and transmitted power received at every temperature setting.
from -27.6 dBm at 25ºC to -32.9 dBm at 175ºC. Beyond this, the power decreases more rapidly to -41.7 dB at 250ºC. Just above 250 ºC the 940 MHz oscillator seizes operation. Figure 7 shows the frequency and transmitted power received at every temperature setting.

The measured phase noise of the 940 MHz design at 25 and 250 ºC is shown in Fig. 8. The phase noise increases at higher temperatures far from the carrier frequency, which is expected. Figure 9 shows the phase noise at 100 kHz offset over the temperature range from 25 to 250 ºC. The phase noise increases as temperature increases, which is expected. The 720 MHz circuit had similar phase noise characteristics.

The measured radiation patterns of the two oscillator designs are shown in Figs. 10 and 11. Both graphs clearly demonstrate how the transmitted power decreases as temperature increases. More important, the radiation patterns for both designs change very little over the temperature ranges.

Figure 5: Measured received power and frequency for the 720 MHz circuit as a function of temperature.

Figure 6: Measured spectrum of the 940 MHz circuit at 25 and 250 ºC.

Figure 7: Measured received power and frequency for the 940 MHz circuit as a function of temperature.

Figure 8: Measured phase noise of the 940 MHz circuit at 25 and 250 ºC.

Figure 9: Measured phase noise of the 940 MHz circuit at 100 kHz offset frequency as a function of temperature.
IV. CONCLUSIONS

Oscillators that utilize miniature chip antennas have been presented. They were operated at 720 and 940 MHz over a temperature range from 25 to 200 and 250°C, respectively. Both designs have a frequency shift less than 1% over there designated temperature ranges. The transmitted power varies little till the oscillators reach the temperature just before they stop operation, then the power decreases significantly. Radiation patterns demonstrate that there is no significant change in the radiation patterns and that the chip antennas may be used to at least 250 ºC. The circuits and their characteristics demonstrate that the technology permits wireless sensors operating through 250 ºC by replacing the MIM capacitor, C_T, by a capacitive pressure sensor as was done in [7].

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REFERENCES


