The Effects of ELDRS at Ultra-Low Dose Rates

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Abstract—We present results on the effects on ELDRS at dose rates of 10, 5, 1, and 0.5 mrad(Si)/s for a variety of radiation hardened and commercial devices. We observed low dose rate enhancement below 10 mrad(Si)/s in several different parts. The magnitudes of the dose rate effects vary. The TL750L, a commercial voltage regulator, showed dose rate dependence in the functional failures, with initial failures occurring after 10 krad(Si) for the parts irradiated at 0.5 mrad(Si)/s. The RH1021 showed an increase in low dose rate enhancement by 2× at 5 mrad(Si)/s relative to 8 mrad(Si)/s and high dose rate, and parametric failure after 100 krad(Si). Additionally the ELDRS-free devices, such as the LM158 and LM117, showed evidence of dose rate sensitivity in parametric degradations. Several other parts also displayed dose rate enhancement, with relatively lower degradations up to ~15 to 20 krad(Si). The magnitudes of the dose rate enhancement will likely increase in significance at higher total dose levels.

I. INTRODUCTION

Linear bipolar circuits are known to exhibit enhanced-low-dose-rate-sensitivity (ELDRS) in an ionizing radiation environment. The physical mechanisms for ELDRS have been discussed in several previous publications [1], [2]. ELDRS has introduced new challenges for radiation hardness assurance. The primary challenge is the significant irradiation time required to examine a part for ELDRS, which is a burden to a project’s schedule and budget. There are several proposed accelerated tests, such as the elevated temperature irradiation and the switched-dose rate method [1], [3]. The elevated temperature method is inconsistent across a variety of devices, for example the LM2941 [4]. The switched-dose rate method has several issues including the large number of samples required, and the difficulty in finding the transition dose from the threshold degradation region to the power-law region [3]. The current U.S. military test standard, MIL-STD-883G TEST METHOD 1019.8, requires irradiating bipolar circuits at a minimum dose rate of 10 mrad(Si)/s. The low dose rate enhancement factor (LDR EF), which is the ratio of the relative degradation at low and high dose rate, is a standard figure-of-merit for ELDRS. The part is considered ELDRS sensitive if the EF for any parameter is > 1.5, as specified in TM1019.8.

However the saturation dose rate for parametric degradation varies for different parts. In fact, many linear bipolar devices exhibit significant degradation for dose rates less than 10 mrad(Si)/s [1], [5]. For example, the LDR EF for the LM324 increases by a factor of 6 from 5 mrad(Si)/s (EF = 2) to 2 mrad(Si)/s (EF = 12). Such a large increase in degradation will exceed the ×1.5 overtest factor for 10 mrad(Si)/s irradiations, as stated in TM1019.8.

Manufacturers have since produced parts that are tolerant at low dose rate environments. The ELDRS-free parts, such as the LM136 voltage reference, can exhibit less degradation at low dose rate (10 mrad(Si)/s) than at high dose rate [6]. But the critical question is how the ELDRS-free parts will respond at lower dose rates. There have been several recent studies on the role of hydrogen to low dose rate sensitivity [3], [5], [6]. Hydrogen contamination from certain packaging can enhance the effects of ELDRS [8]. These studies lead to the suggestion that the transition point for exhibiting ELDRS is moved to lower dose rates. Therefore while the ELDRS-free parts are guaranteed to function within specification at 10 mrad(Si), the degradation may increase significantly at lower dose rates.

The perpetual introduction of new devices, with various innovative processes and circuit designs, necessitates the understanding of the different degradation behaviors at ultra-low dose rates. Here we examine the effects of ELDRS for a large sample of commercial and radiation hardened devices from different manufacturers, at dose rates of 10, 5, 1, and 0.5 mrad(Si)/s.

II. EXPERIMENTAL DETAILS

We examine more than twenty different parts from Linear Technology, Texas Instruments, National Semiconductor, and ST Microelectronics. The various part types include voltage reference, voltage regulator, operational amplifiers, and voltage comparators. They include radiation hardened (lot tested at high dose rate), ELDRS-free (lot tested at 10 mrad(Si)/s), and commercial devices. The parts are...
available in a variety of package types: ceramic, flatpack, metal can, and dual-inline-package (DIP). In some cases the same part is available in both flatpack/DIP and metal can packages.

The irradiations are performed with a $^{60}$Co gamma ray source at room temperature. The dose rates are 10, 5, 1, and 0.5 mrad(Si)/s. Four to five samples of each part are irradiated for each dose rate. At least two samples of each part are used as controls. Most of the parts, including voltage regulators and references, are irradiated with all pins grounded. The operational amplifiers and voltage

<table>
<thead>
<tr>
<th>Part Number (package type)</th>
<th>Lot-Date-Code</th>
<th>Function</th>
<th>Irradiation Bias</th>
<th>Summary of Results</th>
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<tbody>
<tr>
<td>Texas Instruments</td>
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<tr>
<td>LT1009IDR (8-SOIC)</td>
<td>0606</td>
<td>2.5V internal reference</td>
<td>All pins grounded</td>
<td>• Parameters within specification after 100, 30, and 15 krad(Si) for the 5, 1, and 0.5 mrad(Si) parts.</td>
</tr>
</tbody>
</table>
| LM317KTTR (3-DDPAK)        | 0608          | Positive volt reg 3-terminal | All pins grounded | • Parameters within specification after 80, 20, and 15 krad(Si) for the 5, 1, and 0.5 mrad(Si) parts.  
• LDR enhancement: 5 − 0.5 mrad(Si)/s |
| TL750L05CDR (8-pin plastic SOIC) | 0605 | LDO positive voltage reg 5V | All pins grounded | • LDR enhancement for functional failures.  
5 mrad(Si)/s: 35 < V_{out} < 40 krad(Si)  
1 mrad(Si)/s: 10 < V_{out} < 15 krad(Si)  
0.5 mrad(Si)/s: 7.5 < V_{out} < 10 krad(Si). |
| TL750M05CKTRR (TO263-3)    | 0707          | LDO voltage regulator | All pins grounded | • $V_{out}$ failure levels (I_{o} = 10 mA)  
5 mrad(Si)/s: 70 < V_{out} < 80 krad(Si)  
1 mrad(Si)/s: > 20 krad(Si)  
0.5 mrad(Si)/s: > 15 krad(Si). |
| National Semiconductor     |               |          |                  |                    |
| LM117THRQLMV (TO-39 metal can) | 7D5867L019 | Voltage comparator | All pins grounded | • LDR enhancement observed for $V_{ref}$ degradation.  
• Parameters within specification after 90, 20, and 15 krad(Si) for the 5, 1, and 0.5 mrad(Si)/s parts. |
| LM158AIRQLMV (8-lead CERDIP) | 7W4453G019 | Op-Amp | All pins grounded | • LDR enhancement: 5 − 0.5 mrad(Si)/s.  
5 mrad(Si)/s (1 part): 60 < I_{o} < 70 krad(Si). |
| LM136 (3-lead TO-46)       | 200746K019    | Voltage Reference 2.5 | All pins grounded | • Parameters within specification after 100, 20, and 10 krad(Si) for the 5, 1, and 0.5 mrad(Si) devices. |
| LM124AJRQMLV (14-lead CERDIP) | 9R5469G019 | Operational amplifier | Biased and grounded | In progress |
| LM139AWRQMLV (14-lead CERDIP) | JM046X13     | Voltage comparator | Biased and grounded | In progress |
| Linear Technology          |               |          |                  |                    |
| RH1013MH (TO-5 metal can)  | 0329A         | Dual precision op-amp | Biased and grounded | • Parameters within specification after 100, 20, and 10 krad(Si) for 5, 1, and 0.5 mrad(Si). |
| RH1013MJ8 (Ceramic DIP)    | 0305A         |          |                  |                    |
| RH1021CMH-5 (TO-5 can)     | 9783A         | Precision 5V Reference | All pins grounded | • LDR enhancement: 5 mrad(Si)/s.  
5 mrad(Si) (TO-5): 90 < V_{z} < 100 krad(Si). |
| RH1021CMW-5 (Flatpack)     | 0123A         |          |                  |                    |
| RH1009MW (Flatpack)        | 0649A         | 2.5V Reference | All pins grounded | • ELDRS observed.  
5 mrad(Si)/s TO-46 cans: 80 < V_{z} < 90 krad(Si)  
5 mrad(Si)/s Flatpacks: 100 < V_{z} < 120 krad(Si). |
| RH1009MH (TO-46 can)       | 0829H         |          |                  |                    |
| RH1078MW (Glass sealed Flatpack) | 0741A | Single supply, precision op-amp | Biased and grounded | In progress |
| RH1078MH (TO5 metal can)   | 0325A         |          |                  |                    |
| ST Microelectronics        |               |          |                  |                    |
| RHFL4913ESY332 (TO-257)   | 30828A        | Voltage regulator | All pins grounded | • Negligible degradations levels. Parameters within specification after 10 and 13 krad(Si) for the 10 and 1 mrad(Si) devices. |
| RHFL4913KP332 (Flat-16)   | 30814B        |          |                  |                    |
| RH310 (Ceramic Flat-8)     | 30849A        | Operational amplifier | Biased and grounded | In progress |
| RH43B (Ceramic Flat-8)     | 30820A        | Precision single operational amplifier | Biased and grounded | In progress |
comparators are irradiated with both biased and unbiased (all pins grounded) conditions.

We characterized the electrical parameters onsite, with the exception of the devices from National Semiconductor, which were also shipped back to the manufacturer’s testing facility at select doses. The test and bias circuits, where applicable, were fabricated in-house. We used the Agilent 6624 power supply, the HP34401 digital multimeter, and the Keithley 2425 source meter to characterize the RHF4913 and LM117 voltage regulators. We also used the HP34401 and Agilent 33250A waveform generator to characterize the CMRR and open loop gain for the operational amplifiers. In addition we used the Agilent 4156 and Keithley 4200 parameter analyzer to characterize all other parts. We used DC characterization to measure any DC parameter, whereas for some parts the parametric specifications were obtained using the manufacturer’s specific pulse techniques.

III. RESULTS

Table I. is a list of all devices under investigation. The table includes the part number, package type, lot-date-code, part function, bias configuration, and a short summary of results. Here we show highlight results from several select parts.

A. LM158A

The LM158AJRQMLV is an ELDRS-free low power dual operational amplifier manufactured by National Semiconductor, qualified up to 100 krad(Si) at 10 mrad(Si)/s. Figure 1 shows the average input bias current (I_b) vs. total dose at various dose rates. The input bias current is the average of the positive and negative input currents on one device for all parts. The error bars indicate part-to-part variation. There is minimal device-to-device variation within a package. We note that the increasing error bars for the 5 mrad(Si)/s data is due to one part showing significant degradation relative to the other two parts. The I_b of the rogue device increases to beyond specification (> 50 nA) after 70 krad(Si).

Figure 2 shows the change in I_b with dose rate at different TID levels. We observed dose rate enhancement in the I_b degradation after 15 krad(Si), where the degradation increases with decreasing dose rate. In general, the LDR EF for I_b is less than 1, but increases with TID. The average EF for the 5 mrad(Si)/s devices increases to 1.2 after 70 krad(Si), owing to the enhanced degradation of one part. We will realize the significance of the dose rate enhancement at higher TID levels.

B. RH1021

The RH1021 is a voltage reference from Linear Technology previously qualified at high dose rate. We evaluate two package types: RH1021CMH (TO-5 metal cans) and RH1021CMW (10-lead ceramic flatpacks). Figure 3 shows the reference voltage (V_ref) with increasing TID at 5 and 8 mrad(Si)/s, and at high dose rate for the TO-5 packages. The 8 mrad(Si)/s and high dose rate data show similar levels of degradation. However the 5 mrad(Si)/s response showed significantly higher degradation. The LDR EF increases by a factor of 2 after 30 krad(Si). Therefore the part is ELDRS sensitive at ≤ 5 mrad(Si)/s. The reference voltage failed specification after 100 krad(Si).

We also found that the TO-5 packages degraded more significantly than the flatpacks at dose rates of 5, 1, and 0.5 mrad(Si)/s. Figure 4 compares the two package types at 5 mrad(Si)/s. The results indicate that the flatpack devices here do not exhibit hydrogen contamination effects that can enhance ELDRS [8].

C. TL750L

The TL750L05CDR is a commercial low-dropout voltage regulator manufactured by Texas Instruments. We observed distinct dose rate dependence for the functional failures. The initial failures occur after 40, 20, and 10
krad(Si) for the 5, 1, and 0.5 mrad(Si)/s parts, respectively. Figure 5 shows the number of part failures (from 4 parts at each dose rate) vs. total dose. The functional failures are characterized by the failure of the output voltage ($V_{out}$) to regulate with 100 mA load, while remaining functional with 10 mA load. In most cases the part fails to regulate with 10 mA load at the next dose step.

The failures were abrupt, without gradual degradation to the output voltage or any other measured parameter. The degradation behavior is similar to the 29372 low-dropout regulator from a previous study [9]. The output voltage failed to regulate the preset load, as the maximum output drive current degrades with total dose [9]. Consequently the output failed to regulate for the 100 mA load prior to failure with the 10 mA load.

It is also possible that the radiation-induced leakage current becomes significant, so that the internal current limiting protection circuitry shuts down the device. This is consistent with the fact that the failed devices do not draw any current. The 10 mrad(Si)/s irradiations are currently in process.

D. LM117H

The LM117HRQMLV is an ELDRS-free adjustable positive voltage regulator manufactured by National Semiconductor, qualified up to 100 krad(Si) at 10 mrad(Si)/s. Figure 6 shows the change in the reference voltage ($V_{in} = 40$ V and $I_{out} = 10$ mA) with TID at different dose rates. Figure 7 shows the dose at which the reference voltage degrades beyond the pre-irradiation specification ($V_{ref} = 1.3$ V). We observed dose rate dependence for the dose at which $V_{ref}$ exceeds the pre-irradiation specification: 20, 15 and 10 krad(Si) for the 5, 1, and 0.5 mrad(Si)/s parts. The post-irradiation specification limit for the reference voltage is 1.35 V. The dose rate enhancement will likely become more evident at higher TID levels.

E. LM317

The LM317KTTR is a commercial adjustable voltage regulator manufactured by Texas Instruments. The parts are
grounded during irradiation. The device parameters, including the output voltage, line and load regulations, are within specification at this stage of the irradiation. Figure 8 shows the change in $V_{out}$ with $V_{in} = 40$ V and $I_{out} = 80$ mA. The output voltage decreases with increasing TID. Figure 9 shows the change in $V_{out}$ as a function of dose rate at 15 krad(Si). We observed dose rate enhancement in the $V_{out}$ degradation. The dose rate enhancement is significantly higher with the larger output load ($I_{out} = 800$ mA).

F. RH1009

Figure 10 shows the reverse breakdown voltage vs. total dose for the RH1009 voltage reference manufactured by Linear Technology. Two types of packages are included: RH1009MH (TO-46 metal can) and RH1009MW (ceramic flatpack). Figure 10 also contains data from high dose rate results on parts from the same date code. We observed enhanced degradation for the 5 mrad(Si)/s data relative to the high dose rate data. The LDR EF = 4.2 and 3.6 for the TO-46 cans and flatpacks, respectively, after 50 krad(Si). The EF increases to ~ 13 for the TO-46 devices after 90 krad(Si). The average reverse breakdown voltage exceeds specification (2.495 V) after 90 krad(Si) for the TO-46 packages. Therefore the results indicate that these devices are susceptible to ELDRS at 5 mrad(Si)/s.

Additionally, the TO-46 and flatpacks exhibit opposing degradation trends. The TO-46 devices showed decreasing (negative-going) $V_{out}$, while the flatpack devices show increasing (positive-going) $V_{out}$ with TID. The device packaging may have affected the different degradation behaviors. However we also observed increasing $V_{out}$ for the 1 and 0.5 mrad(Si)/s TO-46 parts. Therefore the different behaviors of the package types at 5 mrad(Si)/s are more likely due to part-to-part variation.

IV. CONCLUSION

We have presented results of the effects of ELDRS for a variety of radiation hardened and commercial devices, at dose rates varying from 10 to 0.5 mrad(Si)/s. We observed low dose rate enhancement in several parts, where the degradation
increases with decreasing dose rate to as low as 0.5 mrad(Si)/s. The degradation enhancement is severe for some parts. For example the TL750L exhibited dose rate enhancement for the functional failures. The initial failures occur after 10 krad(Si) at 0.5 mrad(Si)/s. While the RH1021 displayed similar degradation levels at ~ 8 mrad(Si)/s as at high dose rate, the low dose rate EF increased by a factor of 2 at 5 mrad(Si)/s, after 30 krad(Si). Parametric failure occurs after 100 krad(Si).

The cases presented here, in addition to previous examples in [1], illustrate the significance and pervasiveness of low dose rate enhancement at dose rates lower than 10 mrad(Si). The ELDRS-free devices, as shown for the LM158 and LM117, are also susceptible to enhanced degradation at the lower dose rates. The low dose rate EF for the devices that exhibit dose rate enhancement will likely continue to increase with increasing total dose, until we establish the transition dose for exhibiting ELDRS (EF = 1.5), followed by the damage saturation point. These results present further challenges for radiation hardness assurance of bipolar linear circuits.

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VI. REFERENCES