improved the noise temperature substantially. This may be because the mixer was presented with a better impedance match with the use of the isolator.

Cryogenic testing indicates a system noise temperature of 100 K or less at 166 GHz. Prior tests of the MMIC amplifiers alone have resulted in a system noise temperature of 65–70 K in the same frequency range (≈160 GHz) when cooled to an ambient temperature of 20 K. While other detector systems may be slightly more sensitive (such as SIS mixers), they require more cooling (to 4 K ambient) and are not as easily scalable to build a large array, due to the need for large magnets and other equipment.

When cooled to 20 K, this receiver module achieves approximately 100 K system noise temperature, which is slightly higher than single-amplifier module results obtained at JPL (65–70 K when an amplifier is corrected for back-end noise contributions). If this performance can be realized in practice, and a scalable array can be produced, the impact on cosmic microwave background experiments, astronomical and Earth spectroscopy, interferometry, and radio astronomy in general will be dramatic.

This work was done by Lorene A. Samoska, Mary M. Soria, Heather R. Owen, Douglas D. Dawson, Pekka P. Kangashoht, and Todd C. Gaier of Caltech, and Patricia Vold, Judy Lau, Matt Sieth, and Sarah Church of Stanford University for NASA’s Jet Propulsion Laboratory. Further information is contained in a TSP (see page 1). NPO-47873

**Ka-Band, Multi-Gigabit-Per-Second Transceiver**

*John H. Glenn Research Center, Cleveland, Ohio*

A document discusses a multi-Gigabit-per-second, Ka-band transceiver with a software-defined modem (SDM) capable of digitally encoding/decoding data and compensating for linear and nonlinear distortions in the end-to-end system, including the traveling-wave tube amplifier (TWTA). This innovation can increase data rates of space-to-ground communication links, and has potential application to NASA’s future space-based Earth observation system.

The SDM incorporates an extended version of the industry-standard DVB-S2, and LDPC rate 9/10 FEC codec. The SDM supports a suite of waveforms, including QPSK, 8-PSK, 16-APSK, 32-APSK, 64-APSK, and 128-QAM. The Ka-band and TWTA deliver an output power on the order of 200 W with efficiency greater than 60%, and a passband of at least 3 GHz. The modem and the TWTA together enable a data rate of 20 Gbps with a low bit error rate (BER).

The payload data rates for spacecraft in NASA’s integrated space communications network can be increased by an order of magnitude (>10×) over current state-of-practice. This innovation enhances the data rate by using bandwidth-efficient modulation techniques, which transmit a higher number of bits per Hertz of bandwidth than the currently used quadrature phase shift keying (QPSK) waveforms.

This work was done by Rainee N. Simons and Edwin C. Wintucky of Glenn Research Center; and Francis J. Smith, Johann M. Harris, David G. Landon, Osama S. Haddadin, William K. McIntire, and June Y. Sun of L-3 Communications Systems–West. Further information is contained in a TSP (see page 1).

Inquiries concerning rights for the commercial use of this invention should be addressed to NASA Glenn Research Center, Innovative Partnerships Office, Attn: Steven Fedor, Mail Stop 4–8, 21000 Brookpark Road, Cleveland, Ohio 44135. Refer to LEW-18735-1.

**All-Solid-State 2.45-to-2.78-THz Source**

*Applications include laboratory spectroscopy, THz imaging, and heterodyne instrumentation.*

*NASA’s Jet Propulsion Laboratory, Pasadena, California*

Sources in the THz range are required in order for NASA to implement heterodyne instruments in this frequency range. The source that has been demonstrated here will be used for an instrument on the SOFIA platform as well as for upcoming astrophysics missions. There are currently no electronic sources in the 2–3-THz frequency range. An electronically tunable compact source in this frequency range will be used for an instrument on astronomical and Earth spectroscopy, interferometry, and radio astronomy in general will be dramatic.

This work was done by Lorene A. Samoska, Mary M. Soria, Heather R. Owen, Douglas D. Dawson, Pekka P. Kangashoht, and Todd C. Gaier of Caltech, and Patricia Vold, Judy Lau, Matt Sieth, and Sarah Church of Stanford University for NASA’s Jet Propulsion Laboratory. Further information is contained in a TSP (see page 1). NPO-47873

Inquiries concerning rights for the commercial use of this invention should be addressed to NASA Glenn Research Center, Innovative Partnerships Office, Attn: Steven Fedor, Mail Stop 4–8, 21000 Brookpark Road, Cleveland, Ohio 44135. Refer to LEW-18735-1.

Inquiries concerning rights for the commercial use of this invention should be addressed to NASA Glenn Research Center, Innovative Partnerships Office, Attn: Steven Fedor, Mail Stop 4–8, 21000 Brookpark Road, Cleveland, Ohio 44135. Refer to LEW-18735-1.
Onboard Interferometric SAR Processor for the Ka-band Radar Interferometer (KaRIn)

An interferometric synthetic aperture radar (SAR) onboard processor concept and algorithm has been developed for the Ka-band radar interferometer (KaRIn) instrument on the Surface and Ocean Topography (SWOT) mission. This is a mission-critical subsystem that will perform interferometric SAR processing and multi-look averaging over the oceans to decrease the data rate by three orders of magnitude, and therefore enable the downlink of the radar data to the ground.

The onboard processor performs demodulation, range compression, co-registration, and re-sampling, and forms nine azimuth squinted beams. For each of them, an interferogram is generated, including common-band spectral filtering to improve correlation, followed by averaging to the final 1×1-km ground resolution pixel. The onboard processor has been prototyped on a custom FPGA-based cPCI board, which will be part of the radar’s digital subsystem.

The level of complexity of this technology, dictated by the implementation of interferometric SAR processing at high resolution, the extremely tight level of accuracy required, and its implementation on FPGAs are unprecedented at the time of this reporting for an onboard processor for flight applications.

This work was done by Daniel Esteban-Fernandez, Ernesto Rodriguez, Eva Peral, Duane I. Clark, and Xiaoqing Wu of Caltech for NASA’s Jet Propulsion Laboratory. Further information is contained in a TSP (see page 1). NPO-47789