Effects of Radiation and Long-Term Thermal Cycling on EPC 1001 Gallium Nitride Transistors

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ABSTRACT

Electronics designed for use in NASA space missions are required to work efficiently and reliably under harsh environment conditions. These include radiation, extreme temperatures, and thermal cycling, to name a few. Data obtained on long-term thermal cycling of new un-irradiated and irradiated samples of EPC1001 gallium nitride enhancement-mode transistors are presented. This work was done by a collaborative effort including GRC, GSFC, and JPL in support of the NASA Electronic Parts and Packaging (NEPP) Program.
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SCOPE OF WORK

- A NEPP (NASA Electronic Parts and Packaging) collaborative effort among NASA Centers to address performance and reliability of new COTS (Commercial-Off-The-Shelf) power devices based on wide bandgap semiconductor for use in space harsh environment

- Test and evaluate performance of emerging GaN (Gallium Nitride) & SiC (Silicon Carbide) power devices under the exposure to radiation and thermal cycling

- Document results and disseminate findings
TECHNICAL APPROACH:

- Identify and acquire candidate power devices
- Perform parametric evaluation
- Subject devices to radiation exposure representative of mission environment
- Perform long-term thermal cycling on survived parts
- Determine effects of radiation and temperature cycling on performance of devices
- Address reliability, determine risk factors, and identify mitigation techniques for device use in space missions
TEST DEVICES

- **Efficient Power Conversion, EPC1001, GaN transistors grown on Si (Silicon) wafer;** [http://www.epc-co.com](http://www.epc-co.com)
- **Passivated-die form with solder bumps**

Sample die mounted on test structure

<table>
<thead>
<tr>
<th># of Parts</th>
<th>Device Label</th>
<th>Condition</th>
<th>Ion</th>
<th>Energy (MeV)</th>
<th>LET (MeV·cm²/gm)</th>
<th>Range (μm)</th>
<th>Dose (rad)</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>K7063</td>
<td>Irradiated</td>
<td>Au</td>
<td>2342</td>
<td>84.7</td>
<td>122.9</td>
<td>22718</td>
</tr>
<tr>
<td>1</td>
<td>K7064</td>
<td>Irradiated</td>
<td>Xe</td>
<td>1569</td>
<td>98.8</td>
<td>124.5</td>
<td>8301</td>
</tr>
<tr>
<td>1</td>
<td>K7044</td>
<td>Irradiated</td>
<td>Xe</td>
<td>1569</td>
<td>50.9</td>
<td>124.5</td>
<td>7886</td>
</tr>
<tr>
<td>1</td>
<td>K7065</td>
<td>Irradiated</td>
<td>Xe</td>
<td>1569</td>
<td>98.8</td>
<td>124.5</td>
<td>15838</td>
</tr>
<tr>
<td>4</td>
<td>K7068-K7071</td>
<td>Control (un-irradiated)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
## EPC1001 Enhancement-Mode GaN Power Transistor

### Manufacturer’s Specifications

<table>
<thead>
<tr>
<th>Part #</th>
<th>EPC1001</th>
</tr>
</thead>
<tbody>
<tr>
<td>Drain-Source Voltage, $V_{DS}$ (V)</td>
<td>100</td>
</tr>
<tr>
<td>Gate Threshold Voltage, $V_{TH}$ (V)</td>
<td>1.4 @ $V_{DS} = V_{GS}$, $I_D = 5$ mA</td>
</tr>
<tr>
<td>Drain Current, $I_D$ (A)</td>
<td>25</td>
</tr>
<tr>
<td>Drain-Source On Resistance, $R_{DS(ON)}$ (mΩ)</td>
<td>5.6 @ $V_{GS} = 5$V, $I_D = 25$ A</td>
</tr>
<tr>
<td>Operating Temperature, $T_J$ (ºC)</td>
<td>-40 to +125</td>
</tr>
<tr>
<td>Package Type</td>
<td>Passivated-Die with Solder Bumps</td>
</tr>
</tbody>
</table>
Focused Ion Beam and SEM Cross-Section of EPC GaN Transistor

SEM Micrograph, 65X, 52 deg. Tilt

SEM Micrograph after FIB Cut, 2500X, 52 deg. Tilt

Sn/Pb
RADIATION DATA

- Devices were irradiated under bias at increasing drain biases
- Transfer curves were measured between irradiations
- Devices still functioned but were well out of spec after irradiation

Device K7044
RADIATION DATA

Device K7063

- Drain Current
- Gate Current
- Drain Voltage

Graphs showing the relationship between elapsed time, drain current, gate current, drain-to-source voltage, and gate-to-source voltage for Device K7063.
THERMAL CYCLING TEST

Cycling profile

- Total # of Cycles 1000
- Temperature rate of change: 10 °C/min
- 18 min temp change time
- Temperature range: -55 °C to +125 °C
- Soak time at extreme temperatures: 10 min
THERMAL CYCLING SETUP & MEASUREMENT

Parameters

- I-V Output Characteristics
- Gate Threshold Voltage, $V_{TH}$
- Drain-Source On-Resistance, $R_{DS(on)}$

Equipment

- SONY/Tektronix 370A Curve Tracer
- Keithley 238 Source-Measure-Units
- Sun System Environmental Chamber

Measurements

- Pre, during, & post-cycling
- At room temperature
I-V Curves for K7068 (control)

Pre-Cycling

After 500 Cycles

After 824 Cycles

After 1000 Cycles
I-V Curves for K7069 (control)

Pre-Cycling

- $V_G = 2.0 \text{ V}$
- $V_G = 1.950 \text{ V}$

After 500 Cycles

- $V_G = 1.950 \text{ V}$
- $V_G = 1.95 \text{ V}$

After 824 Cycles

- $V_G = 2.0 \text{ V}$
- $V_G = 1.950 \text{ V}$

After 1000 Cycles

- $V_G = 1.95 \text{ V}$
- $V_G = 1.950 \text{ V}$
I-V Curves for K7070 (control)

**Pre-Cycling**

- $V_{GS} = 1.9 \text{ V}$
- $1.4 \text{ V}$
- $1.3 \text{ V}$
- $1.2 \text{ V}$
- $1.1 \text{ V}$
- $1.0 \text{ V}$

**After 500 Cycles**

- $V_{GS} = 1.77 \text{ V}$
- $1.27 \text{ V}$
- $1.17 \text{ V}$
- $1.07 \text{ V}$
- $0.97 \text{ V}$
- $0.87 \text{ V}$

**After 824 Cycles**

- $V_{GS} = 1.77 \text{ V}$
- $1.27 \text{ V}$
- $1.17 \text{ V}$
- $1.07 \text{ V}$
- $0.97 \text{ V}$
- $0.87 \text{ V}$

**After 1000 Cycles**

- $V_{GS} = 1.72 \text{ V}$
- $1.22 \text{ V}$
- $1.12 \text{ V}$
- $1.02 \text{ V}$
- $0.92 \text{ V}$
I-V Curves for K7071 (control)

Pre-Cycling

After 500 Cycles

After 824 Cycles

After 1000 Cycles
I-V Curves for K7063 (irradiated, Au ions, 22.7 krad)

Pre-Cycling

After 500 Cycles

After 824 Cycles

After 1000 Cycles
I-V Curves for K7064 (irradiated, Xe ions, 8.3 krad)

Pre-Cycling

- $V_{GS} = 1.85 \text{ V}$
- $V_{DS} = 1.35 \text{ V}
- I_D = 0.15 \text{ A}$

After 500 Cycles

- $V_{GS} = 1.815 \text{ V}$
- $V_{DS} = 1.315 \text{ V}$
- I_D = 0.15 A

After 824 Cycles

- $V_{GS} = 1.815 \text{ V}$
- $V_{DS} = 1.315 \text{ V}$
- I_D = 0.15 A

After 1000 Cycles

- $V_{GS} = 1.72 \text{ V}$
- $V_{DS} = 1.22 \text{ V}$
- I_D = 0.15 A
I-V Curves for K7044 (irradiated, Xe ions, 7.9 krad)

**Pre-Cycling**

- $V_{GS} = 1.95 \, V$
- $1.45 \, V$
- $1.35 \, V$
- $1.25 \, V$
- $1.15 \, V$

**After 500 Cycles**

- $V_{GS} = 1.85 \, V$
- $1.35 \, V$
- $1.25 \, V$
- $1.15 \, V$
- $1.05 \, V$

**After 824 Cycles**

- $V_{GS} = 1.85 \, V$
- $1.35 \, V$
- $1.25 \, V$
- $1.15 \, V$
- $1.05 \, V$
- $0.95 \, V$

**After 1000 Cycles**

- $V_{GS} = 1.76 \, V$
- $1.26 \, V$
- $1.16 \, V$
- $1.06 \, V$
- $0.96 \, V$
I-V Curves for K7065 (irradiated, Xe ions, 15.8 krad)

Pre-Cycling

After 500 Cycles

After 824 Cycles

After 1000 Cycles
Drain Current of EPC1001 GaN Transistors to 1000 Thermal Cycles

- K7063 Au 22.7 krad
- K7044 Xe 7.9 krad
- K7064 Xe 8.3 krad
- K7065 Xe 15.8 krad
- K7068 not-irradiated
- K7069 
- K7071 
- K7070 

V_g = 1.3 V
V_d = 0.2 V
GATE THRESHOLD VOLTAGE, \( V_{\text{TH}} \)

![Graph showing GATE THRESHOLD VOLTAGE, \( V_{\text{TH}} \) over number of thermal cycles for various samples designated by numbers and symbols (K7068, K7069, K7070, K7071, K7044 (irrad.), K7063 (irrad.), K7064 (irrad.), K7065 (irrad.).) The graph indicates changes in threshold voltage with thermal cycling.](#)
Drain-Source On Resistance, $R_{DS(ON)}$
OBSERVATIONS

- All eight GaN transistors remained functional after exposure to radiation followed by 1000 thermal cycles between -55 & +125 °C.

- Main impact of radiation was increase in leakage current of devices.

- Thermal cycling seemed to introduce inconsistent variation in I-V characteristic curves of the GaN transistors; notably in their transconductance.

- $V_{TH}$ of tested devices experienced an initial decrease with cycling but seemed to level off after exposure to about 130 cycles; possibly due to thermal conditioning.

- For the $R_{DS(ON)}$ data, at 1000 thermal cycles the values of $R_{DS(ON)}$ occurred in two distinct clusters. A two-sample t-test ($p<0.005$) showed that the means of the clusters were different. The cause of the two distinct clusters is not known, but further investigation should probably wait until the second generation of the devices is tested.
PROPOSED FOLLOW-UP

- Conduct multi-stress tests (electrical/thermal) on these control and irradiated GaN FETs (Field Effect Transistors).
- Perform overstress tests to determine failure mechanisms
- Evaluate and assess performance of second generation of these devices
- Repeat work on newly-developed GaN and SiC COTS power devices in support of NEPP Program
ACKNOWLEDGMENT

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