Recent Radiation Test Results for Trench Power MOSFETs

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Abstract—Single-event effect (SEE) radiation test results are presented for various trench-gate power MOSFETs. The heavy-ion response of the first (and only) radiation-hardened trench-gate power MOSFET is evaluated: the manufacturer SEE response curve is verified and importantly, no localized dosing effects are measured, distinguishing it from other, non-hardened trench-gate power MOSFETs. Evaluations are made of n-type commercial and both n- and p-type automotive grade trench-gate devices. The n-type devices yielded expected localized dosing effects including when irradiated in an unbiased (0-V) configuration, adding to the challenge of inserting these parts into space flight missions.

I. INTRODUCTION

VERTICAL trench-gate style MOSFETs have been a staple commercially for low- to medium-voltage applications due to their lower on-state resistance (R_DS_ON). A lower R_DS_ON is achieved in part from the vertically-oriented gates penetrating past the body region, thereby eliminating the parasitic JFET that limits the minimum cell pitch in planar-gate vertical double-diffused MOSFET (VDMOS). With the advent of CubeSats, these trench-gate MOSFETs have become common on spacecraft tolerant of higher risk. Trench-gate power MOSFETs are vulnerable to both catastrophic single-event effects (SEE) and degradation due to localized ionizing dose effects from heavy ions [1-7]. In 2013, International Rectifier Corp. (now Infineon, A.G.) released the first (and to date, only) radiation-hardened trench-gate power MOSFET. It is an n-type, 20 V, logic-level device targeted for point-of-load applications [8].

II. EXPERIMENTAL METHODS

A. Heavy-Ion Testing

Heavy-ion test data were taken at the Texas A&M University Cyclotron Facility (TAMU) in air (1.5 cm airgap) or at Lawrence Berkeley National Laboratory (LBNL) in vacuum (Table II). All ion energies used were sufficient to place the Bragg peak past the epilayer into the substrate region of the MOSFETs. Six devices-under-test (DUTs) can be mounted on the test board with daughter cards and individually accessed via dry Reed relays controlled by an Agilent 34907A data acquisition/switch unit. All terminals of the devices not under test are then floating. The equivalent circuit of the selected device is shown in Fig. 1. Additional tests of the Si7414DN were conducted under 0 V drain-voltage conditions.
source \((V_{DS})\) and 0 V gate-source \((V_{GS})\). This bias condition was achieved either by setting the gate and drain current-voltage sourcing and measurement units (SMUs) to 0 V to be able to measure current, or by tying the DUT pins together at the daughter card.

Samples were delidded or decapsulated and fully electrically characterized off-site; on-site prior to the first irradiation run and following each irradiation run, a gate stress test was performed in which the gate leakage current \((I_{GSS})\) was measured as a function of gate voltage at 0 VDS, and the drain-source breakdown voltage \((BV_{DSS})\) or the drain-source leakage current at 0 VGS \((I_{DSS})\) was measured. Measurement equipment included a Keithley 2635A or 2400 SMU for gate voltage supply and current measurement (< 1 nA accuracy) and a Keithley 2657A or 2410 SMU for the drain voltage supply and drain current measurement.

![Fig. 1. Equivalent power MOSFET heavy-ion test circuit.](image)

### Table II

<table>
<thead>
<tr>
<th>Facility</th>
<th>Ion</th>
<th>Air Gap ((cm))</th>
<th>Surface Energy ((MeV))</th>
<th>Surface LET ((MeV/cm^2/mg))</th>
<th>Range ((\mu m))</th>
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<td>TAMU</td>
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<td>48</td>
<td>90</td>
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</table>

### B. Proton Testing

Proton test data were taken on the Si7414DN at the Massachusetts General Hospital (MGH) Francis H. Burr Proton Beam Therapy Center. The proton beam energy was 200 MeV; the scatter beam style was used to randomize the pencil-beam location during proton spills. Individual DUTs mounted to daughter cards were placed at normal incidence and approximately 15 cm from the beam port in air. Source, drain, and gate voltages were supplied directly to the daughter card by two Keithley 2400 SMUs. In this preliminary test, no stiffening capacitance was added to the drain node. Four DUTs were irradiated to a maximum fluence of \(1 \times 10^{11} \text{ cm}^2\) per step in VDS, with one DUT receiving a total fluence of \(1 \times 10^{11} \text{ cm}^2\) at 60 VDS.

III. **Results**

#### A. Si7414DN

1) **Heavy-Ion Tests**

Testing was performed with argon and neon ions to evaluate the threshold for catastrophic SEE from galactic cosmic ray ion species with a relatively high flux on orbit (i.e., before the “iron knee” of the flux vs. linear energy transfer (LET) curve for galactic cosmic rays (GCRs)). Additional tests with copper and krypton bounded the failure threshold for this higher flux portion of the GCR spectrum. Devices under test were irradiated with VGS at 0 V and VDS stepped in 3-V increments per beam run.

Two DUTs were irradiated only at the threshold failure bias condition to confirm that prior beam exposures had not lowered this threshold. DUTs experienced either localized dosing effects (see Fig. 2 for an example) or single-event burnout (SEB). SEB resulted in a resistive pathway between drain and source rather than a complete short, as shown for this same DUT in Fig. 3. In Fig. 4, the BV_DSS curves are shown for this DUT prior to irradiation and after the two beam runs. As shown in Fig. 4, cumulative, gradual degradation of the drain leakage current during irradiation resulted in an elevation of drain current at voltages below breakdown, but the voltage at which the characteristic sharp increase in current defining breakdown occurs did not change. In this figure, a solid line marking 64 V is provided as a guide for comparing the pre- and post-30 V irradiation curves. BV_DSS curve data were taken in one-volt increments at 0 VGS. Applying the manufacturer 250 \(\mu\)A drain current condition, BV_DSS dropped from 66 V to under 6 V after irradiation at VDS = 33 V with VGS = 0 V. The corresponding strip tape capturing this SEB event is in Fig. 3. To rule out TID effects, BV_DSS was re-measured 8 months later at -20 VGS and showed no significant change (Fig. 4).

Past research by others has shown that ion strikes through the length of the gate oxide locally shift the flatband voltage, forming a transistor region with lower gate threshold voltage [1]. As shown in this study and in Fig. 5, this shift is worse at non-zero gate bias. Fig. 6 shows the normalized device gate threshold voltage as a function of different ion species. For a given ion species, VGS = 0 V and the DUT VDS started at 24 V and was incremented by 3 V after each exposure, except for 2 DUTs which were held at 0 VDS while exposed to 659 MeV Cu or 886 MeV Kr ions (+ and × symbols). At 0 VDS, the more pronounced role of interface charge trapping is apparent by the lower rate of change in threshold voltage with increasing dose.

As shown in Fig. 4 after irradiation at 33 VDS, heavy-ion total ionizing dose also affects the zero-gate drain leakage current \((I_{DSS})\). \(I_{DSS}\) as a function of total ionizing dose from either 659 MeV Cu or 886 MeV Kr is shown in Fig. 7 for the same 2 DUTs irradiated at 0 VGS and 0 VDS whose shift in gate threshold voltage is given in Fig. 6. Pre-radiation \(I_{DSS}\) values were 7 nA and 3 nA, respectively.

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Fig. 2. Si7414DN striptape data showing gate and drain current as a function of time (ion fluence). Ar beam on at 0 s.

Fig. 3. Si7414DN striptape data showing catastrophic SEB. Drain-source became resistive pathway. Ar beam on at 0 s. This DUT was exposed previously to the beam under a 30 Vgs bias (see Fig. 2).

Fig. 4. Si7414DN breakdown voltage curves prior to irradiation and after the two beam exposures at drain voltages indicated in legend. Dashed curve taken 9 months later extends the 33-V post-rad curve to show \( I_D-V_{DS} \) linearity out to higher current/voltage. Dotted line shows \( BV_{DSS} \) curve is unaffected at hard-off (-20 V) \( V_{GS} \). Solid vertical black line marks 64 V. \( BV_{DSS} \) is defined by the manufacturer at 250 \( \mu \)A drain current.

Fig. 5. \( I_D-V_{GS} \) curves for two Si7414DN DUTs irradiated with \( 3 \times 10^6 \) cm\(^{-2} \) Ar; one at -10 V\(_{GS} \), 24 V\(_{DS} \) and the other at 0 V\(_{GS} \) and 24 V\(_{DS} \). The minimum off-state current is an artifact of the precision setting of the SMU.

Fig. 6. Normalized gate threshold voltage as a function of total ionizing dose from protons or heavy ions. All parts were irradiated at 0 V\(_{GS} \). For a given ion species, V\(_{DS} \) started at 24 V and was incremented by 3 V after each exposure, except for 2 DUTs which were held at 0 V\(_{DS} \) while exposed to Cu or Kr ions (+ and × symbols).

Fig. 7. Zero gate voltage drain leakage current for Si7414DN as a function of fluence for samples irradiated at 0 V\(_{GS} \) and 0 V\(_{DS} \). Pre-rad levels were 7 nA (Cu) and 3 nA (Kr).
A large variability in the threshold voltage for destructive SEE in commercial trench-gate MOSFETs was shown in [9]. Si7414DN also exhibited variability: as shown in SEE threshold voltage versus LET plot in Fig. 8, two DUTs irradiated with argon (LET = 8.2 MeV·cm²/mg) suffered SEB at 45 V_DS, having passed at 42 V_DS. The DUT shown in Fig. 3 failed at only 33 V_DS. An additional DUT failed immediately upon exposure to the 10 MeV/u argon beam (LET = 9.7 MeV·cm²/mg) while at an initial bias condition of 45 V_DS (indicated in Fig. 8 by the red “X” marker), suggesting that the threshold was not reduced by prior beam exposures. Also shown in Fig. 8, the threshold for SEB under neon irradiation (LET = 2.7 MeV·cm²/mg) was 45 V, based on one DUT, and similar thresholds were found with copper (LET = 21 MeV·cm²/mg) and krypton (LET = 31 MeV·cm²/mg).

### 2) Proton Tests

The heavy-ion results showing the threshold for SEB essentially unchanged across the LET and species range evaluated (Fig. 8) raise the possibility of SEB vulnerability from the proton environment. In a preliminary test, four DUTs were irradiated with 200 MeV protons as described in the Methods section above. At V_DS values below 42 V – 45 V, only charge collection was observed during exposure (Fig. 9). At 48 V for one DUT, and 45 V for the remaining three, current spikes were measured that increased in frequency (but not measured magnitude) with increased V_DS (Figs. 10-11). Note that the peak drain current during these spikes is not known due to the coarseness of the striptape measurements. Additional tests that include drain stiffening capacitance (Fig. 1) in compliance with MIL-STD-750, TM1080 [10], are needed to determine whether these spikes are quenched SEB events, given their similar onset with that of the heavy-ion induced SEB threshold (Fig. 8). Finally, as shown in Fig. 6, post-irradiation characterizations revealed the expected gate threshold voltage decrease from proton ionizing dose; this degradation was less that that from heavier ions.
per beam run. This device demonstrated localized total ionizing dose effects similar to those shown for the Si7414DN. The threshold VDS at 0 VGS for SEB is shown in Fig. 12. This device becomes less susceptible to SEB at lower LET and lighter ion species. Although this trend differs from that of the Vishay commercial Si7414DN, it is unlikely to be associated with the grade of the device. Part-to-part variability is roughly commensurate with that seen for the Si7414DN.

Fig. 12. SQS460EN threshold drain voltage for SEB at 0 VGS as a function of LET for individual DUTs. Square markers indicate highest passing voltage; error bars extend to the voltage at which SEB occurred. The × marker indicates where SEB occurred on the first beam run, thus no prior degradation had accumulated.

C. SQJ431EP

The Vishay SQJ431EP trench-gate MOSFET is a -200 V p-type automotive-grade transistor. The localized dosing of the gate oxide in p-type MOSFETs does not impact the overall function of the device because the positive gate threshold shift of the localized area is swamped by overall lower threshold voltage. The failure mode for the p-type unhardened power MOSFETs is typically SEGR. The SQJ431EP exhibited SEGR with 886 MeV krypton irradiation (LET = 31 MeV·cm²/mg) at 0 VGS and 170 VDS for 2 DUTs after passing at 160 VDS; however, the threshold for SEGR was not found due to the third DUT’s gate rupturing during its first irradiation at 150 VDS. Under 548 MeV argon and 659 MeV copper irradiation, all samples passed at the full -200 V rated drain voltage. Fig. 13 plots these results. Of the DUTs that failed under krypton irradiation, two failed during the post-irradiation gate stress (PIGS) test; the third exhibited two sudden increases in gate leakage current during the beam run (Fig. 14) in addition to failing the PIGS test.

Fig. 13. SQJ431EP drain voltage for SEGR at 0 Vos as a function of LET for individual DUTs. Square markers indicate highest passing voltage; error bars extend to the voltage at which SEGR occurred. The × marker indicates where SEGR occurred on the first beam run; the threshold for SEGR was thus not found.

Fig. 14. Striptape of gate (negative) and drain (positive) currents during irradiation of SQJ431EP with krypton. Two small breaks in the gate occurred during this run.

D. NVTFS5116PL

The On-Semiconductor -60 V p-type trench-gate power MOSFET passed at 0 VGS with 659 MeV copper irradiation but exhibited SEGR during the beam run with 886 MeV krypton irradiation. VDS was incremented in 2-volt steps between runs. Fig. 15 shows the individual DUT failure threshold VDS for SEGR as a function of LET, and Fig. 16 provides a striptape showing an example of the gate breakage during the beam run.
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Fig. 15. NVTFS5116PL drain voltage threshold for SEGR at 0 VGS as a function of LET for individual DUTs. Square markers indicate highest passing voltage; error bars extend to the voltage at which SEGR occurred.

Fig. 16. Striptape of gate (negative) and drain (positive) currents during irradiation of NVTFS5116PL with krypton, showing SEGR.

E. BSS84AKV

The Nexperia BSS84AKV -50 V p-type trench-gate power MOSFET is a dual power MOSFET whose package dimensions are 1.6 mm by 2 mm. Due to the small die size, the fluence for each beam run was increased to 1×10^7 cm^-2. The two die within the package are electrically isolated and thus can be individually tested for SEE by selectively biasing one and leaving the other die floating. Samples were irradiated at 0 VGS with VDS incremented in 2-volt steps. No effects occurred with 659 MeV copper irradiation. As shown in Fig. 17, under 886 MeV krypton, 2 samples passed at -50 VDS; however, 3 samples exhibited SEGR during the beam run at -46 VDS after passing at -44 VDS. There was no correlation between passing or failing samples and whether the die had been previously exposed to the beam while floating.

Fig. 17. BSS84AKV drain voltage threshold for SEGR at 0 VGS as a function of LET for individual DUTs. Square markers indicate highest passing voltage; error bars extend to the voltage at which SEGR occurred.

F. IRHLF87Y20

At this time, only one radiation-hardened trench-gate vertical power MOSFET is available. The Infineon (formerly International Rectifier) generation R8 IRHLF87Y20 is a 20 V n-type power MOSFET. Irradiation with 1039 MeV silver resulted in no measurable total ionizing dose effects. Fig. 18 shows the SEE threshold VDS as a function of VGS, as well as the manufacturer SEE response curve as reported in the data sheet. In this study, VDS was incremented in 2-volt steps, and each data point is the result of 3 test samples. Failures were due to either SEB or SEGR. Although there was no part-to-part variability in the voltage at which failures occurred, three different failure signatures were seen, demonstrating the greater complexity these trench-gate power MOSFETs have as compared to planar-gate power MOSFETs. The striptape in Fig. 19 shows SEGR occurring during the beam run at 0 VGS and 20 VDS. In Fig. 20, SEB occurs in another sample at these same applied voltage conditions. Finally, in Fig. 21, SEB is seen at -1 VGS and 20 VDS, but a partial recovery spontaneously occurs. Examination of the drain current in Fig. 20 shows a random telegraph noise-like behavior, suggestive of a charge trapping/detrapping phenomenon.

Fig. 18. SEE response curve for IRHLF87Y20 as measured in this study verifies that reported in the manufacturer data sheet.
Commercial, automotive, and radiation-hardened trench-gate vertical power MOSFETs were evaluated for SEE sensitivity. The SEE safe operating area for the commercial and automotive-grade devices is difficult to define due to the extent of the part-to-part variability. In some cases, a bimodal distribution may be present (see Fig. 8, LET = 8.2 MeV·cm²/mg, and Fig. 17, LET = 31 MeV·cm²/mg). A standard radiation hardness assurance procedure is to apply a derating factor (typically 0.75, per [11]) to the highest passing \( V_{DS} \) of the sample that failed at the lowest \( V_{DS} \). This approach is likely inadequate given the extent of the part-to-part variability. For example, a 0.75 derating factor applied to the data in Fig. 15 suggests NVTFS5116PL can be operated safely up to a \( V_{DS} \) of 37.5 V; however, application of a 99/90 one-sided tolerance limit (KTL) to these data results in 30 V, indicating a larger sample size is needed to determine the distribution of failures.

Hardness assurance is further complicated by the localized dosing effects in the unhardened n-type trench-gate power MOSFETs. As shown in Fig. 6, the use of a hard-off \( V_{GS} \) bias to counter-act gate threshold voltage shift can increase the amount of shift for a given dose (and potentially create an SEGR susceptibility). As demonstrated in Figs. 6-7, flight spares having all nodes grounded degrade with heavy-ion fluence.

In general, unhardened p-type power MOSFETs tend to be more robust to heavy-ion effects than unhardened n-type power MOSFETs due to the lower mobility of the holes, making SEB unlikely to occur. Instead, SEGR is the typical failure mode. For the devices evaluated in this study, the limited sample sizes suggest that the p-type devices were not susceptible to SEEs from 659 MeV copper ions (LET = 21 MeV·cm²/mg), whereas the unhardened n-type devices were susceptible to SEEs with 400 MeV – 548 MeV argon (LET = 8.2 MeV·cm²/mg – 9.7 MeV·cm²/mg), with one part susceptible to heavy ions as light as neon (LET = 2.7 MeV·cm²/mg). The susceptibility of the n-type devices to SEB at these low LETs suggests vulnerability to SEB from secondary reaction products when exposed to high-energy protons. In addition, the p-type power MOSFETs experienced SEEs at a higher percentage of their rated \( V_{DS} \) as compared to the n-type devices. Finally, the p-type devices do not exhibit the localized dosing effects that impact the unhardened n-type devices when an incident heavy ion ionizes charge along the length of the gate oxide.

At this time, only one radiation-hardened n-type trench-gate power MOSFET is available. This study verified the manufacturer SEE response curve for the IRHLF87Y20. In addition, no localized dosing effects were measured. The device exhibited three different failure signatures during irradiation, demonstrating greater complexity of failure mechanisms than those of planar-gate vertical power MOSFETs.

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VI. REFERENCES


